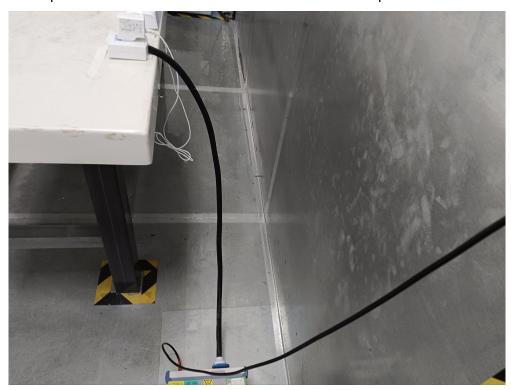


Test Setup Photo

Description: Front View of Conducted Emission Test Setup for Power Port

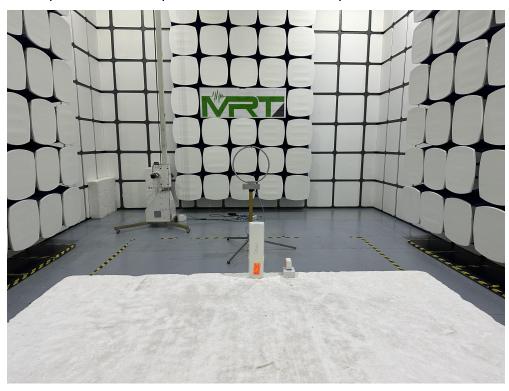


Description: Back View of Conducted Emission Test Setup for Power Port





Description: Radiated Spurious Emission Test Setup for $9kHz \sim 30MHz$



Description: Radiated Spurious Emission Test Setup for 30M \sim 1GHz





Description: Radiated Spurious Emission Test Setup for 1GHz \sim 18GHz

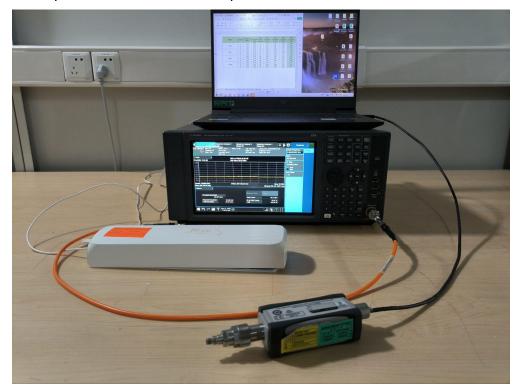


Description: Radiated Spurious Emission Test Setup for $18\text{GHz} \sim 40\text{GHz}$





Description: Conducted Test Setup



Description: DFS Test Setup





Description: CBP Test Setup

