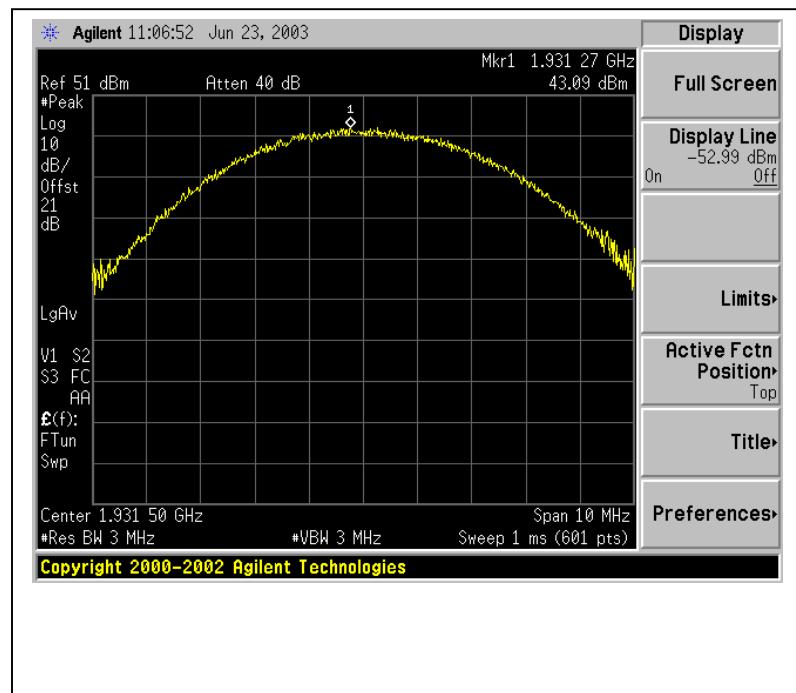
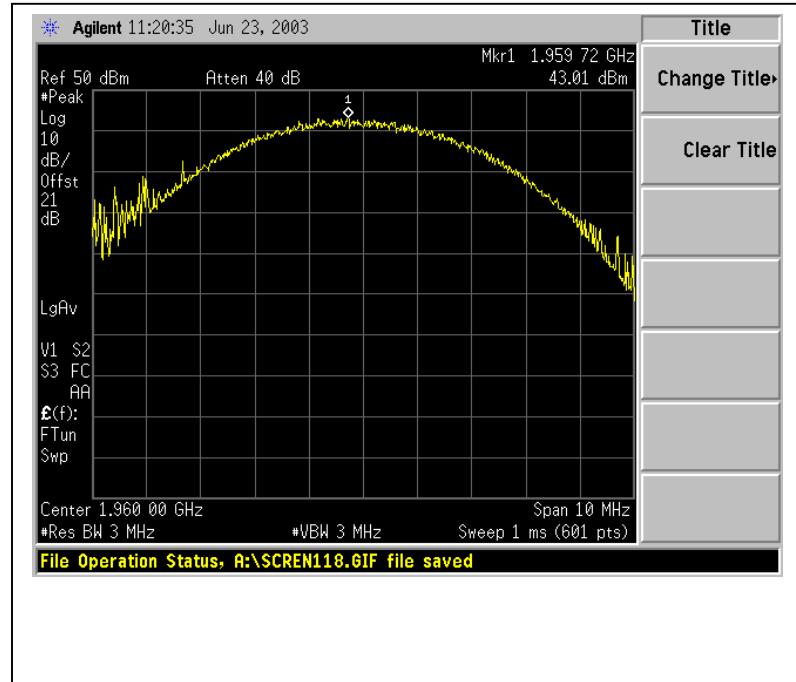


## 1900MHz PCS (CDMA)- DOWNLINK

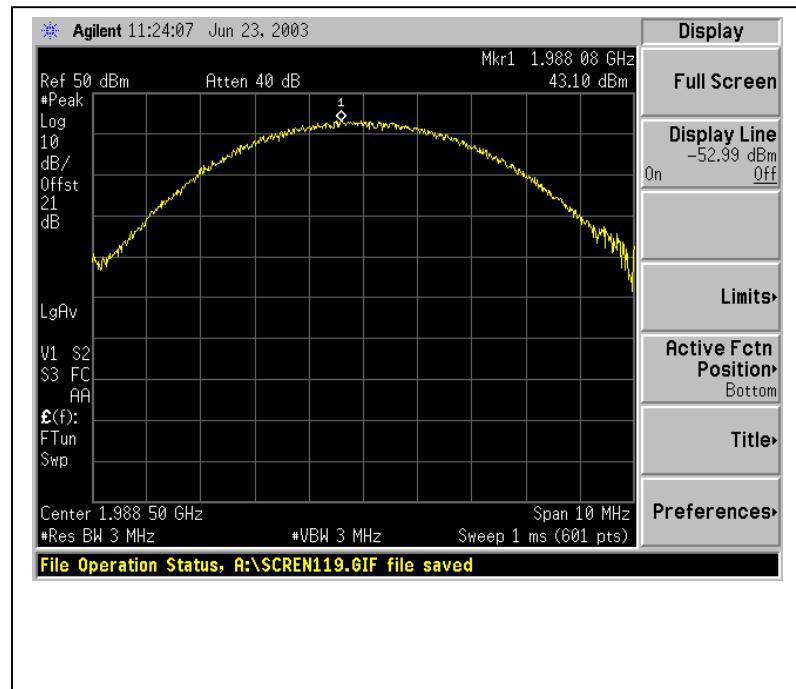
Plot # 1



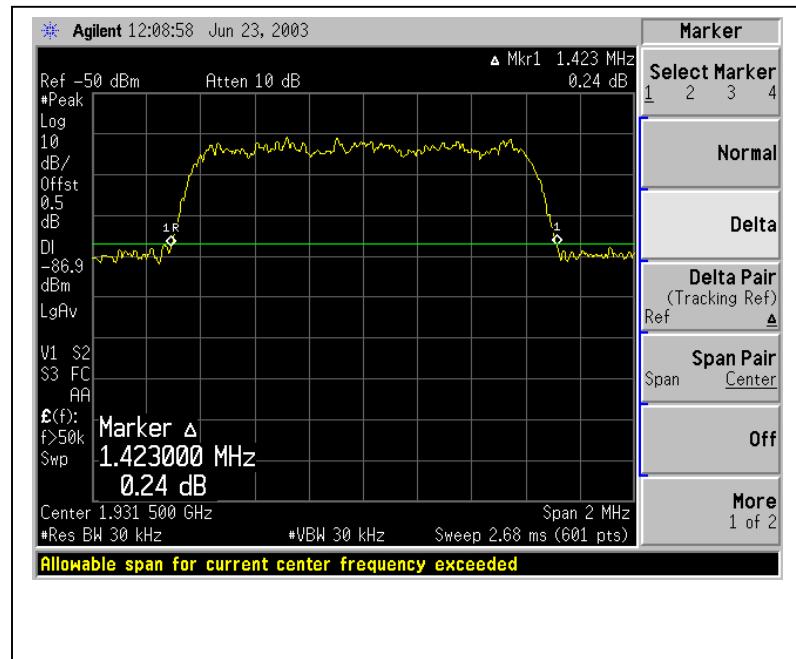
Plot # 2

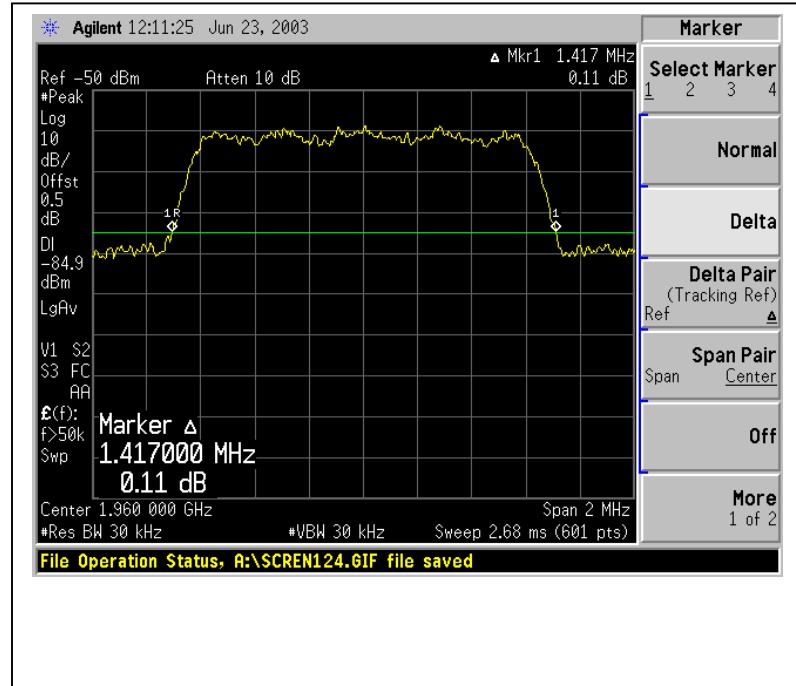


Plot # 3

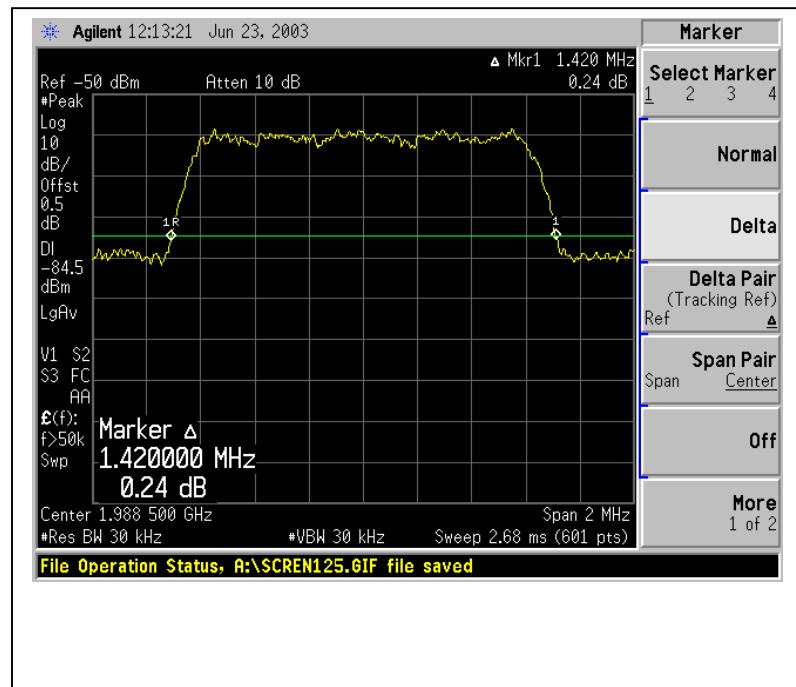


Plot # 4

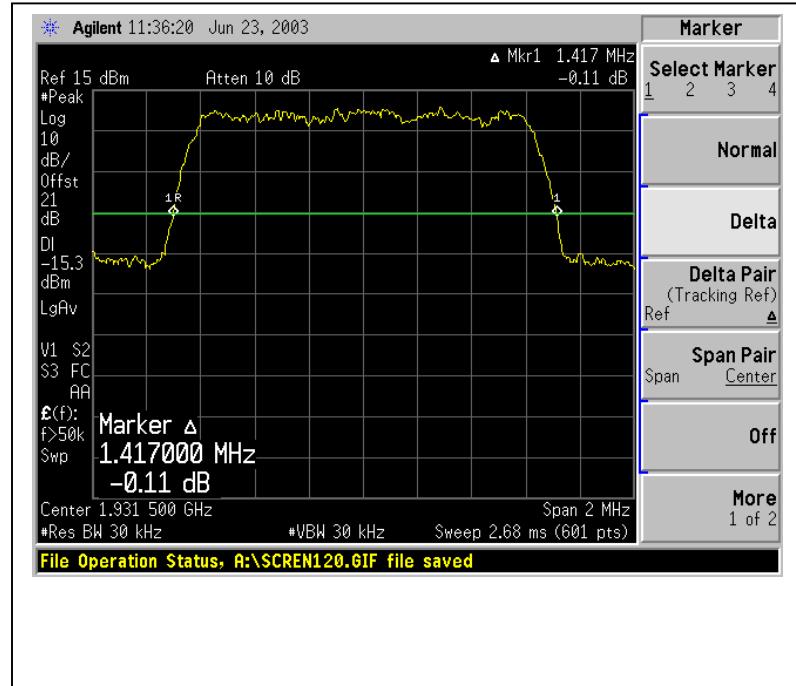




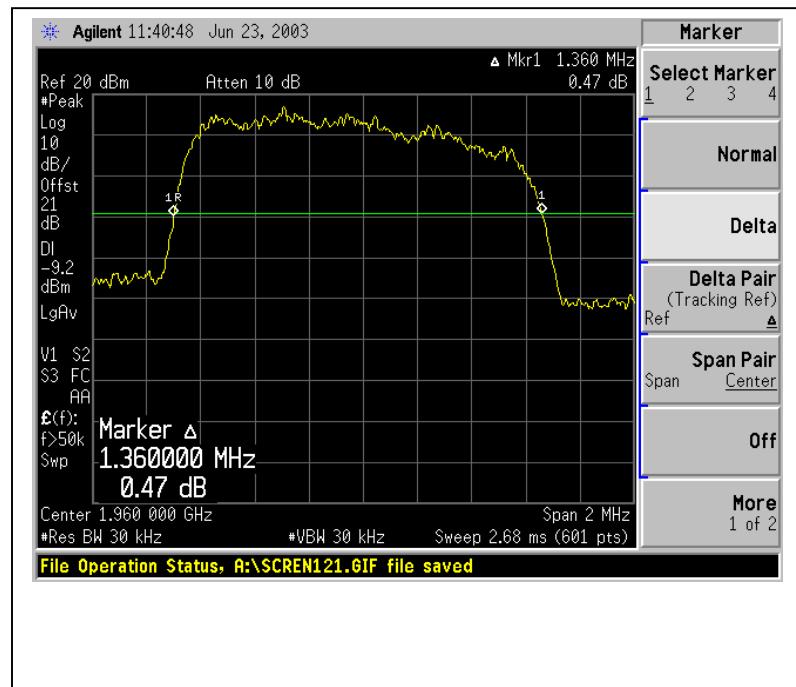
Plot # 5



Plot # 6

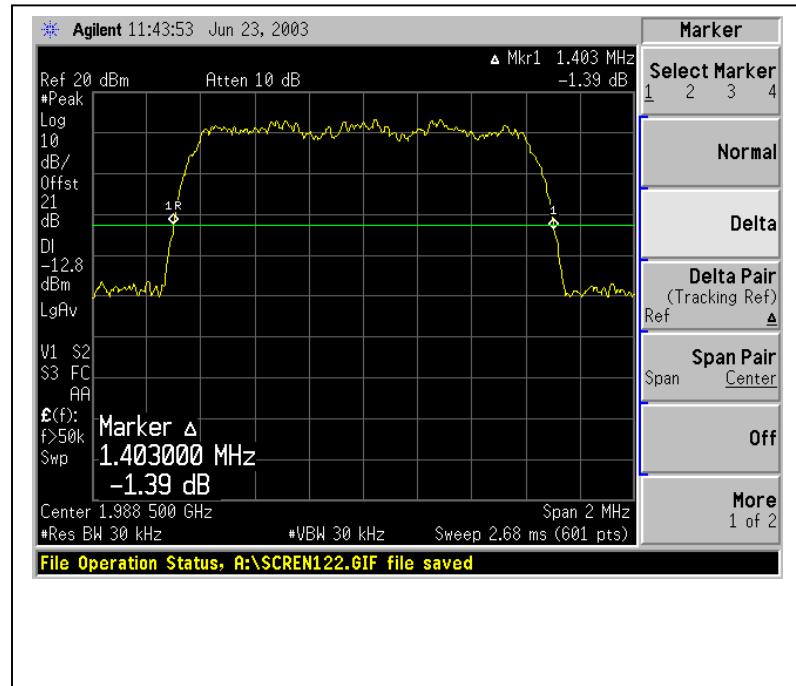


Plot # 7

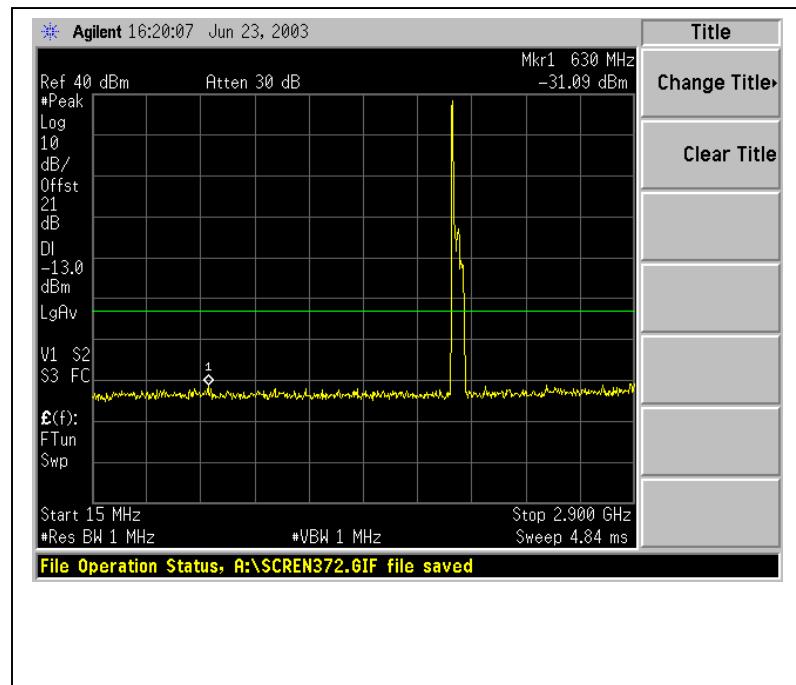


Plot # 8

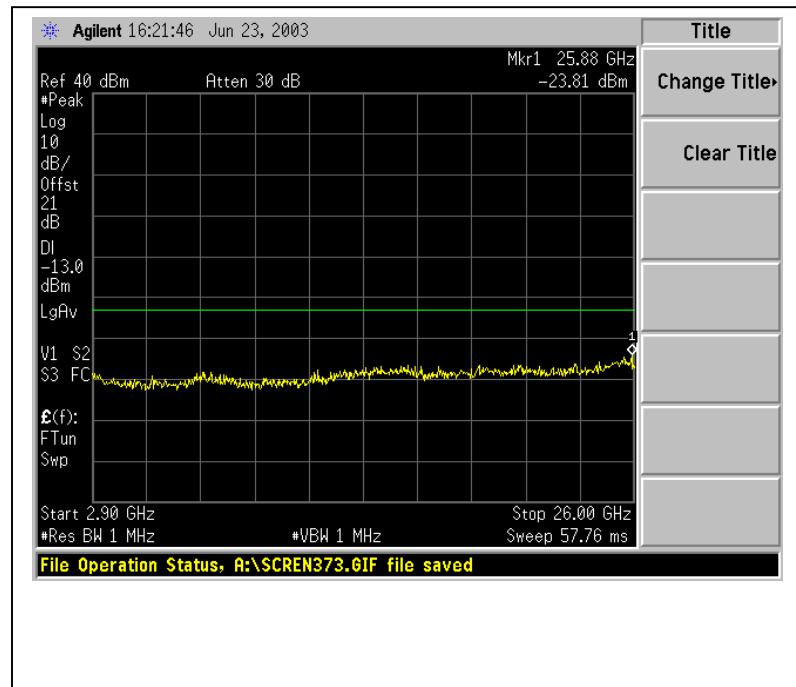
Plot # 9



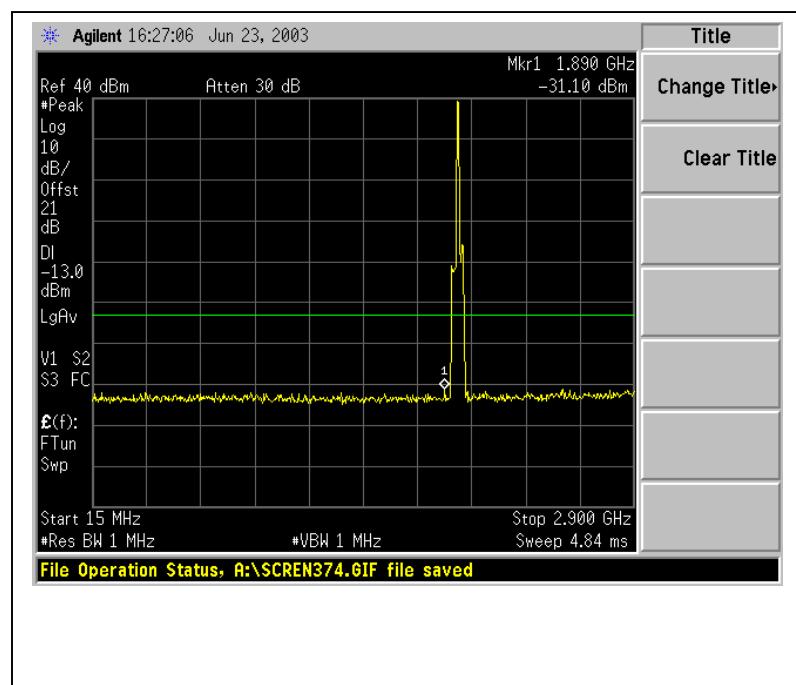
Plot # 10



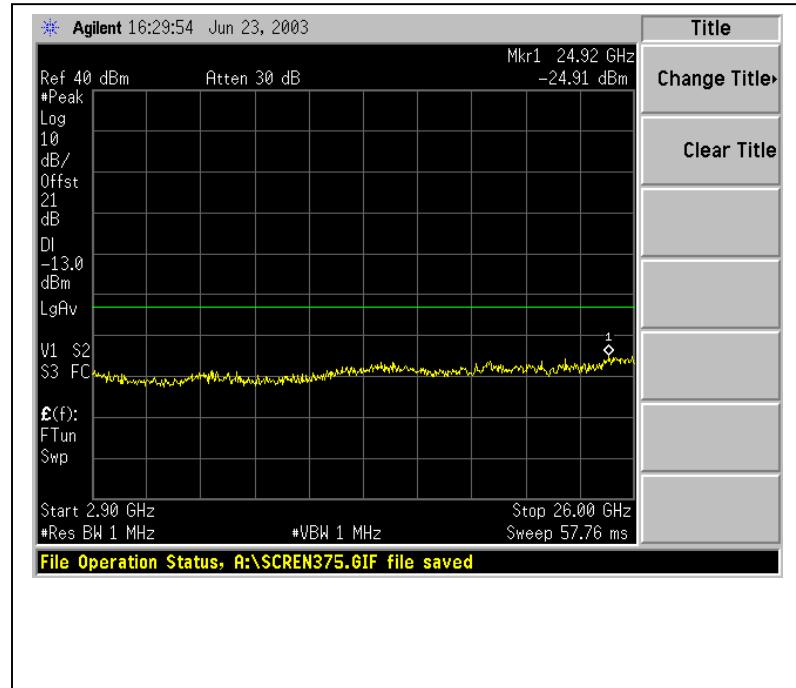
Plot # 11



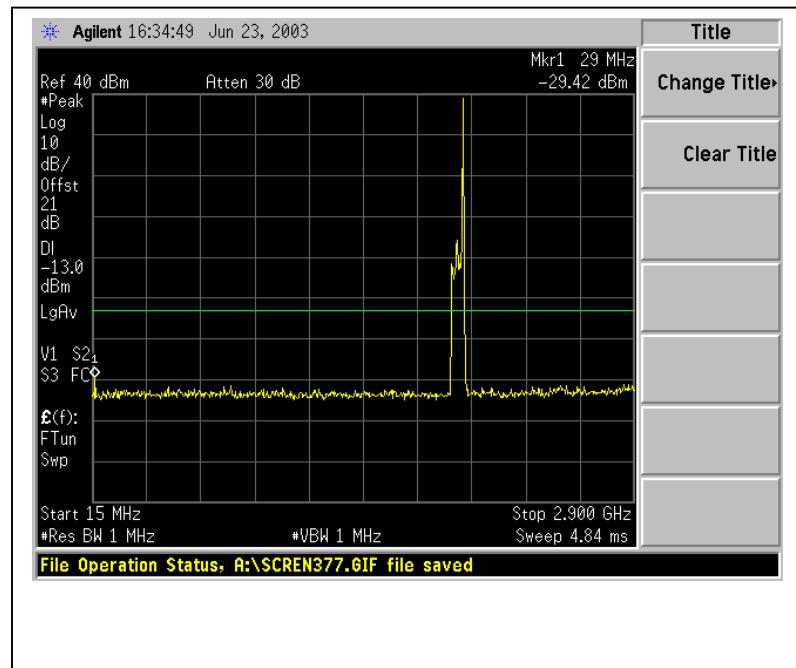
Plot # 12



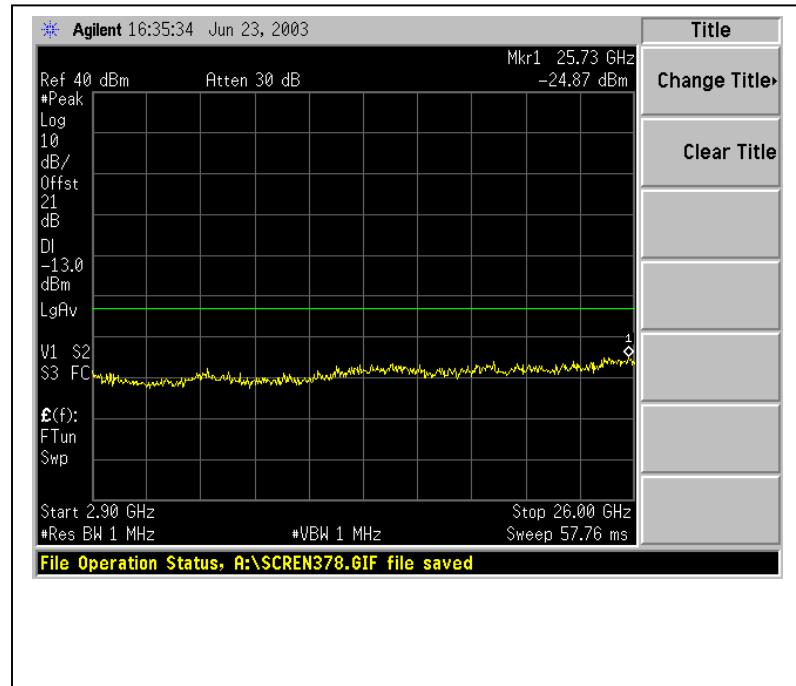
Plot # 13



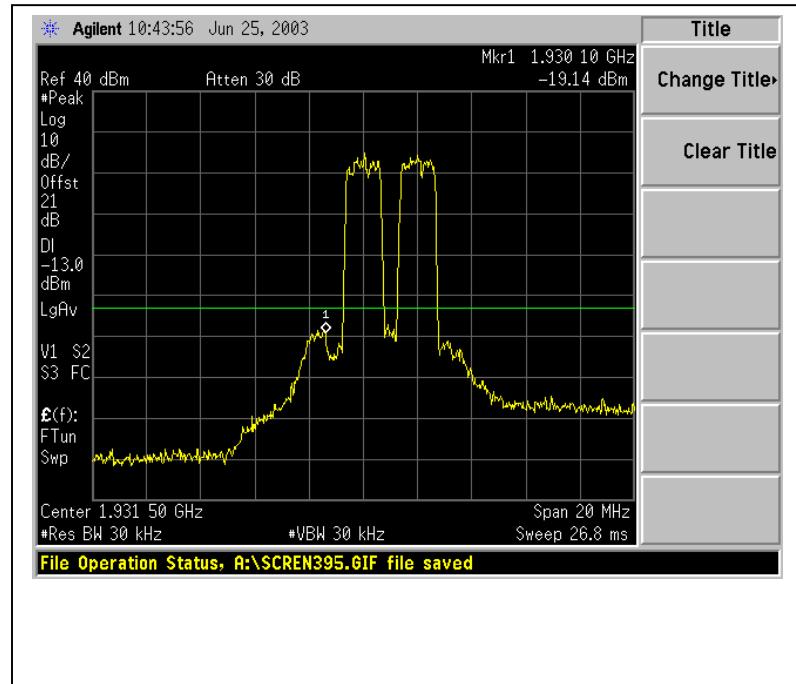
Plot # 14



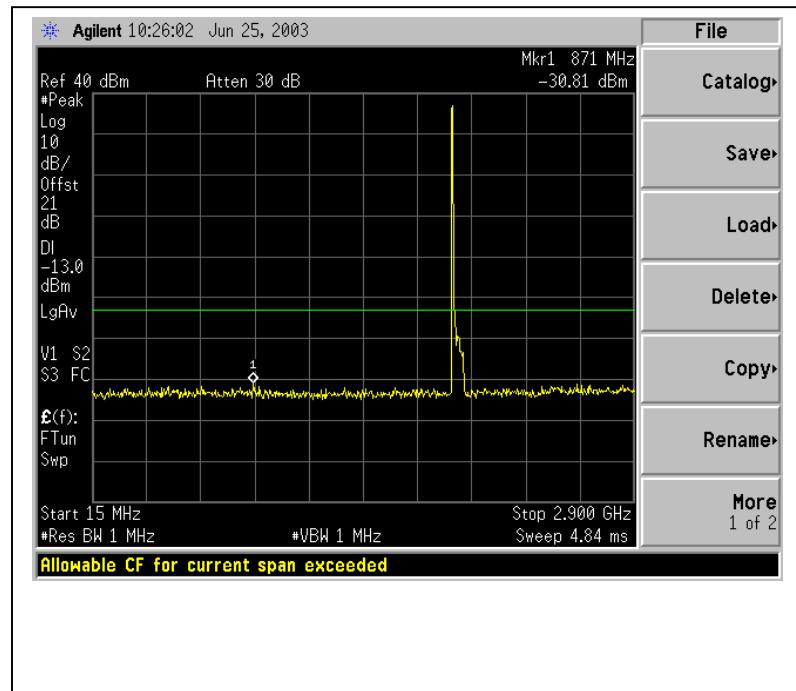
Plot # 15



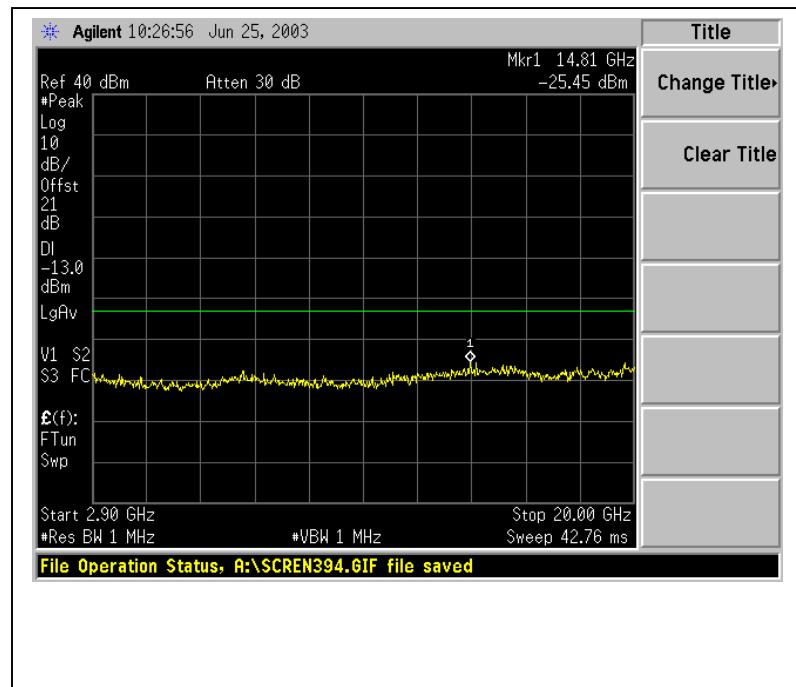
Plot # 16 (A)

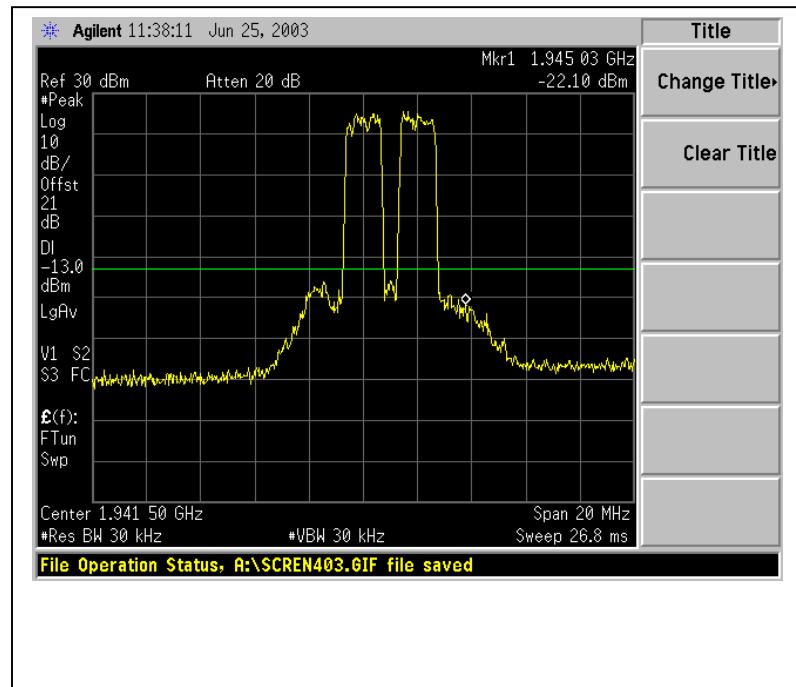


Plot # 17

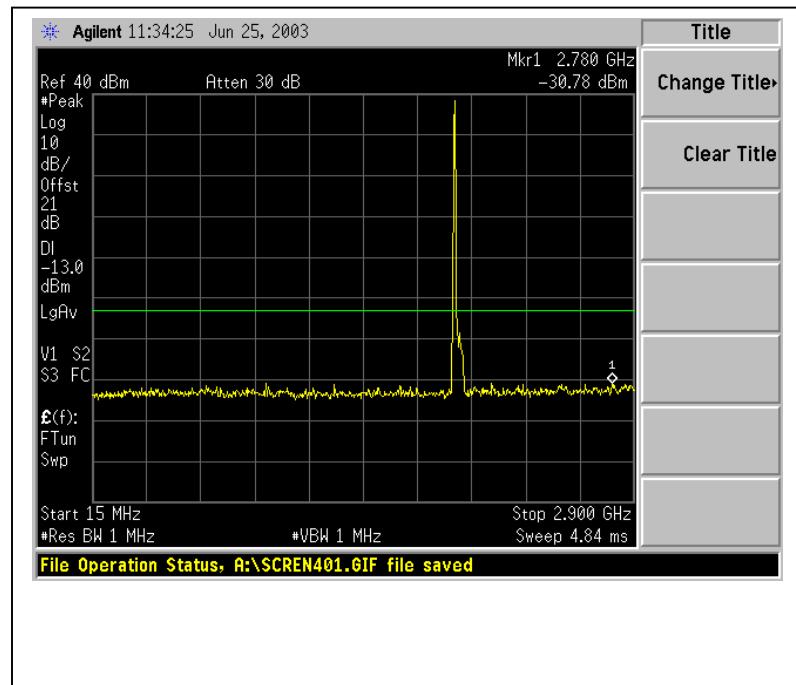


Plot # 18



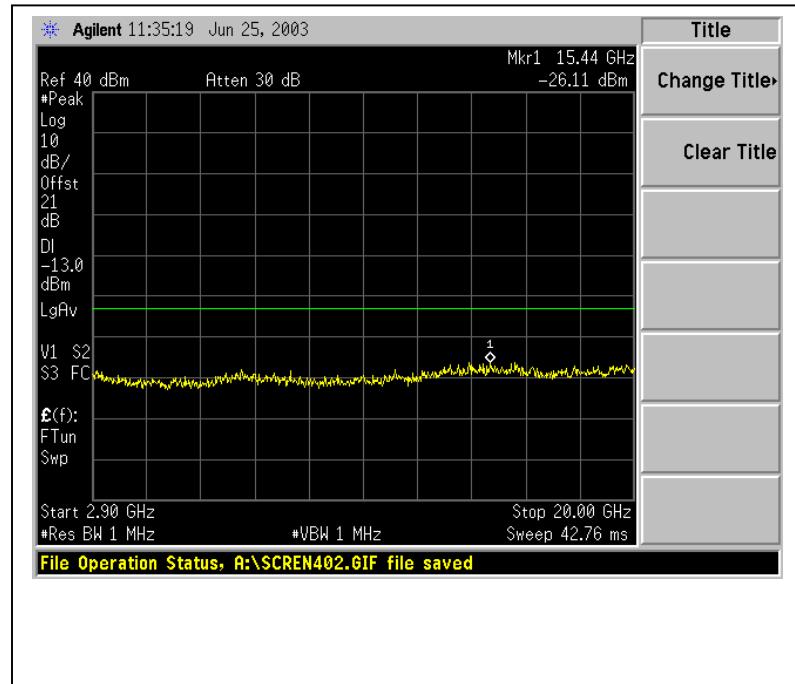


Plot # 19 (A)

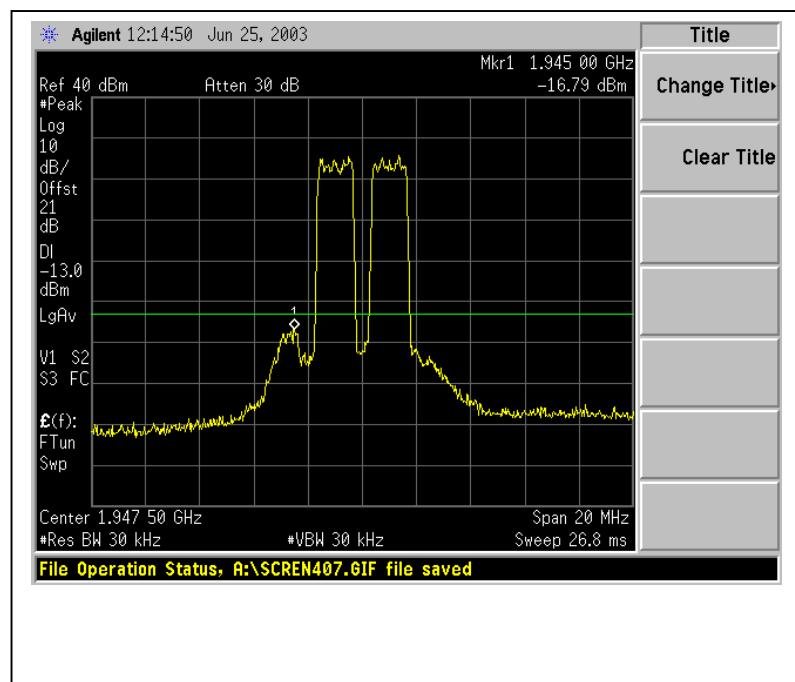


Plot # 20

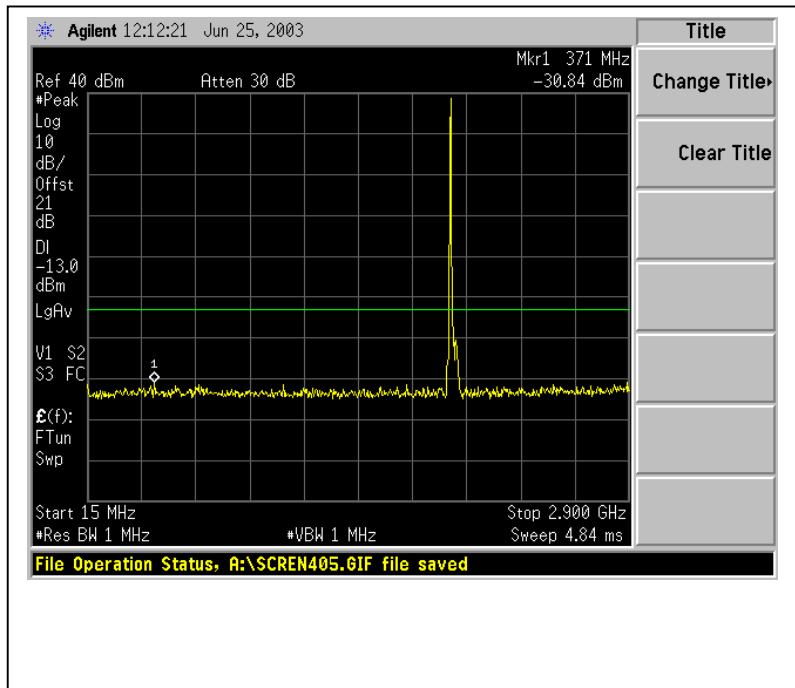
Plot # 21



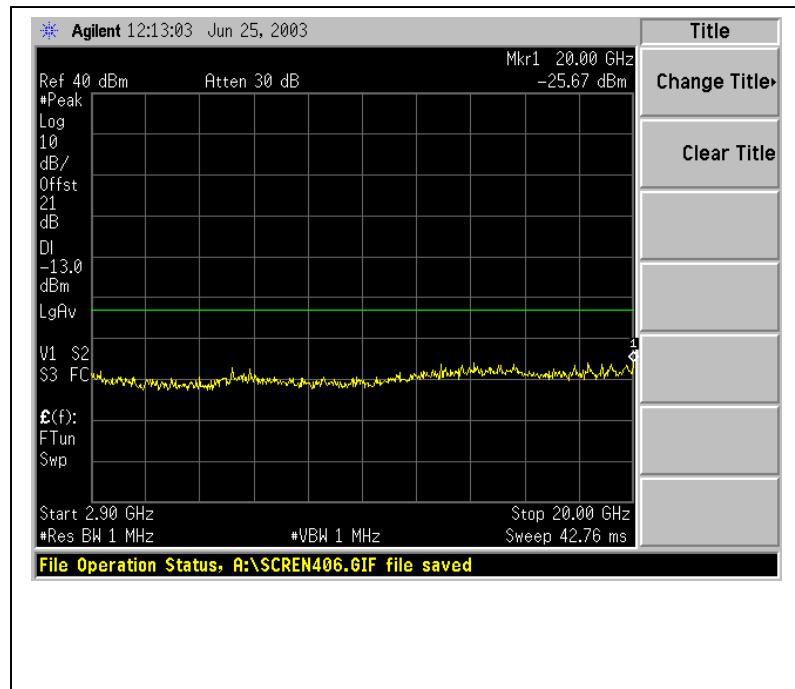
Plot # 22 (D)

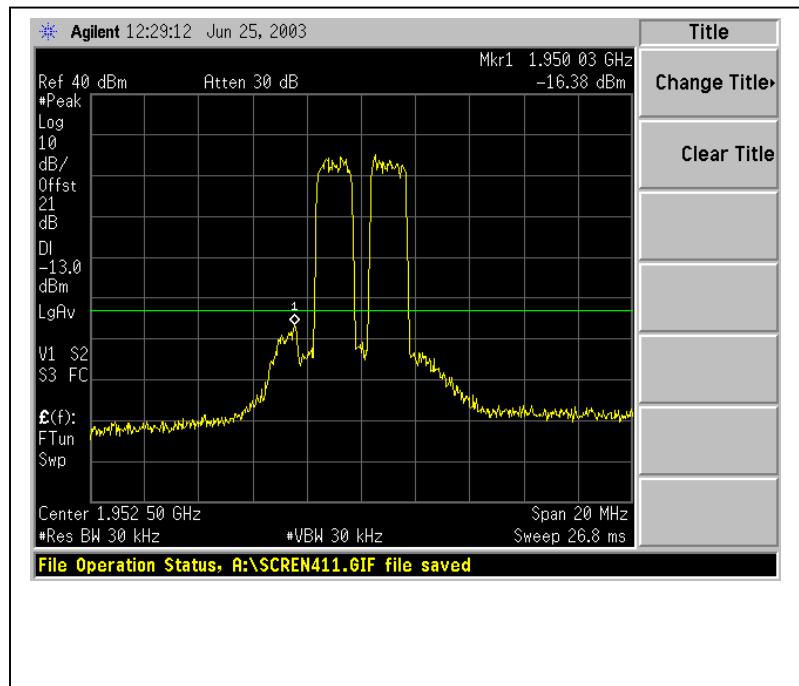


Plot # 23

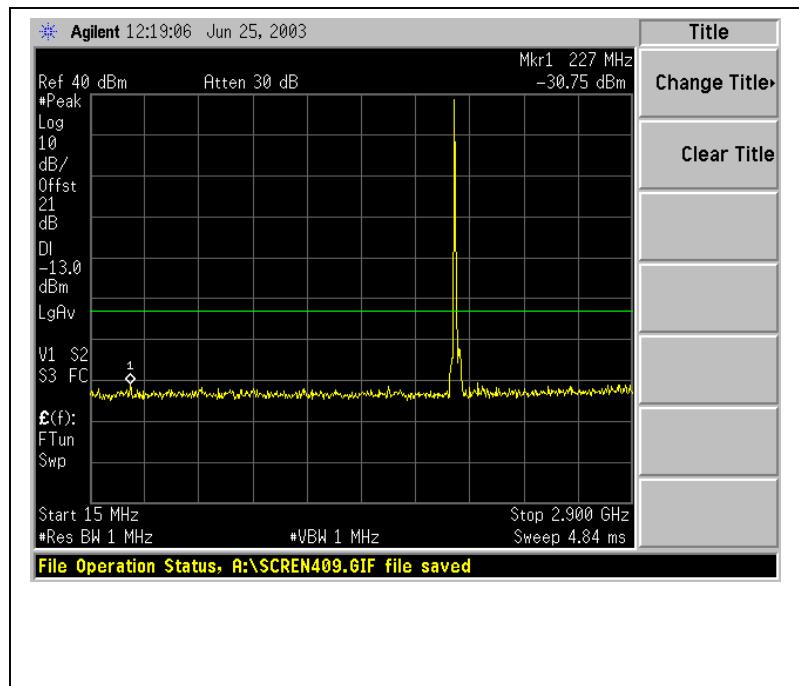


Plot # 24



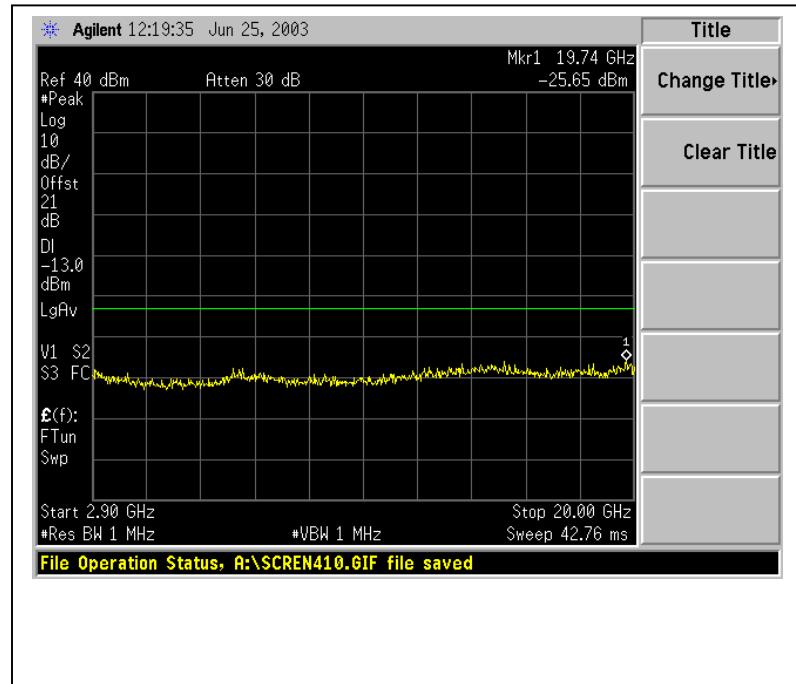


Plot # 25 (B)

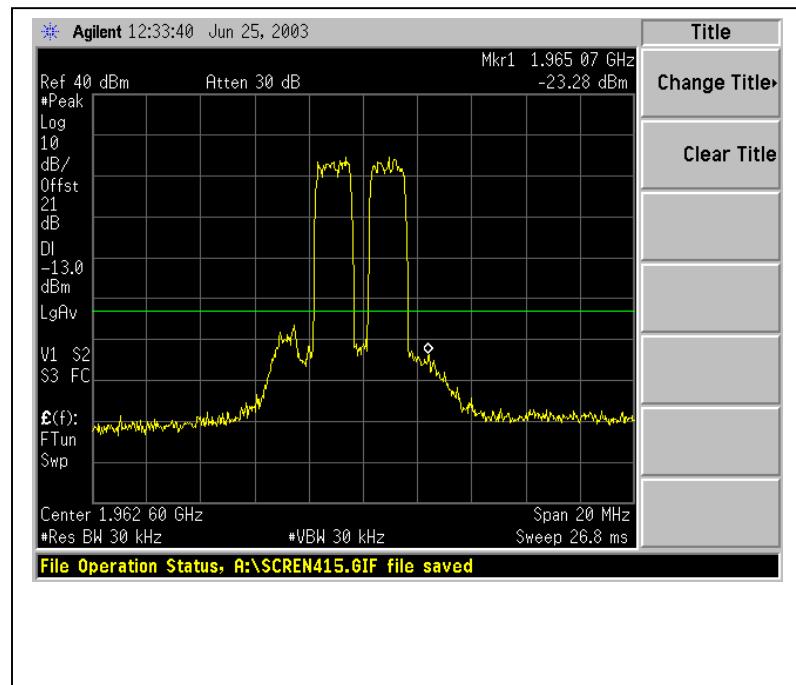


Plot # 26

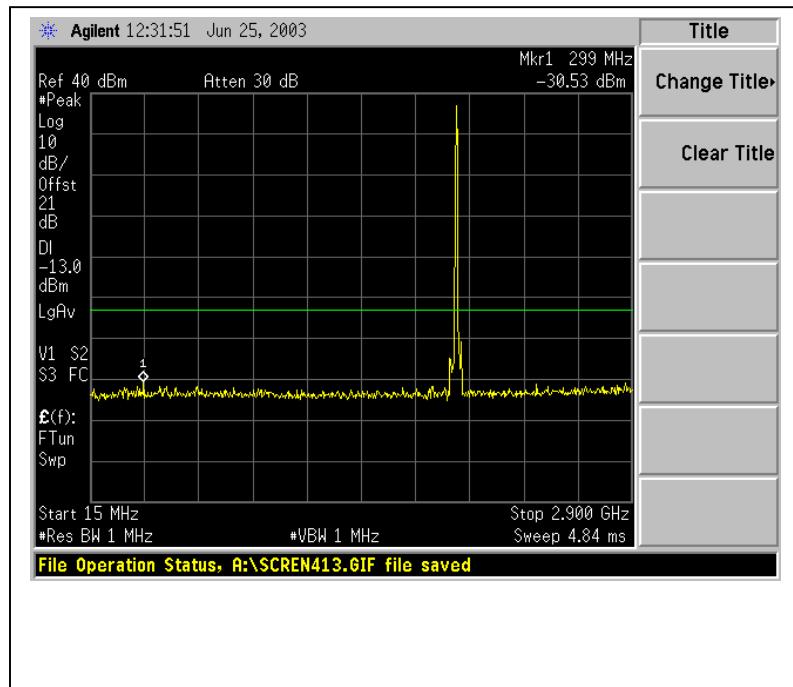
Plot # 27



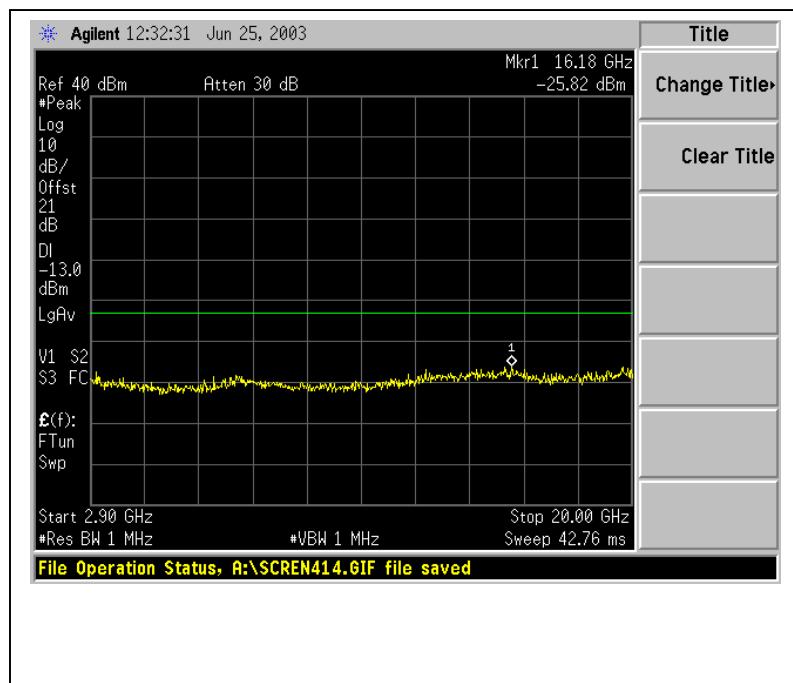
Plot # 28 (B)



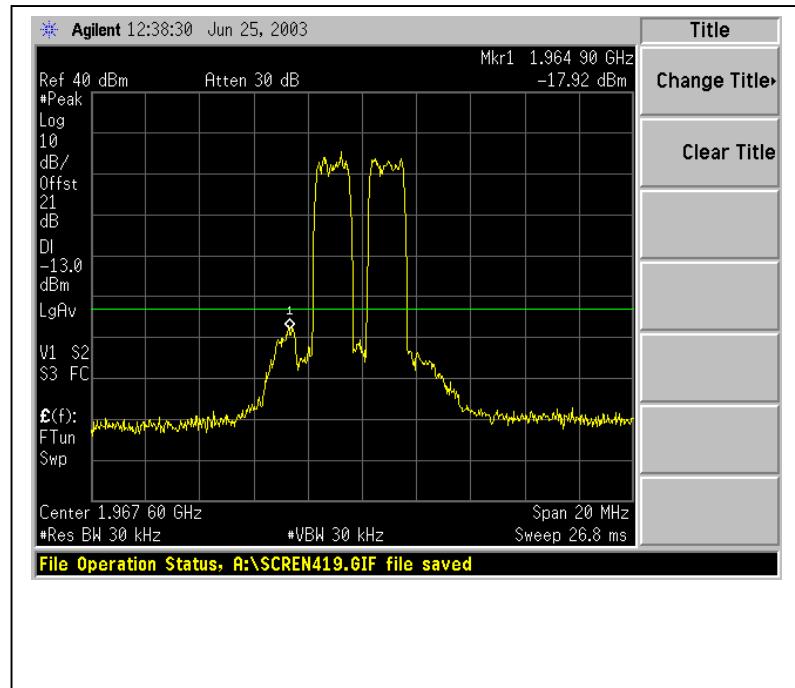
Plot # 29



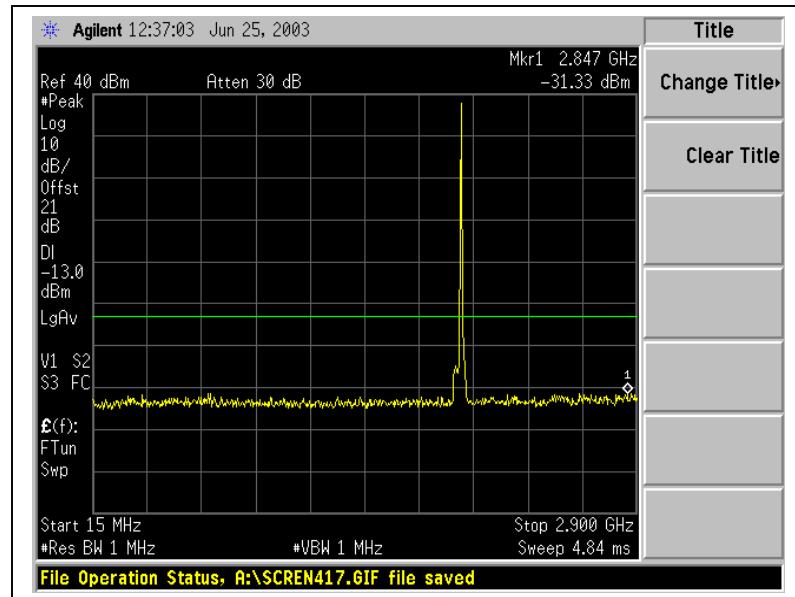
Plot # 30



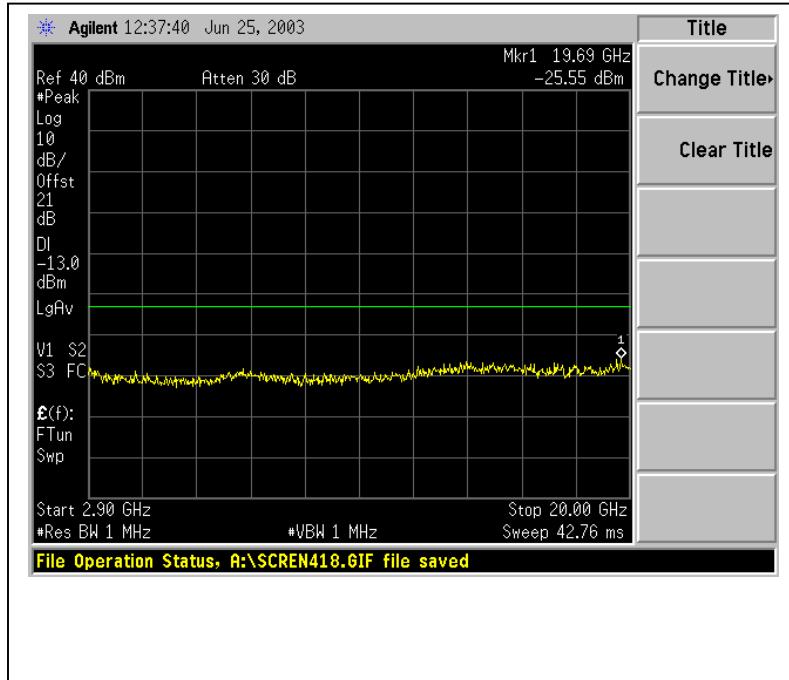
Plot # 31 (E)



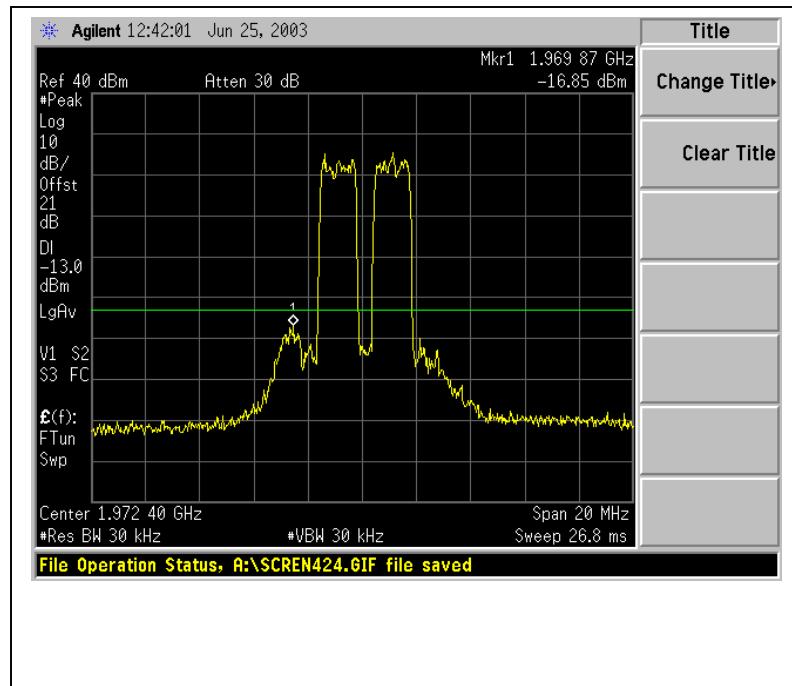
Plot # 32



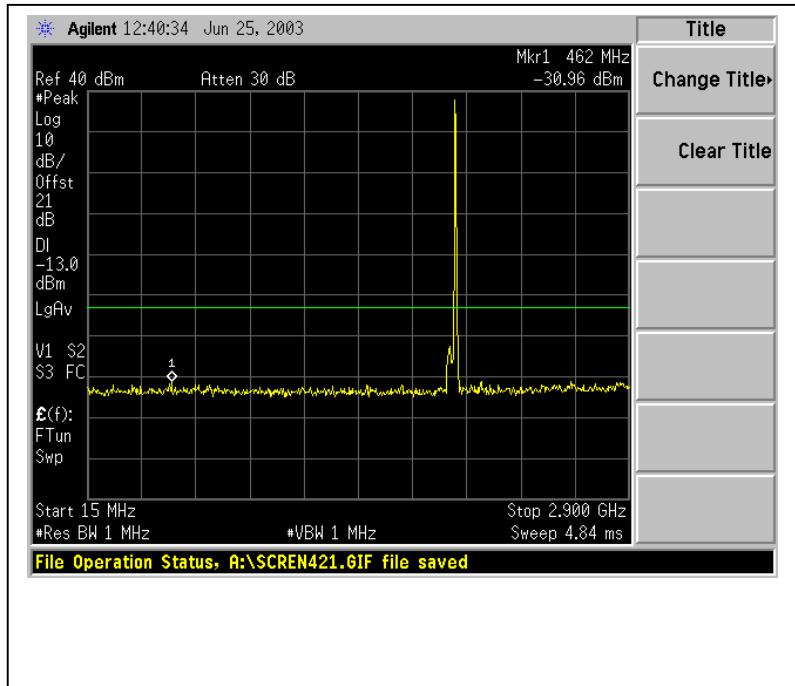
Plot # 33



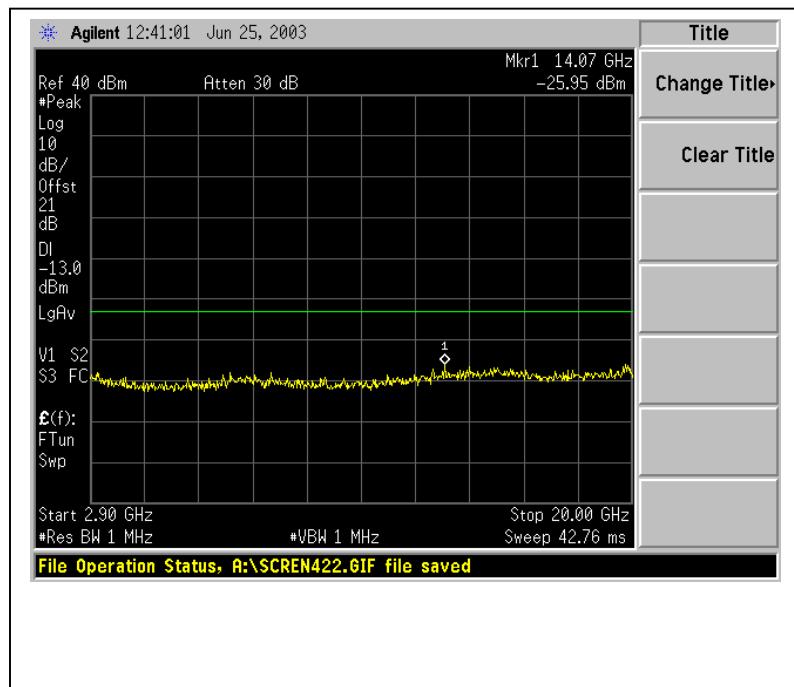
Plot # 34 (F)

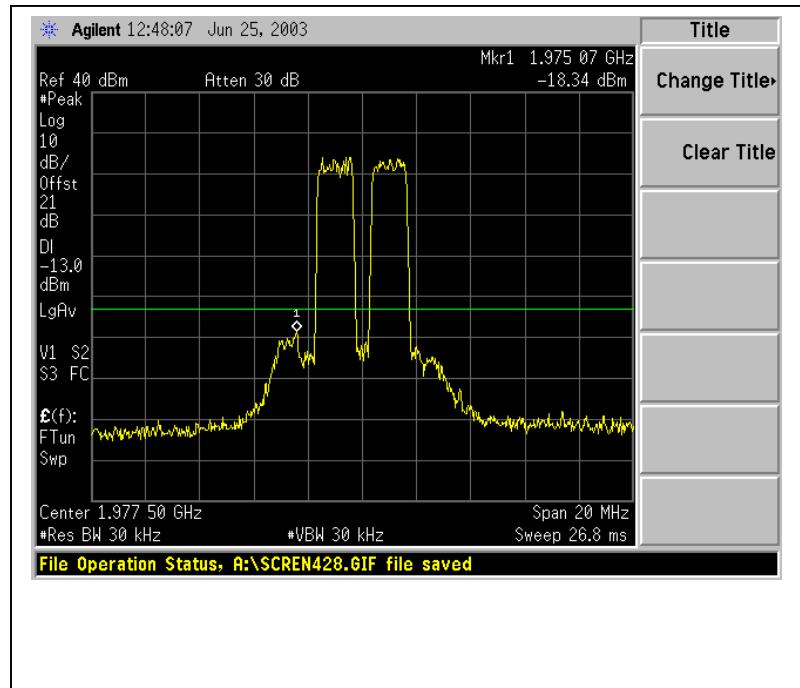


Plot # 35

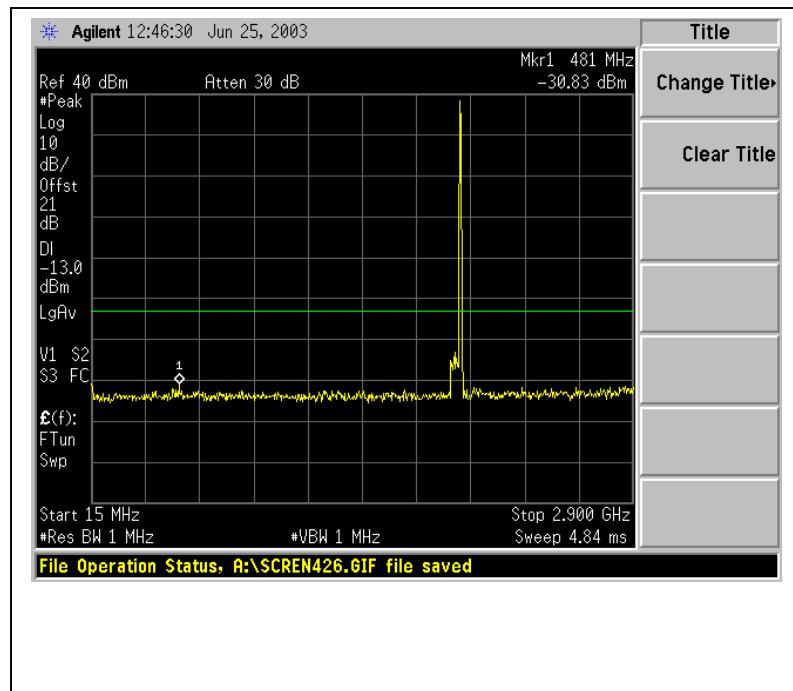


Plot # 36



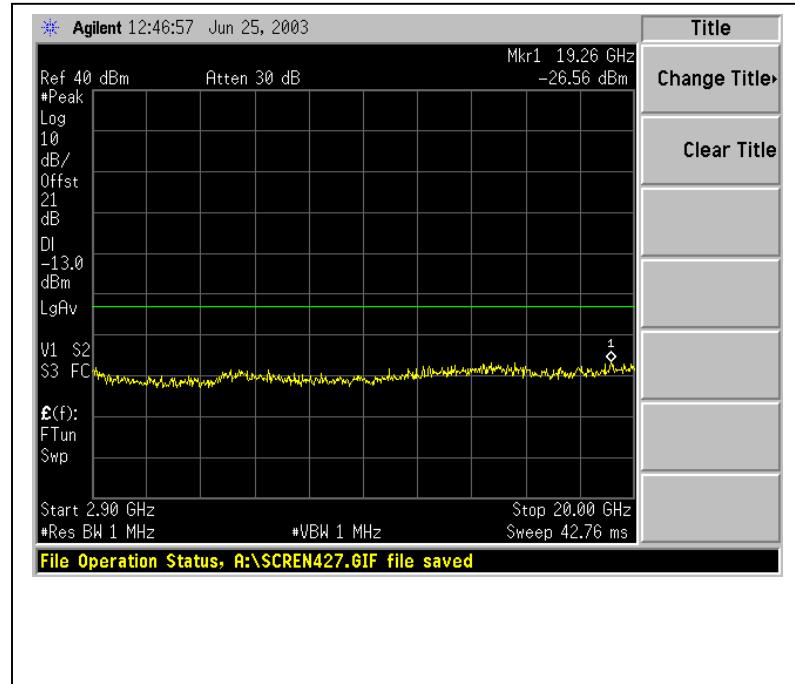


Plot # 37 (C)

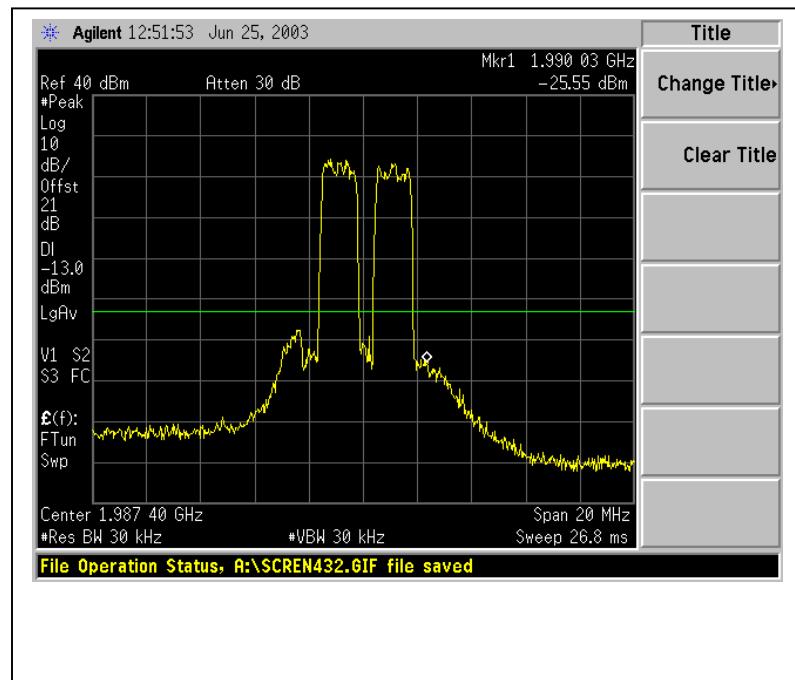


Plot # 38

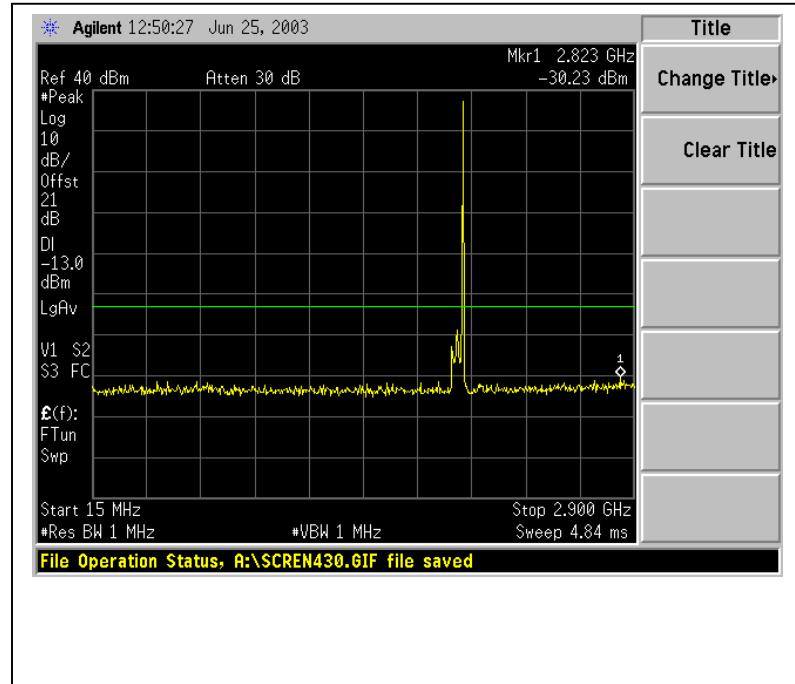
Plot # 39



Plot # 40 (C)



Plot # 41



Plot # 42

