

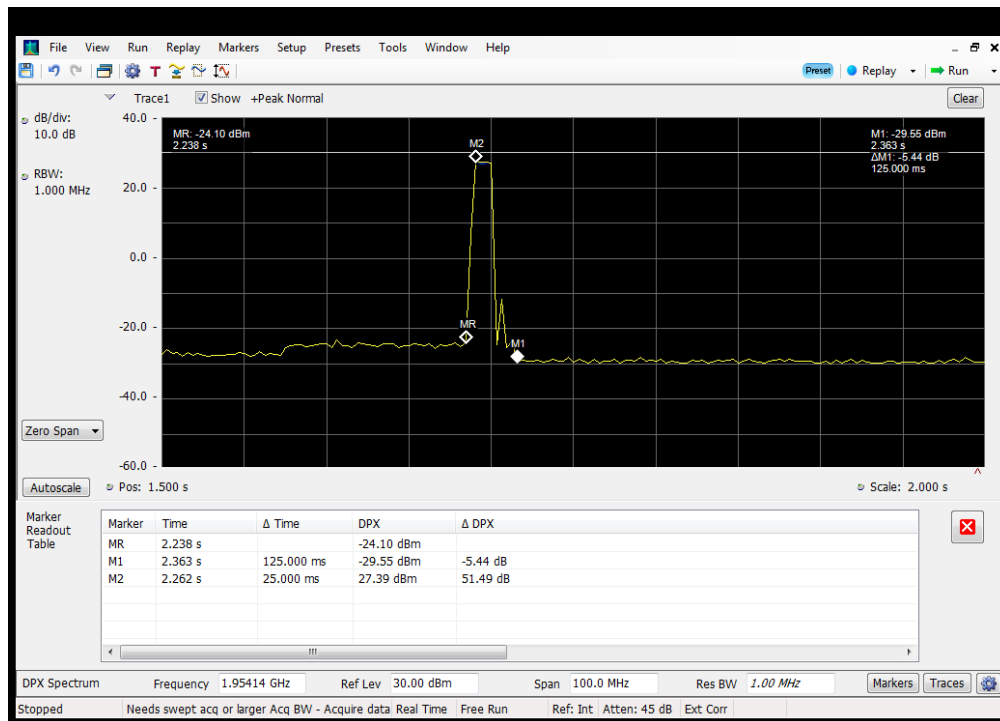


Annex I

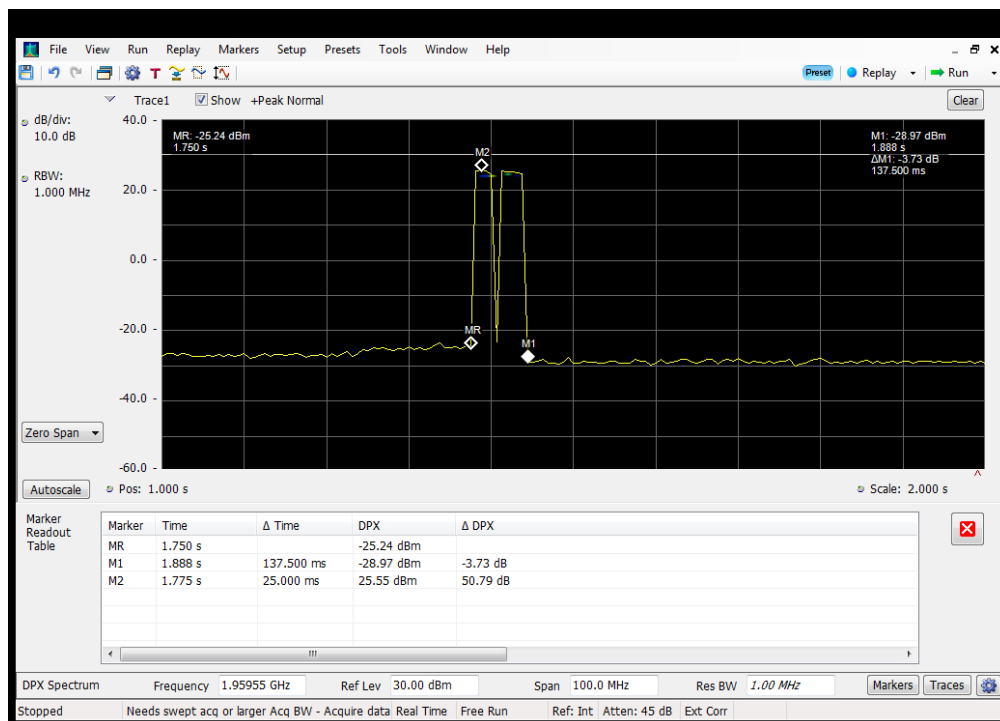
Anti - Oscillation



Detection Time_Downlink_1930 - 1995 MHz_B25_Server 2 to Dedicated Donor Port 1

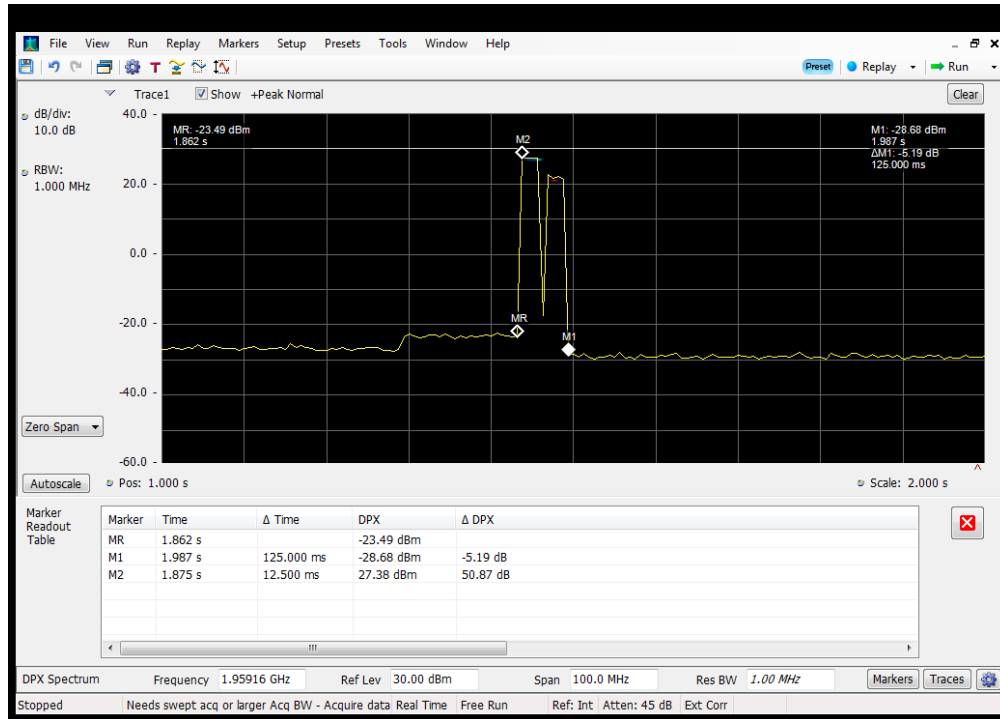


Detection Time_Downlink_1930 - 1995 MHz_B25_Server 4 to Dedicated Donor Port 1

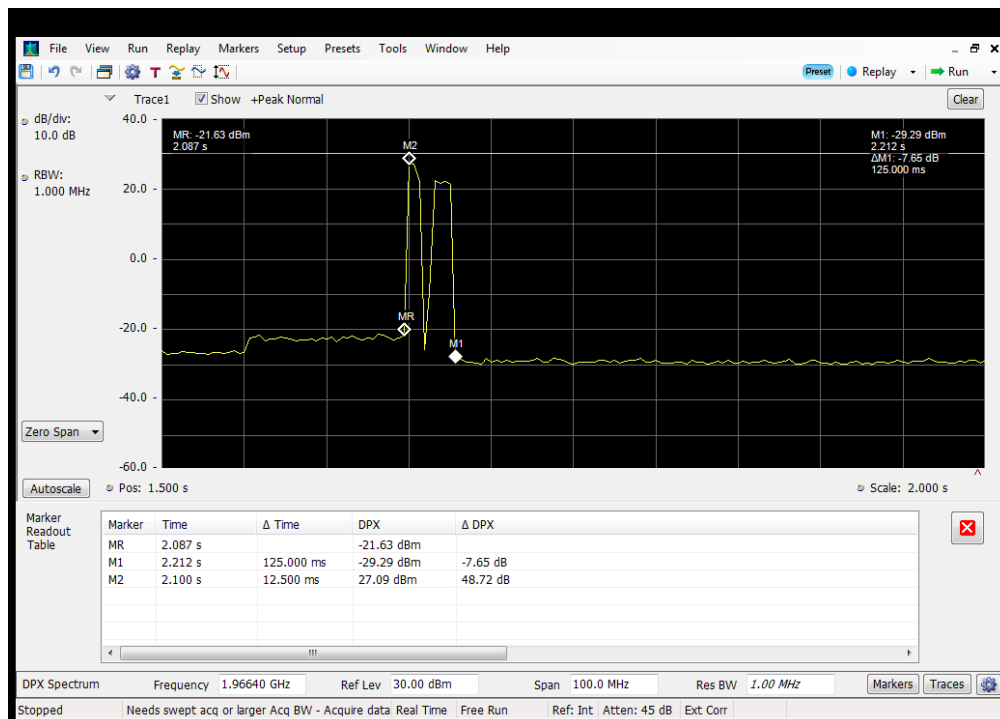




Detection Time_Downlink_1930 - 1995 MHz_B2_Server 1 to Dedicated Donor Port 1

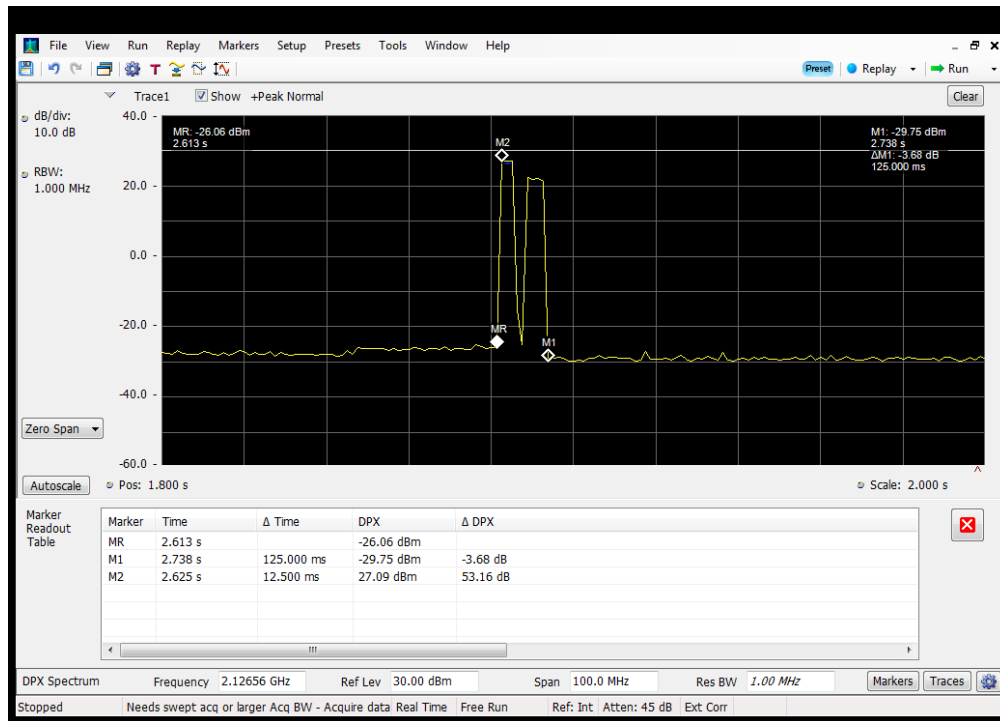


Detection Time_Downlink_1930 - 1995 MHz_B2_Server 3 to Dedicated Donor Port 1

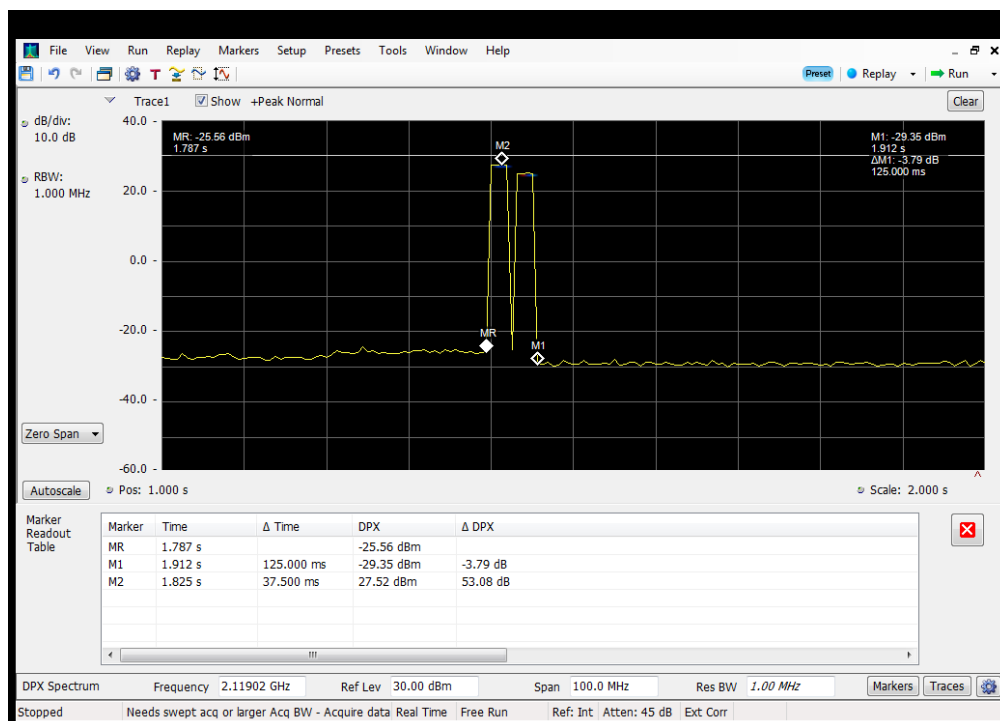




Detection Time_Downlink_2110 - 2155 MHz_B4_Server 1 to Dedicated Donor Port 1

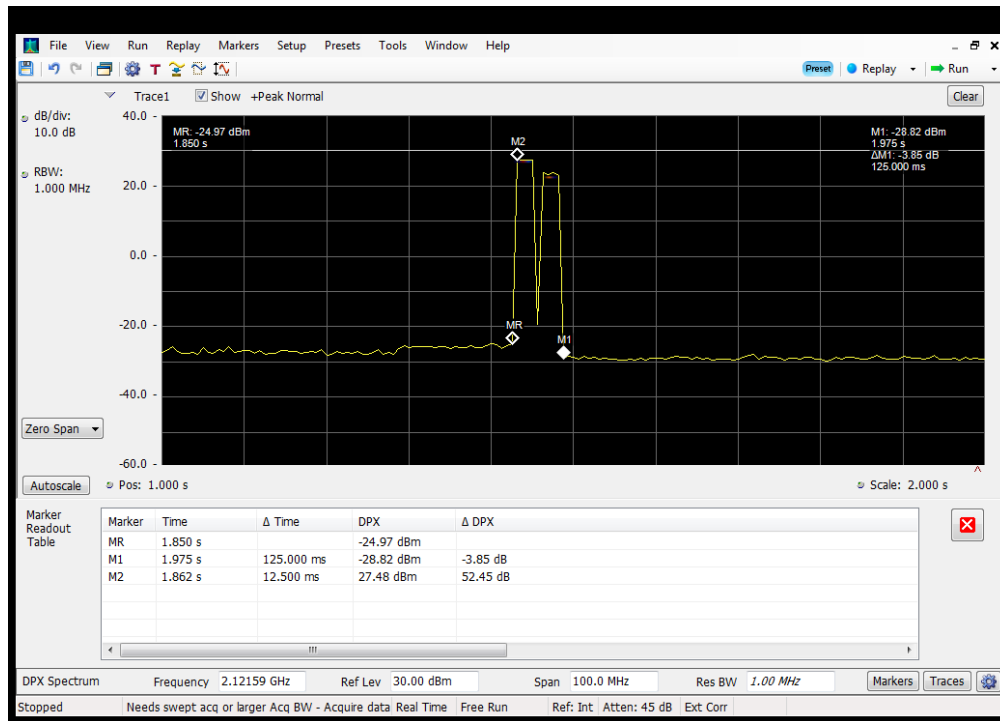


Detection Time_Downlink_2110 - 2155 MHz_B4_Server 2 to Dedicated Donor Port 1

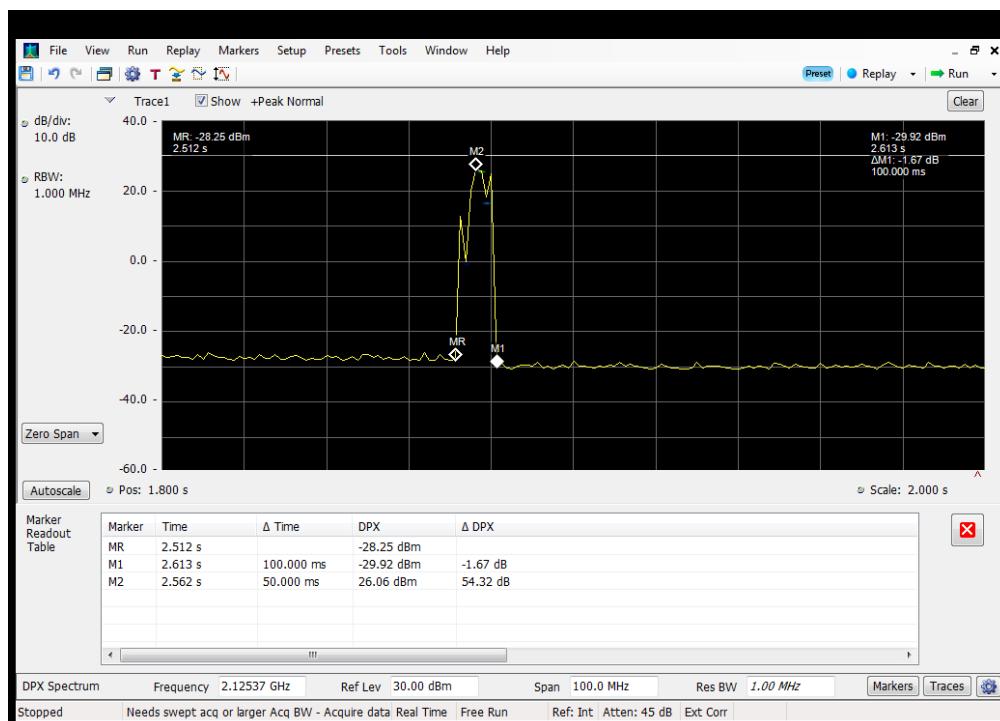




Detection Time_Downlink_2110 - 2155 MHz_B4_Server 3 to Dedicated Donor Port 1

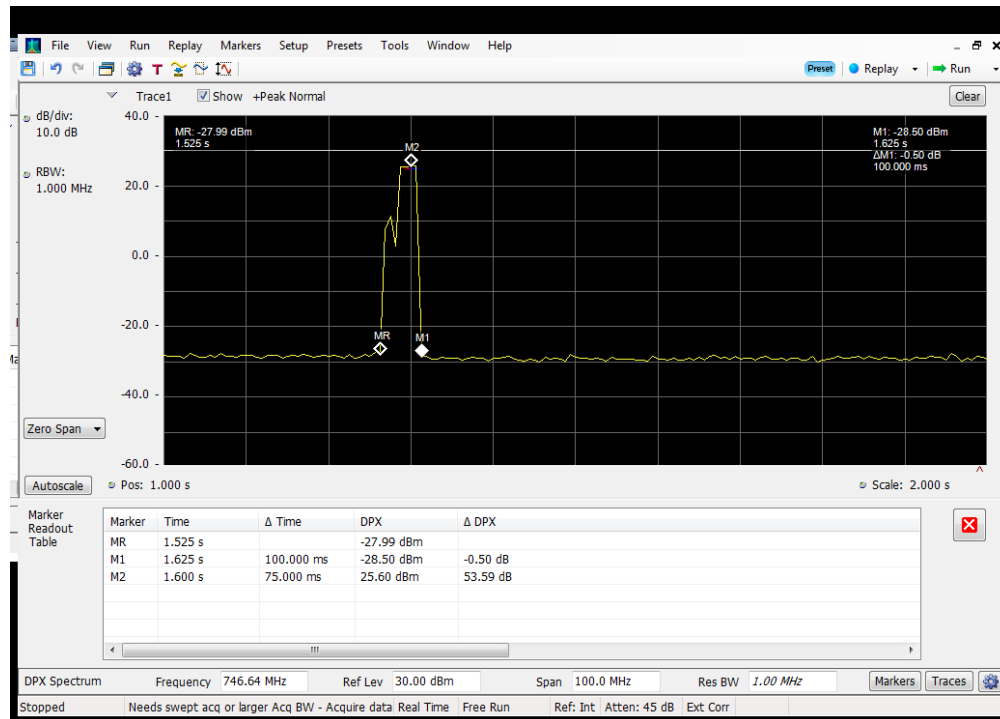


Detection Time_Downlink_2110 - 2155 MHz_B4_Server 4 to Dedicated Donor Port 1





Detection Time_Downlink_728 - 746 MHz_B12_Server 1 to Common Donor Port 1

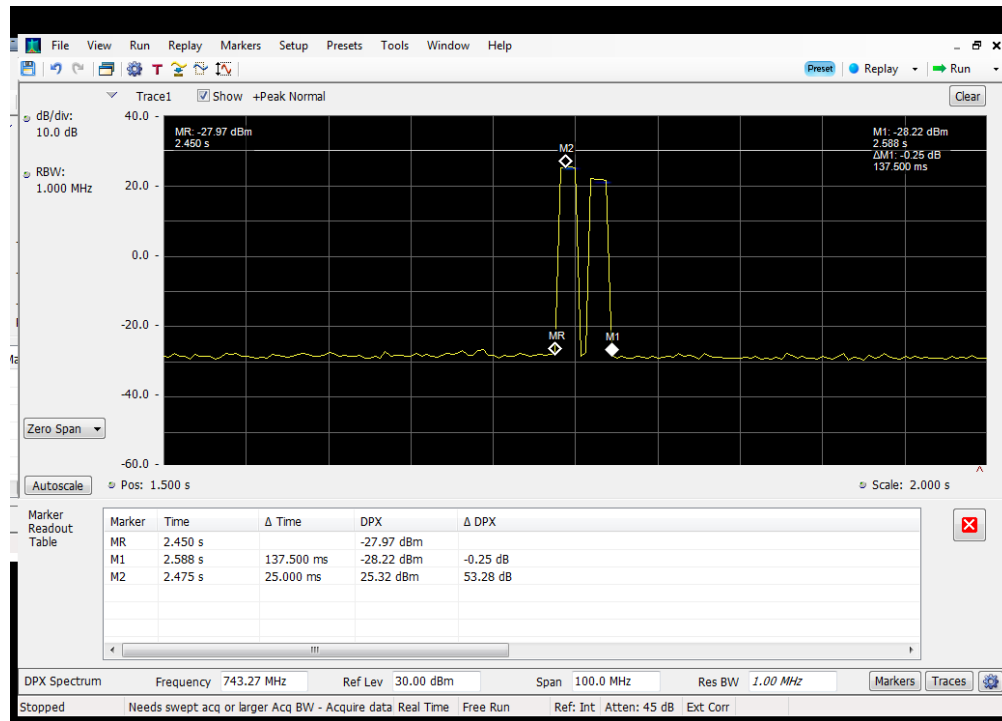


Detection Time_Downlink_728 - 746 MHz_B12_Server 1 to Dedicated Donor Port 3

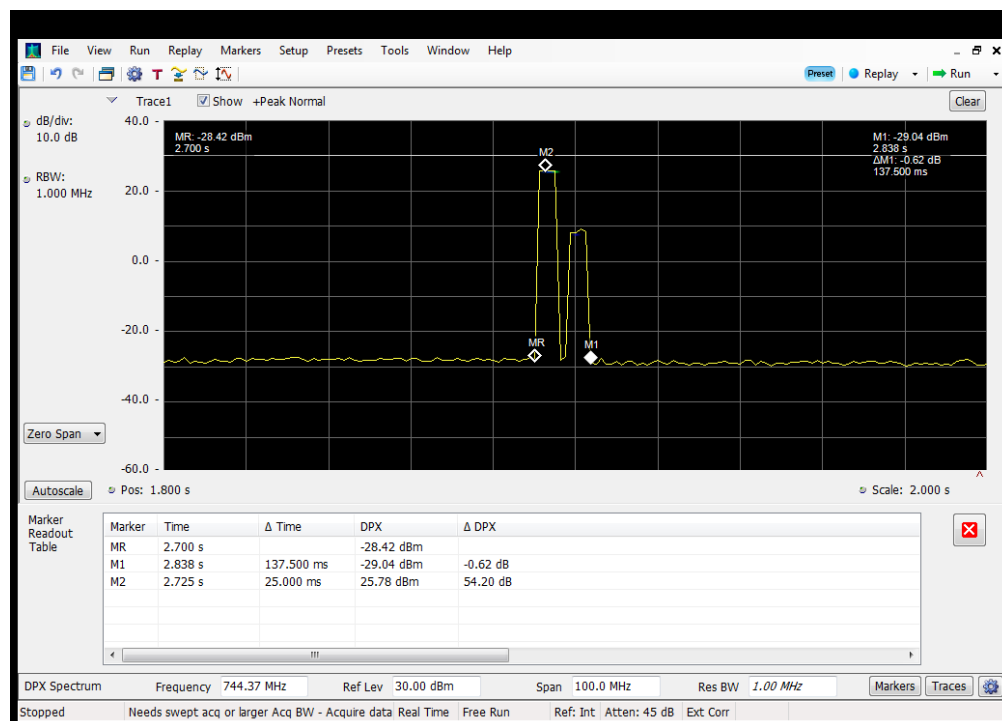




Detection Time_Downlink_728 - 746 MHz_B12_Server 2 to Common Donor Port 1

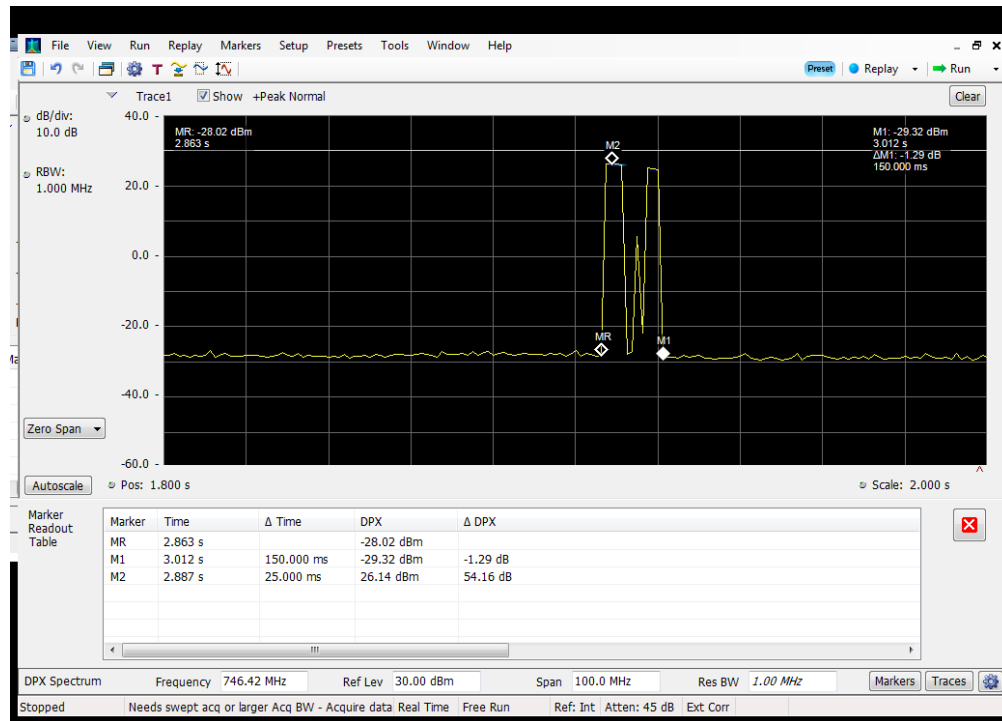


Detection Time_Downlink_728 - 746 MHz_B12_Server 2 to Dedicated Donor Port 3

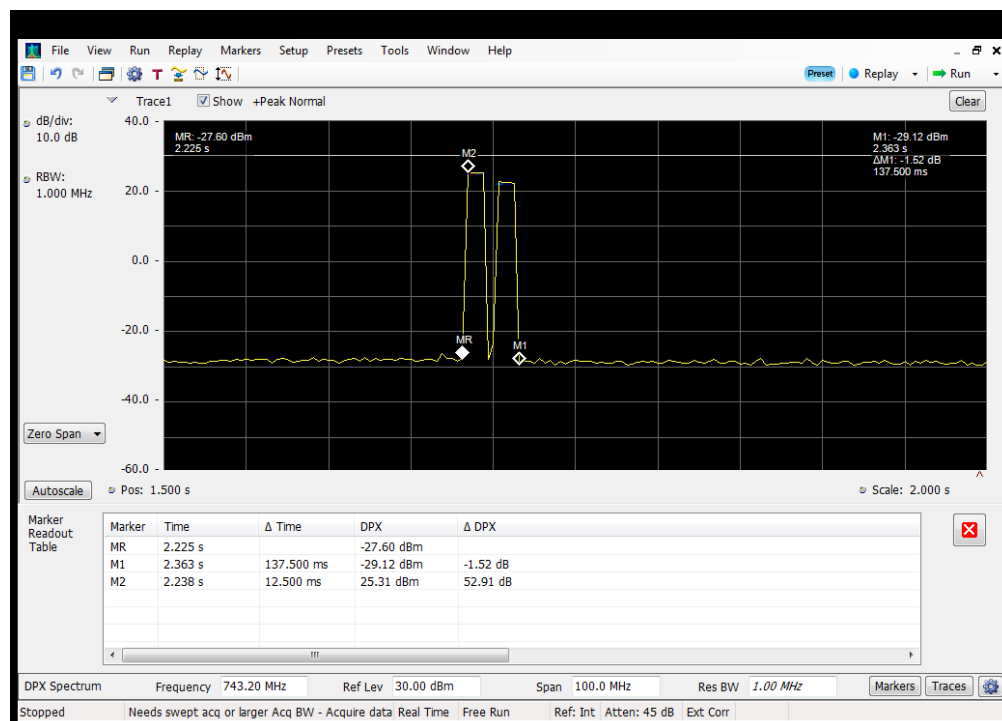




Detection Time_Downlink_728 - 746 MHz_B12_Server 3 to Common Donor Port 1

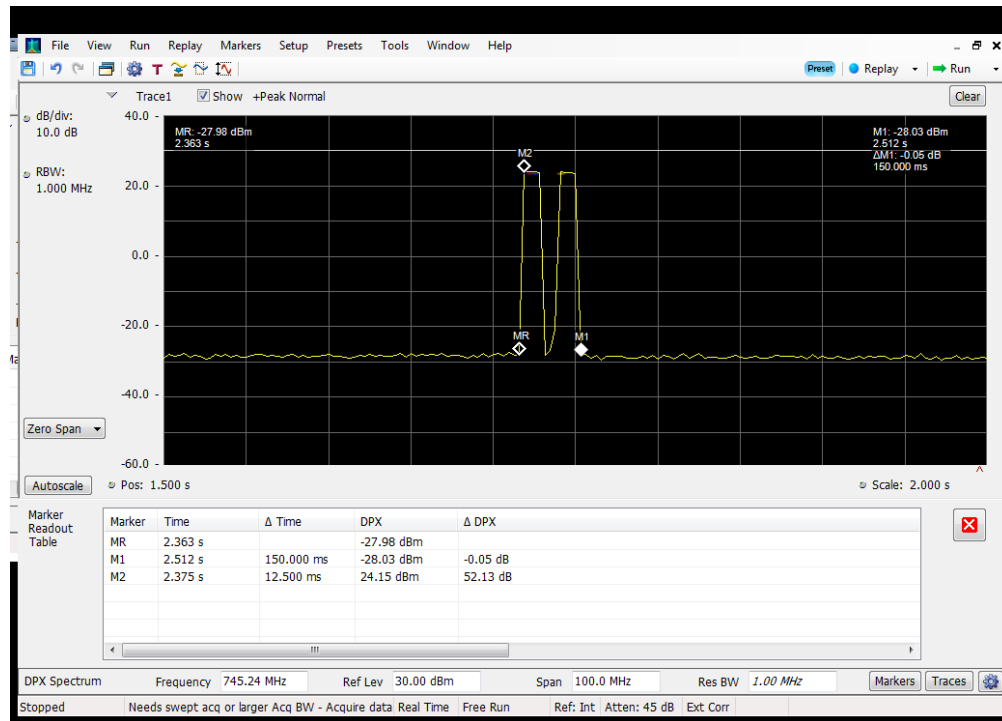


Detection Time_Downlink_728 - 746 MHz_B12_Server 3 to Dedicated Donor Port 3

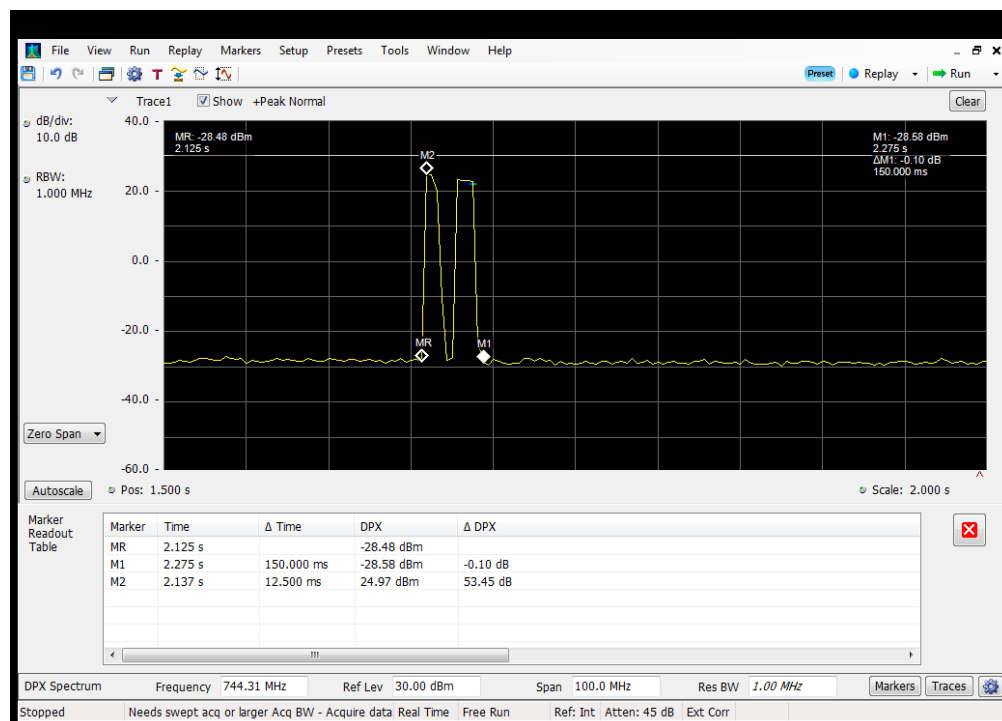




Detection Time_Downlink_728 - 746 MHz_B12_Server 4 to Common Donor Port 1

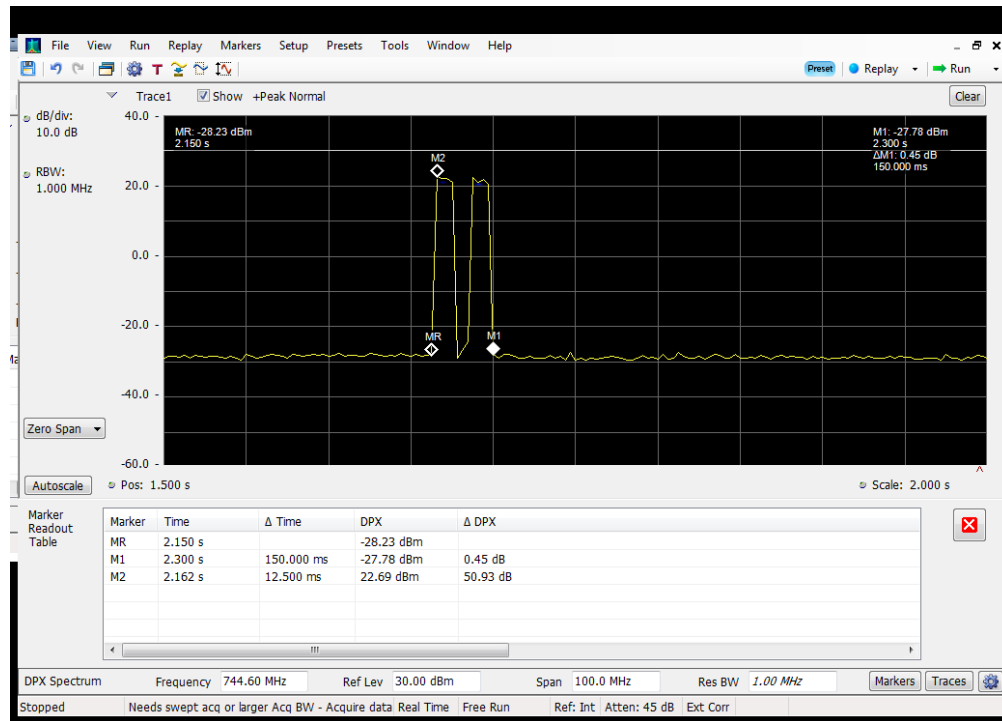


Detection Time_Downlink_728 - 746 MHz_B12_Server 4 to Dedicated Donor Port 3

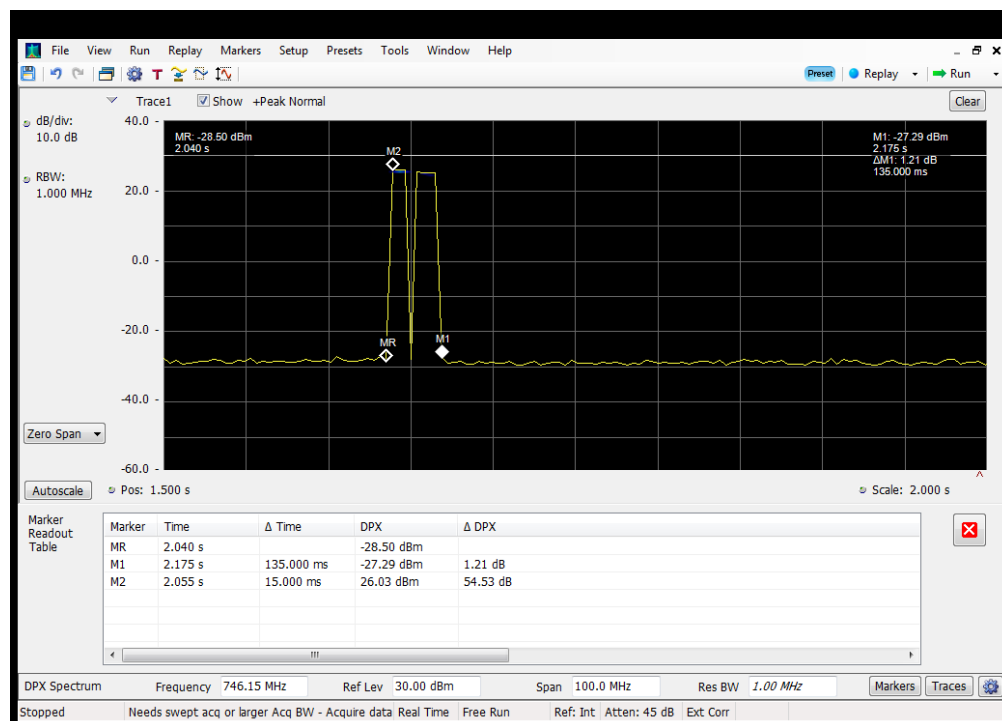




Detection Time_Downlink_746 - 757 MHz_B13_Server 1 to Common Donor Port 1

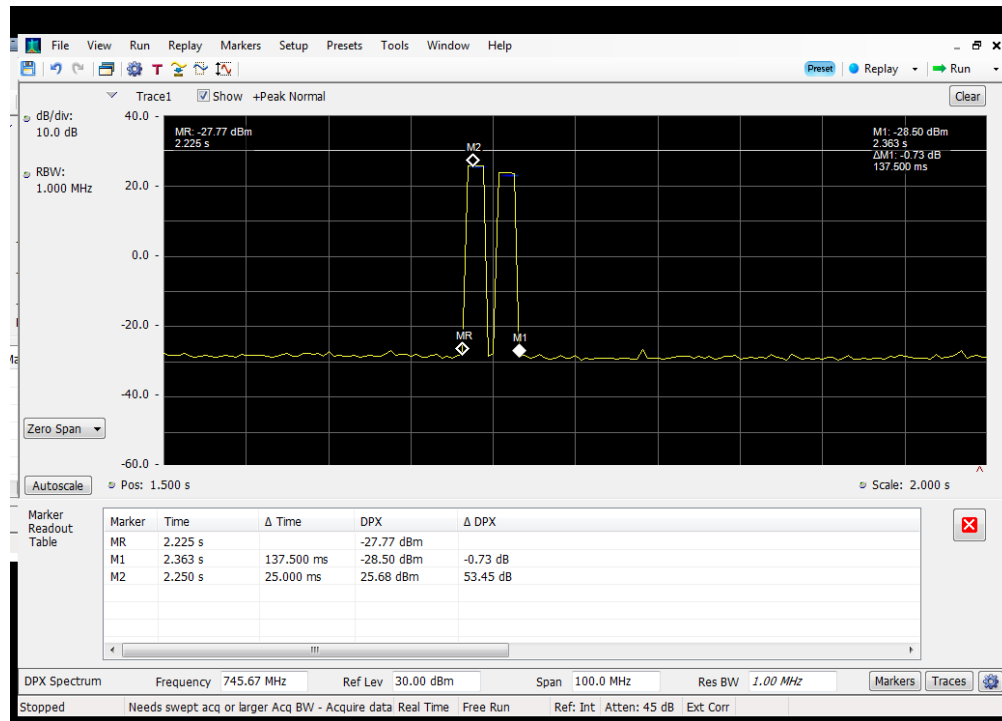


Detection Time_Downlink_746 - 757 MHz_B13_Server 1 to Dedicated Donor Port 3

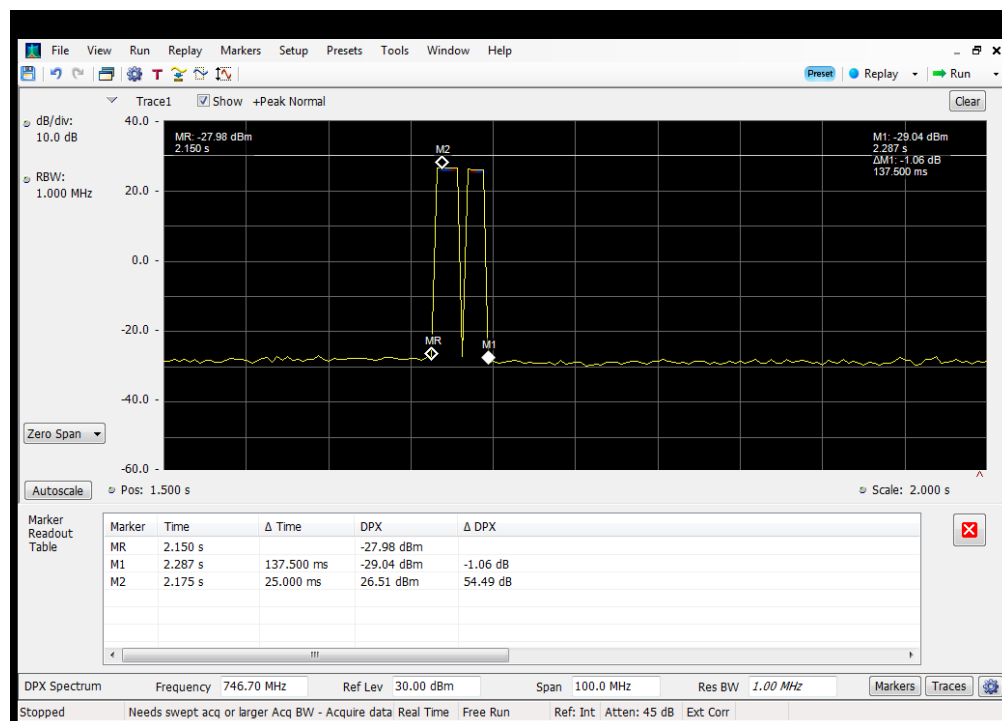




Detection Time_Downlink_746 - 757 MHz_B13_Server 2 to Common Donor Port 1

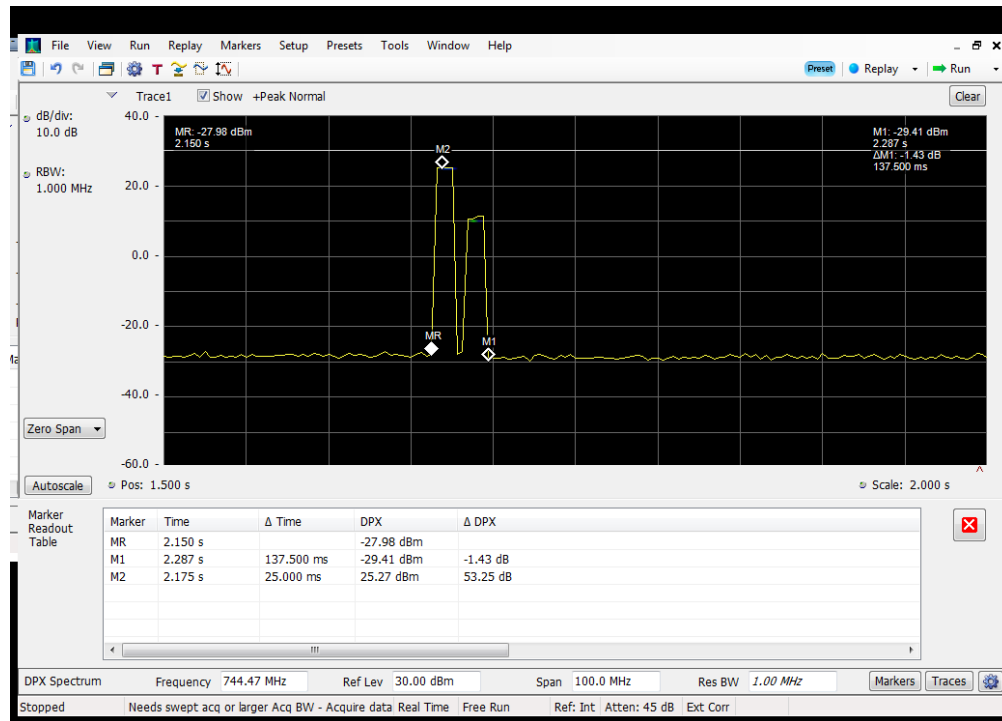


Detection Time_Downlink_746 - 757 MHz_B13_Server 2 to Dedicated Donor Port 3

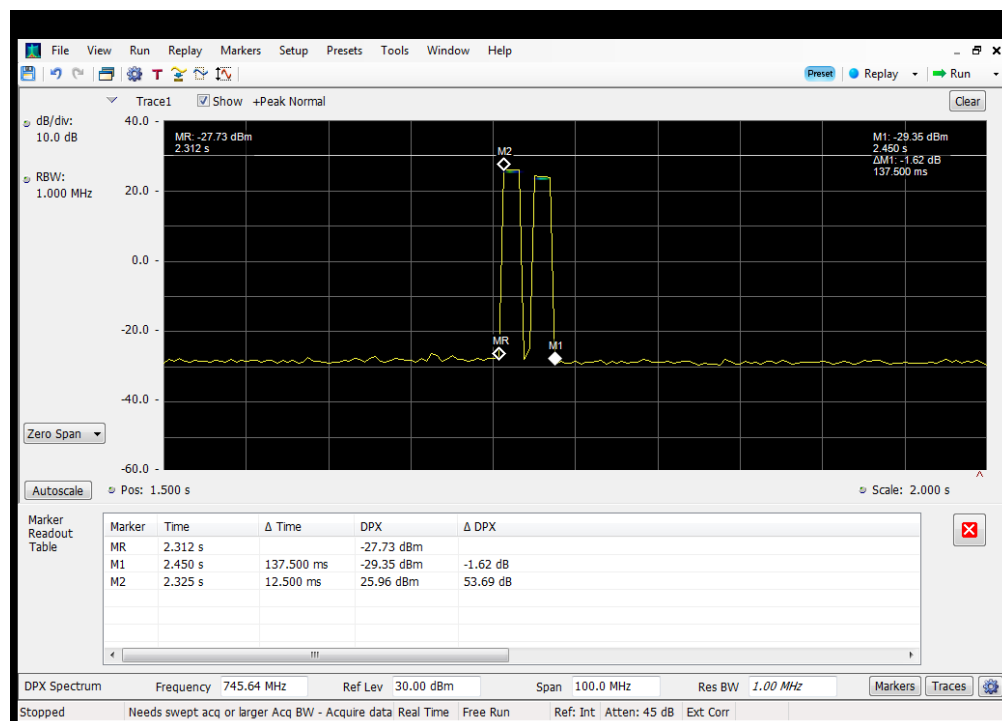




Detection Time_Downlink_746 - 757 MHz_B13_Server 3 to Common Donor Port 1

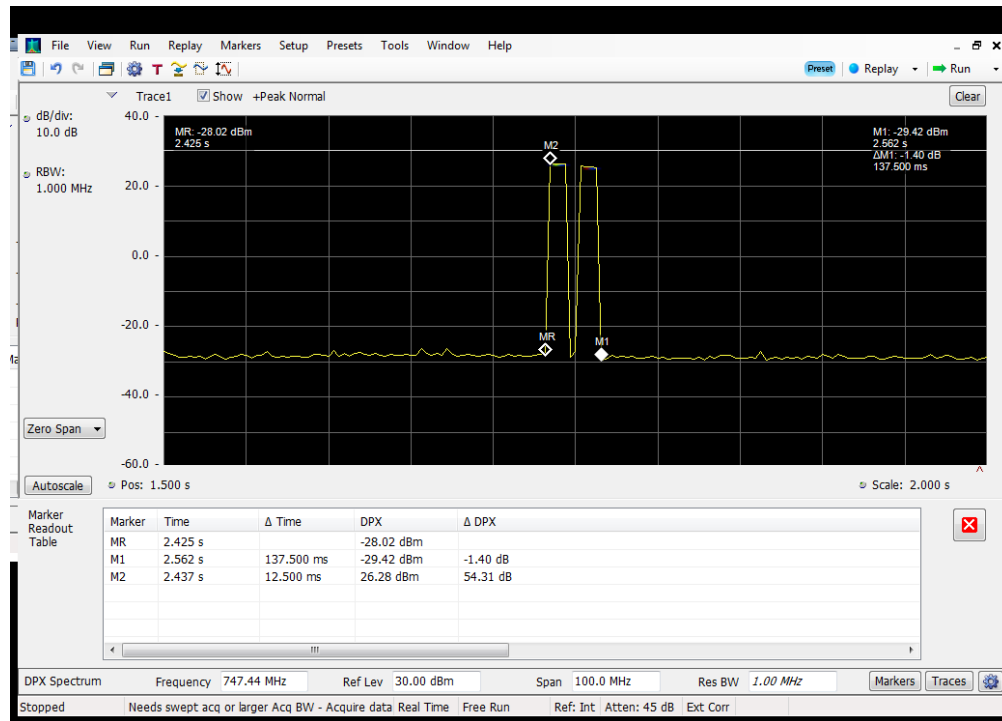


Detection Time_Downlink_746 - 757 MHz_B13_Server 3 to Dedicated Donor Port 3

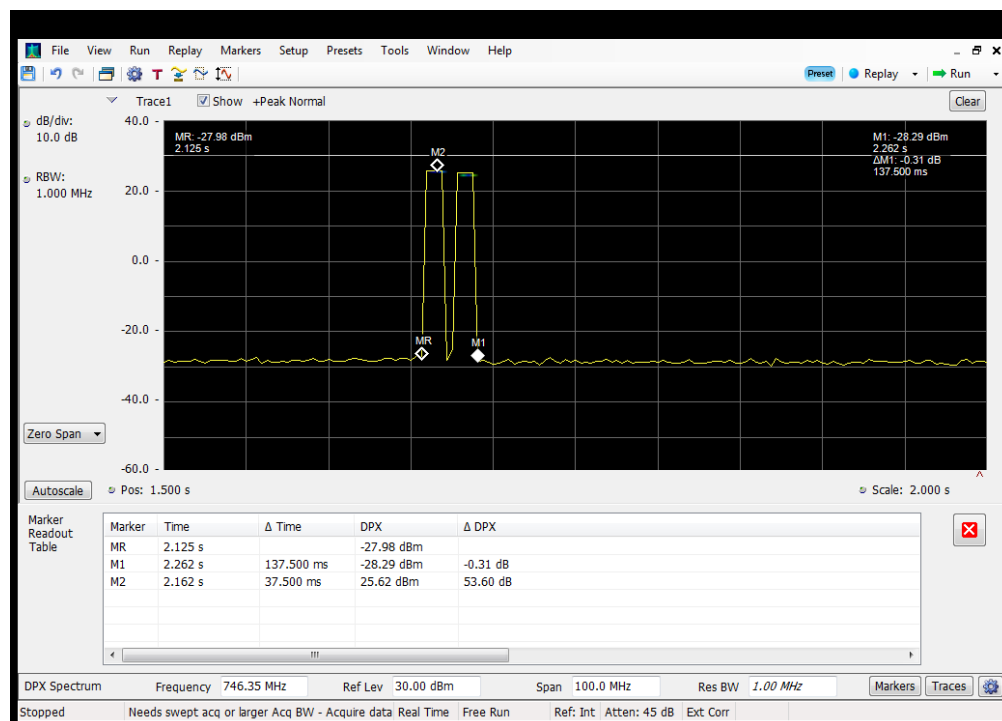




Detection Time_Downlink_746 - 757 MHz_B13_Server 4 to Common Donor Port 1

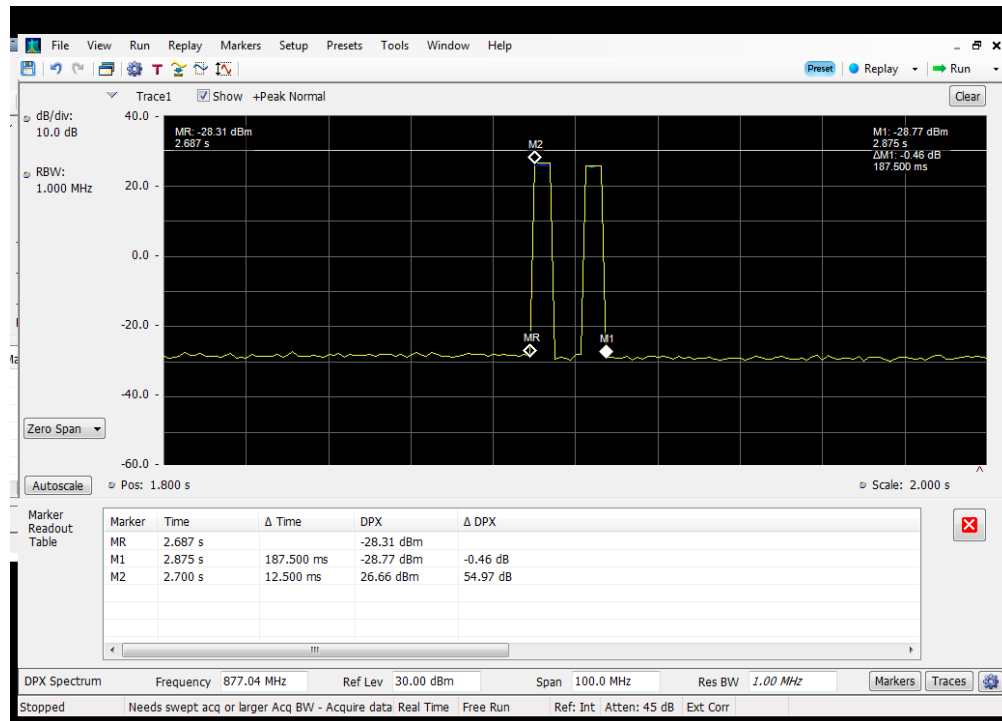


Detection Time_Downlink_746 - 757 MHz_B13_Server 4 to Dedicated Donor Port 3

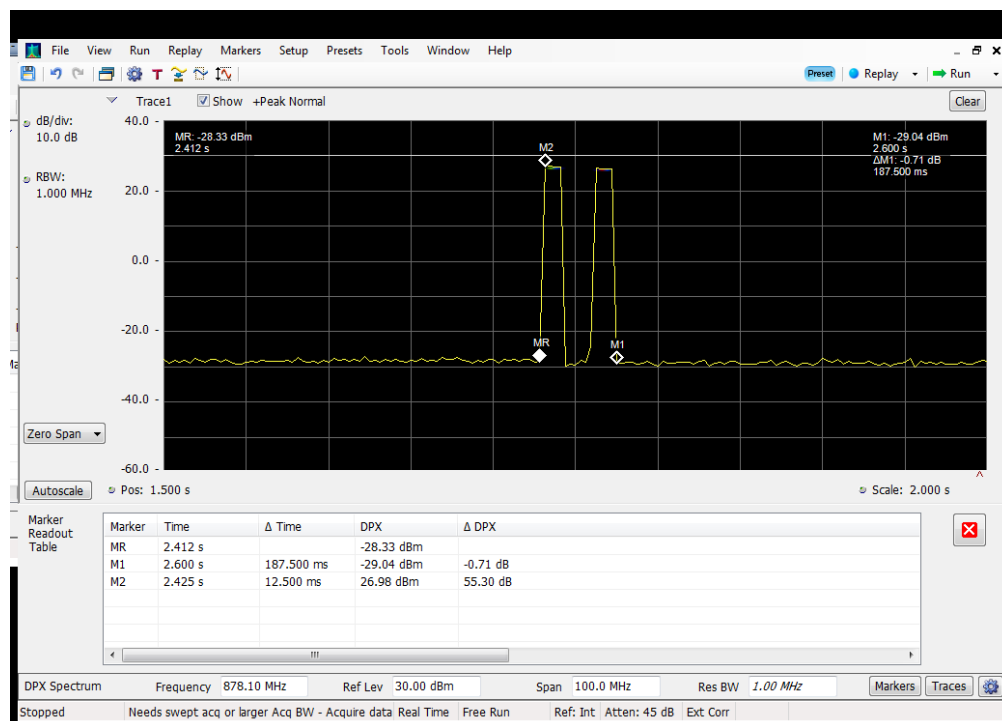




Detection Time_Downlink_869 - 894 MHz_B13_Server 4 to Common Donor Port 1

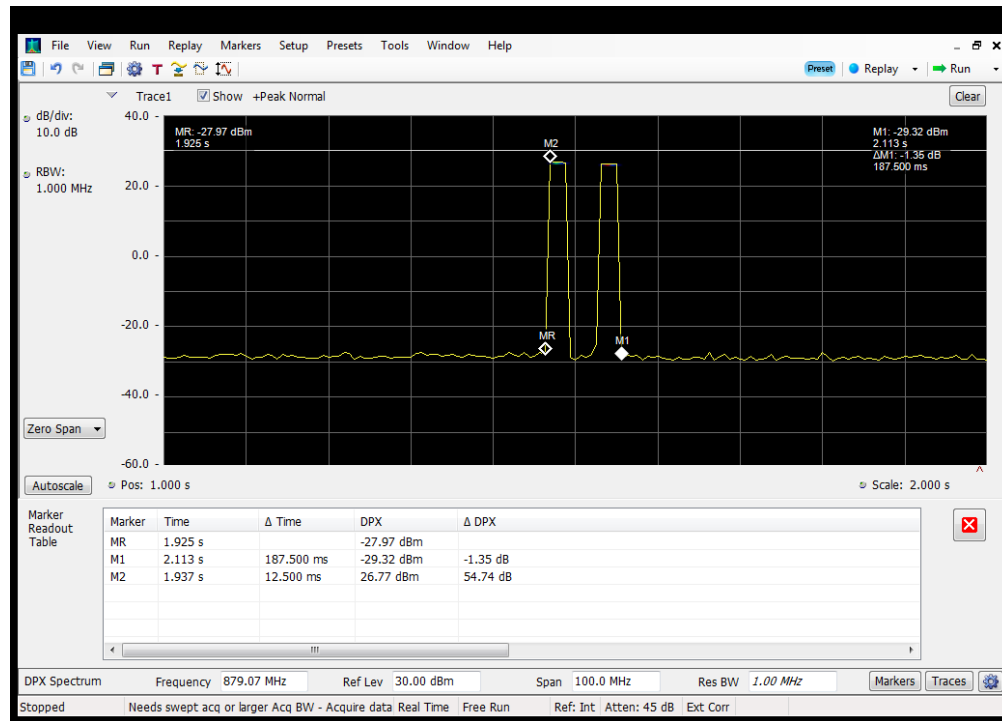


Detection Time_Downlink_869 - 894 MHz_B5_Server 1 to Common Donor Port 1

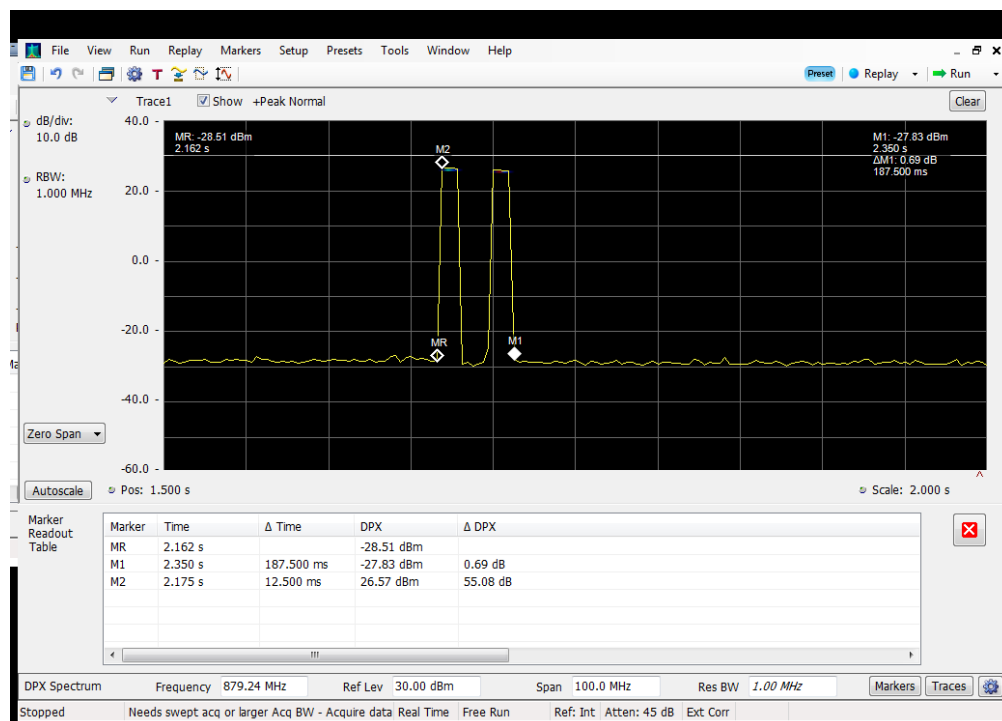




Detection Time_Downlink_869 - 894 MHz_B5_Server 1 to Dedicated Donor Port 2

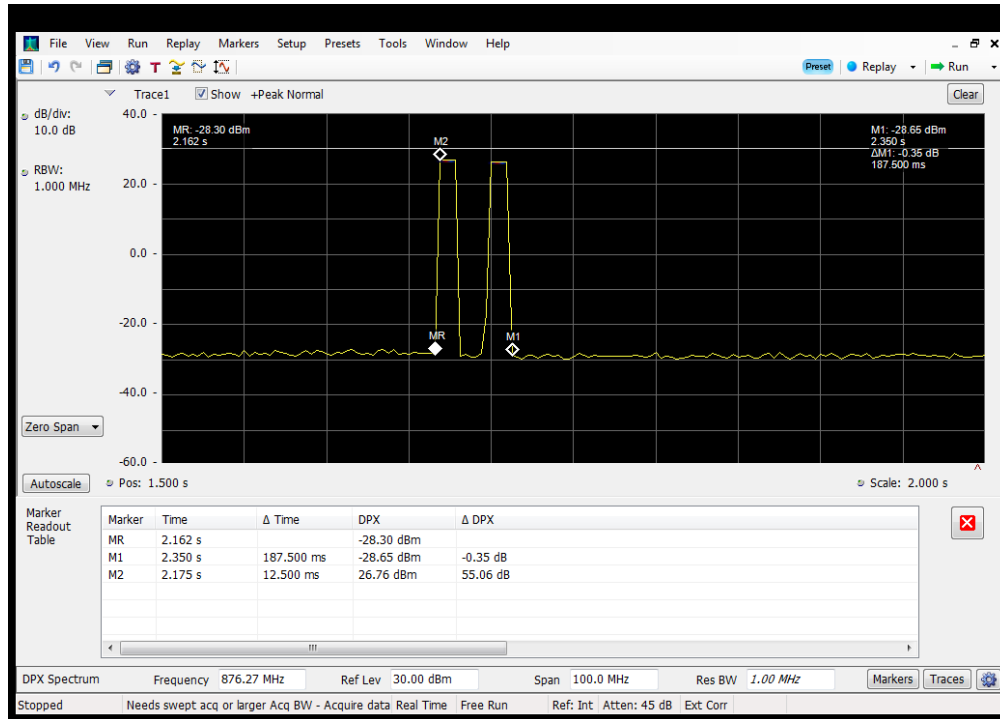


Detection Time_Downlink_869 - 894 MHz_B5_Server 2 to Common Donor Port 1

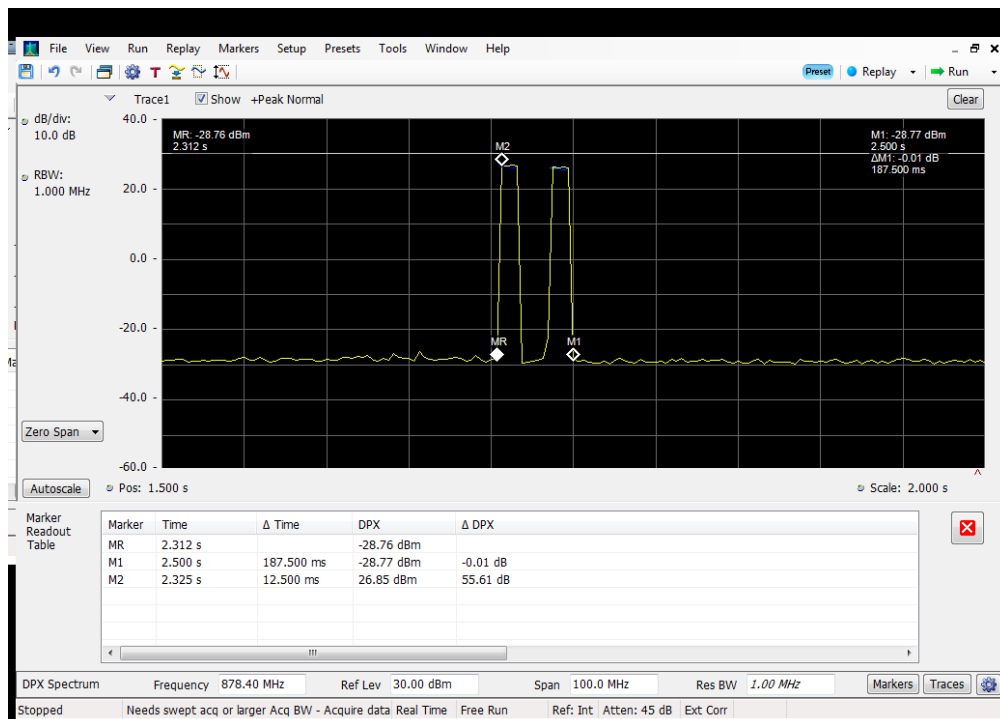




Detection Time_Downlink_869 - 894 MHz_B5_Server 2 to Dedicated Donor Port 2

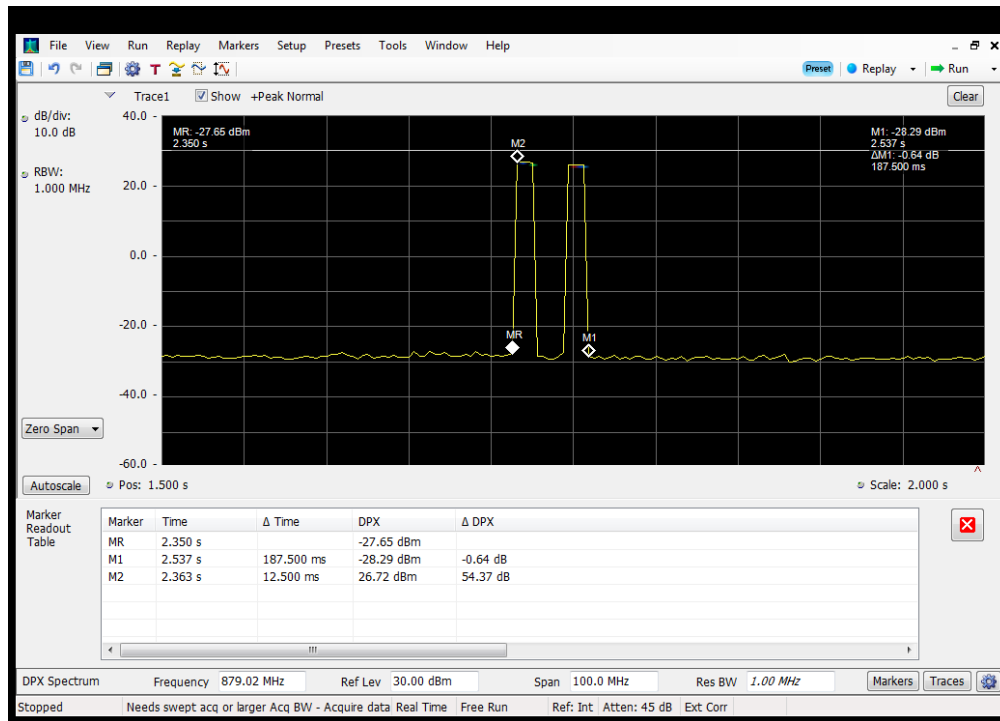


Detection Time_Downlink_869 - 894 MHz_B5_Server 3 to Common Donor Port 1

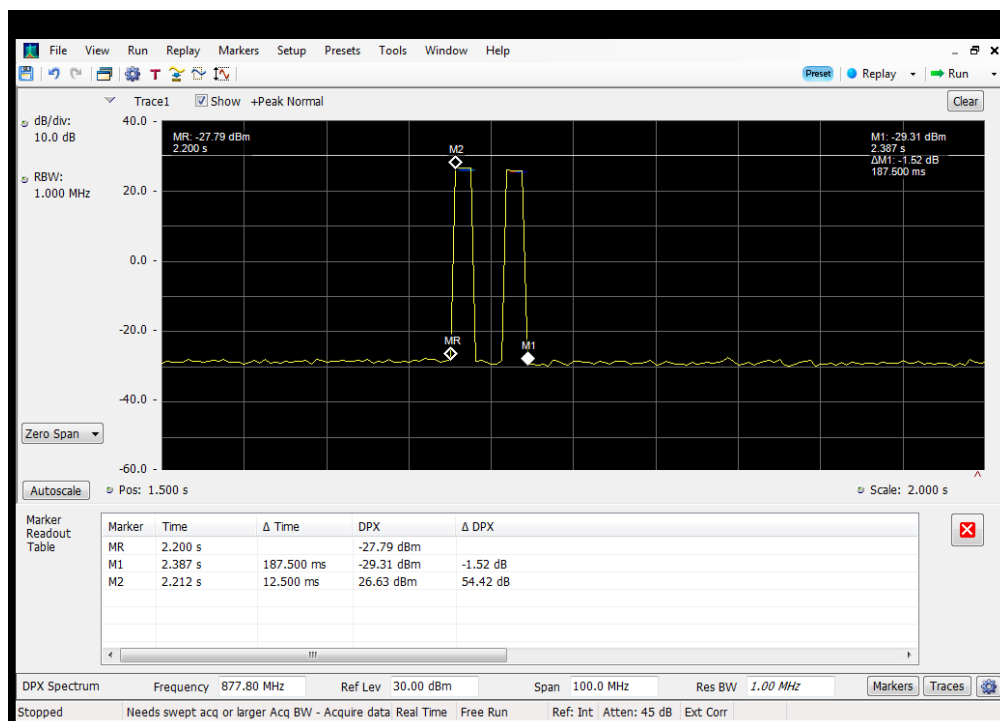




Detection Time_Downlink_869 - 894 MHz_B5_Server 3 to Dedicated Donor Port 2

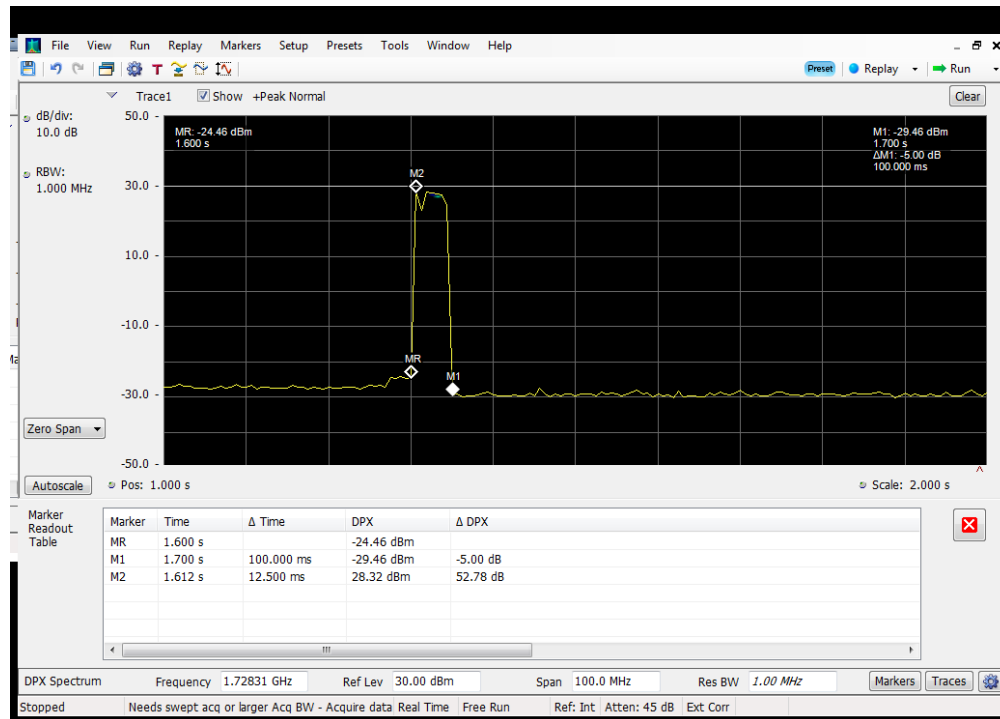


Detection Time_Downlink_869 - 894 MHz_B5_Server 4 to Dedicated Donor Port 2

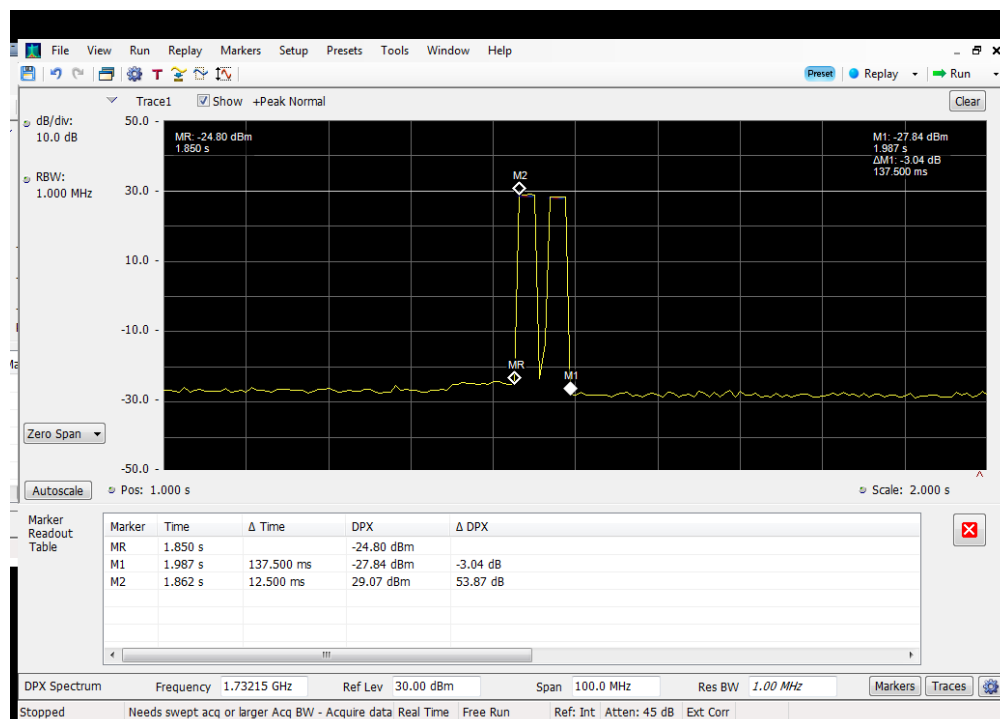




Detection Time_Uplink_1710 - 1755 MHz_B4_Server 1 to Dedicated Donor Port 1

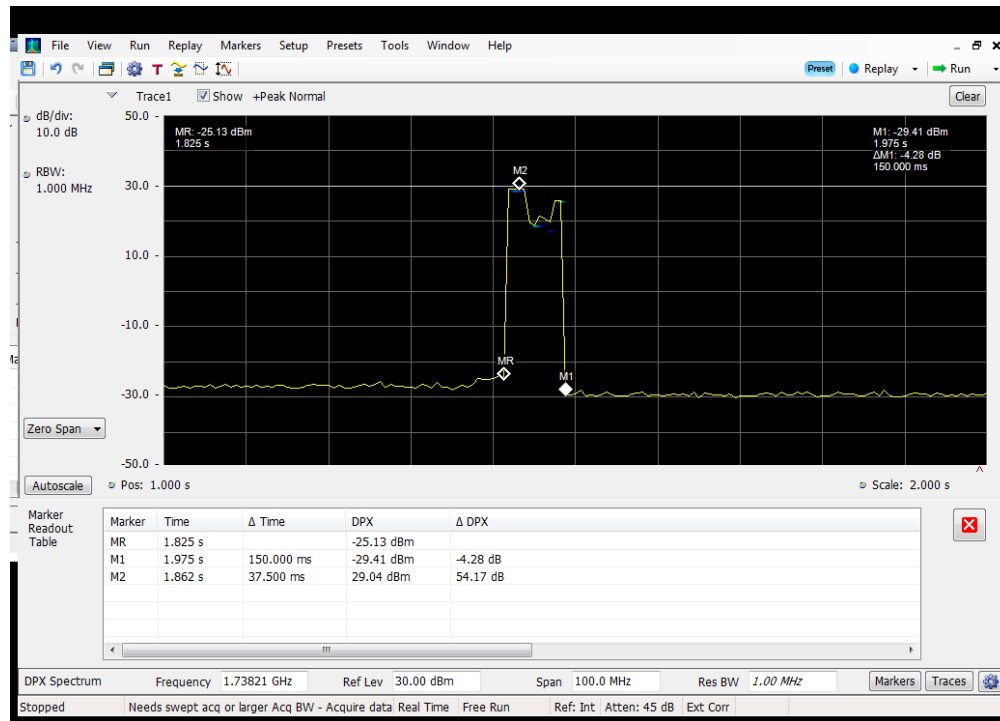


Detection Time_Uplink_1710 - 1755 MHz_B4_Server 2 to Dedicated Donor Port 1

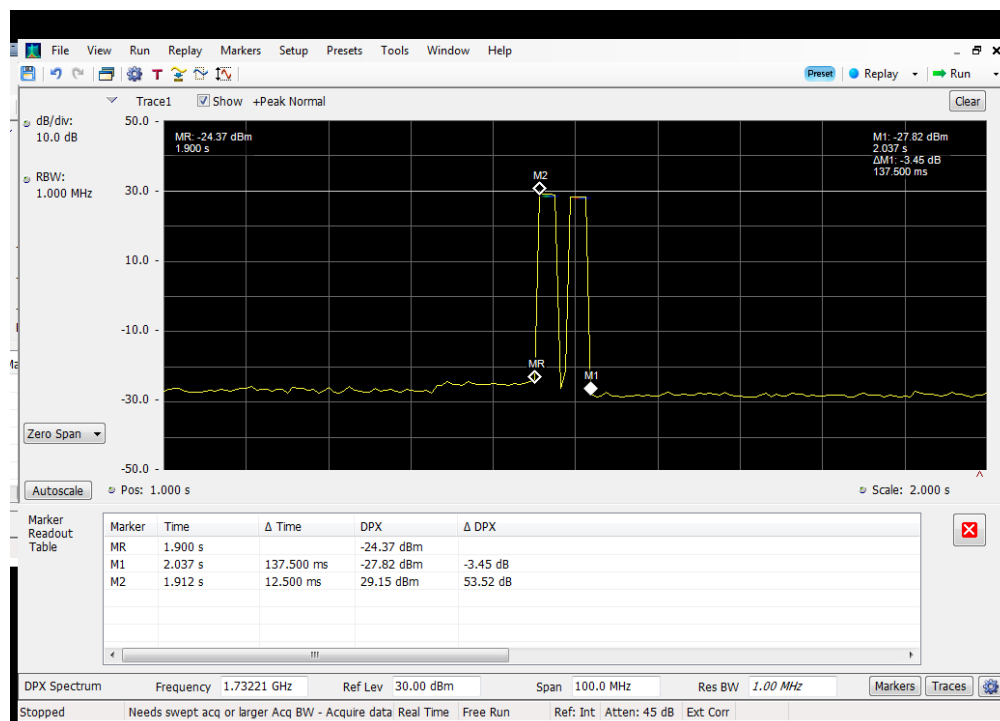




Detection Time_Uplink_1710 - 1755 MHz_B4_Server 3 to Dedicated Donor Port 1

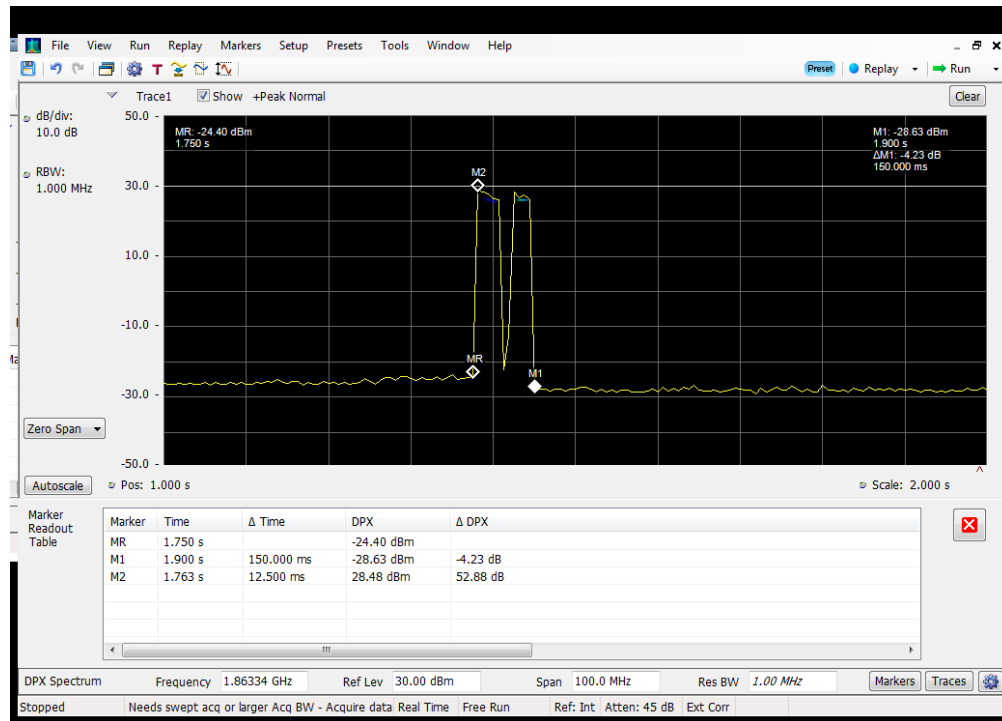


Detection Time_Uplink_1710 - 1755 MHz_B4_Server 4 to Dedicated Donor Port 1

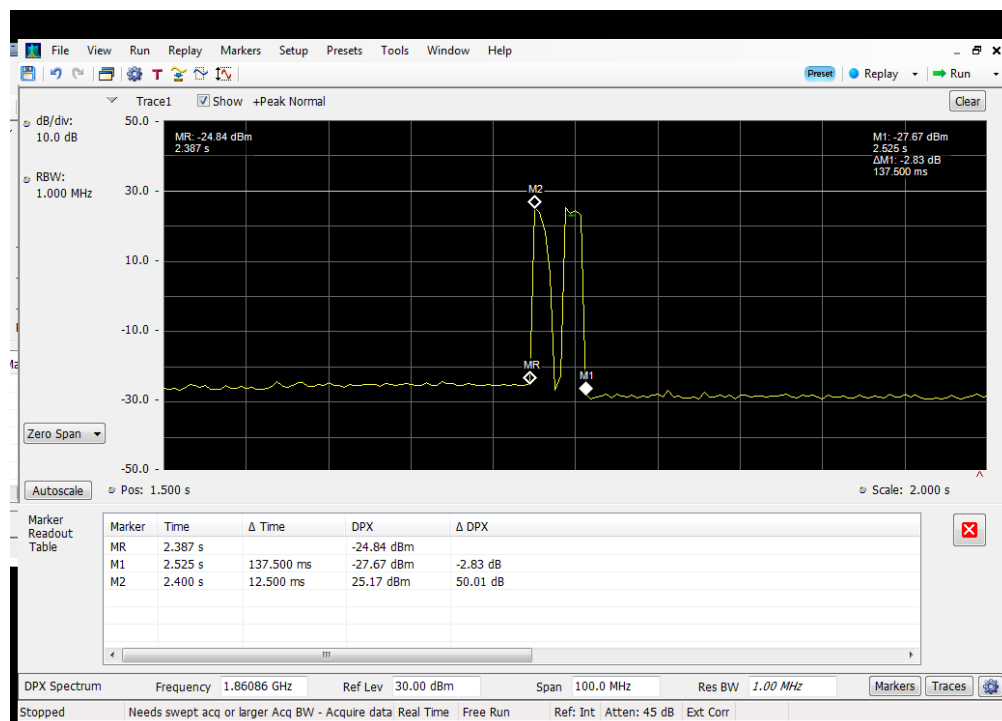




Detection Time_Uplink_1850 - 1915 MHz_B25_Server 1 to Dedicated Donor Port 1

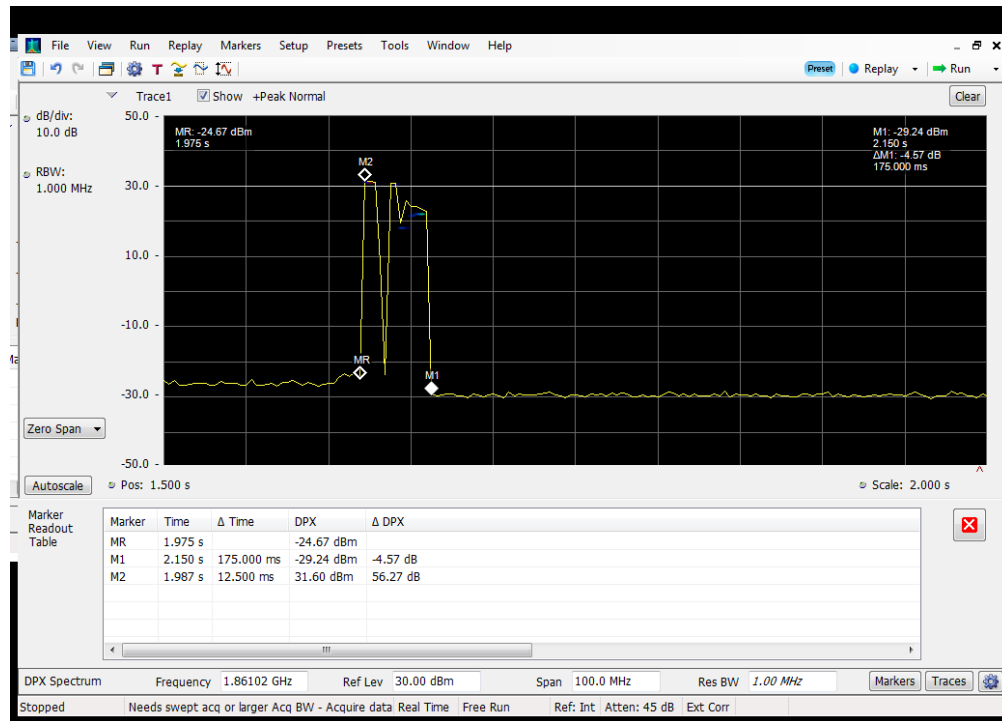


Detection Time_Uplink_1850 - 1915 MHz_B25_Server 2 to Dedicated Donor Port 1

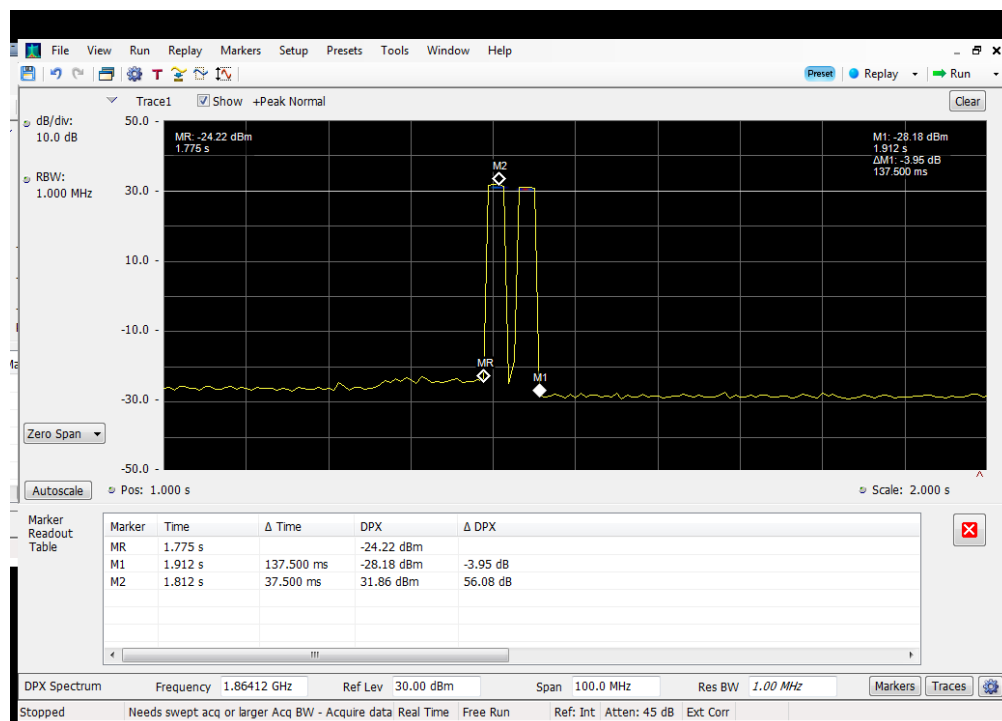




Detection Time_Uplink_1850 - 1915 MHz_B25_Server 3 to Dedicated Donor Port 1

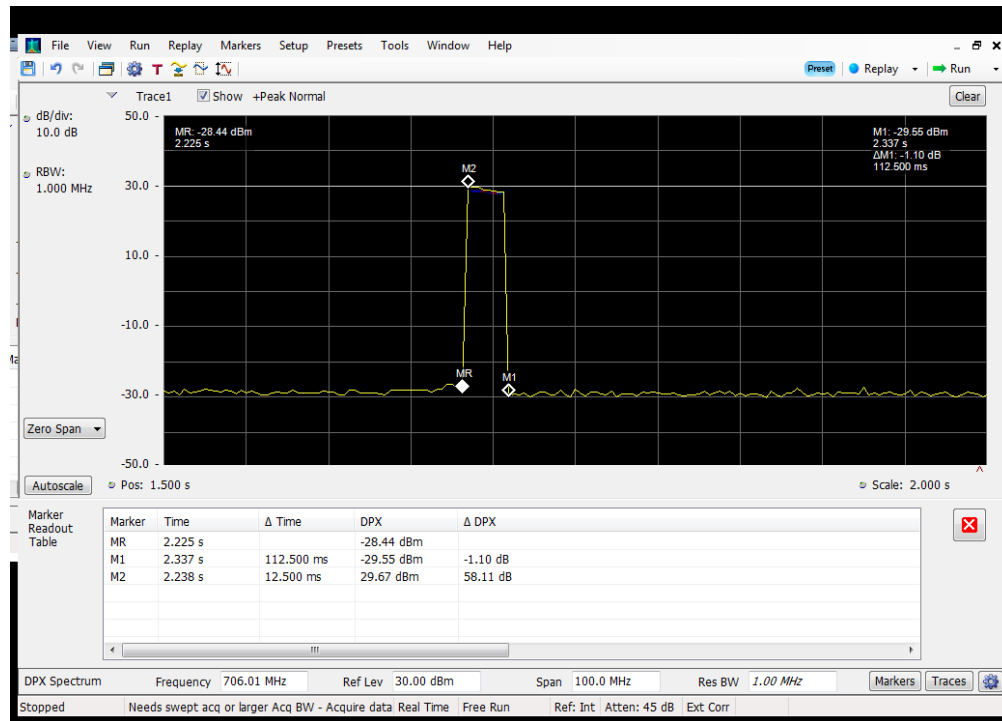


Detection Time_Uplink_1850 - 1915 MHz_B25_Server 4 to Dedicated Donor Port 1

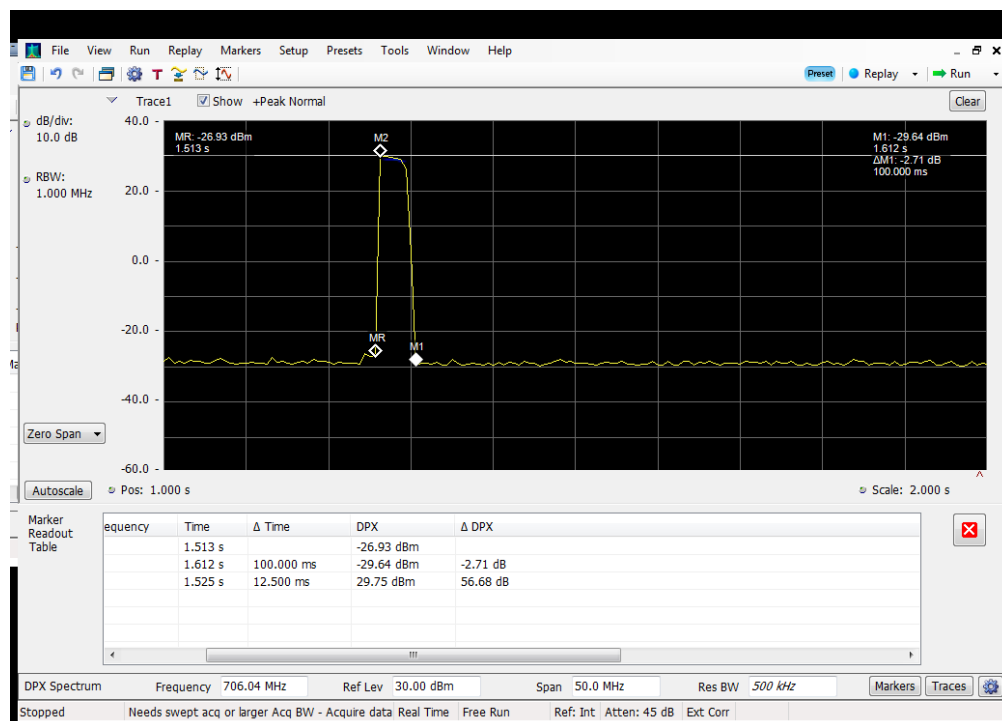




Detection Time_Uplink_698 - 716 MHz_B12_Server 1 to Common Donor Port 1

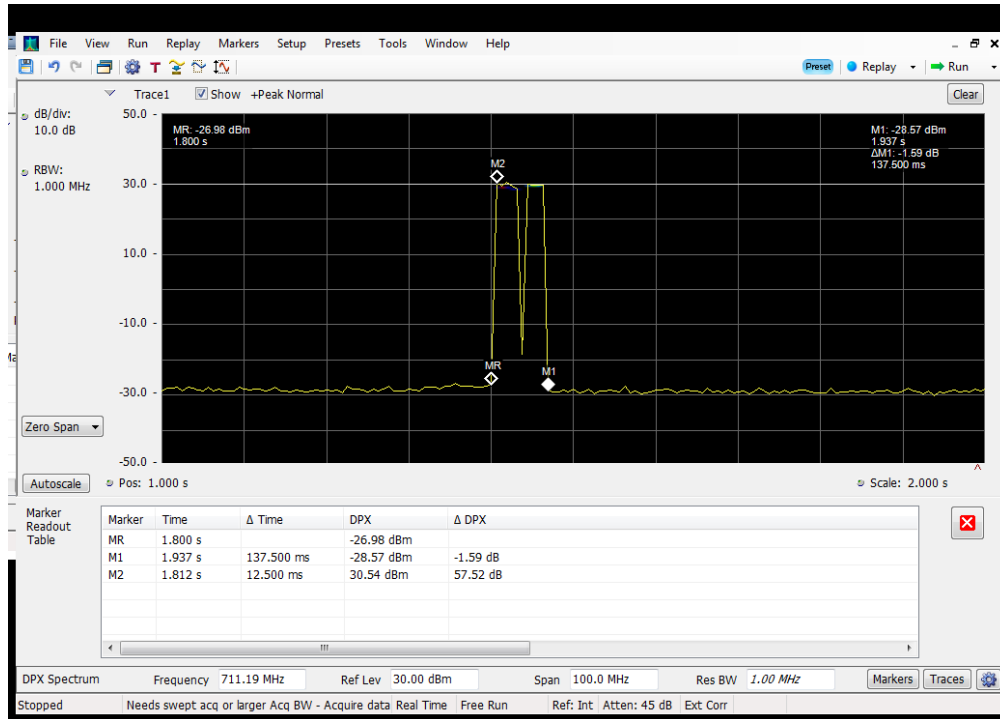


Detection Time_Uplink_698 - 716 MHz_B12_Server 1 to Dedicated Donor Port 3

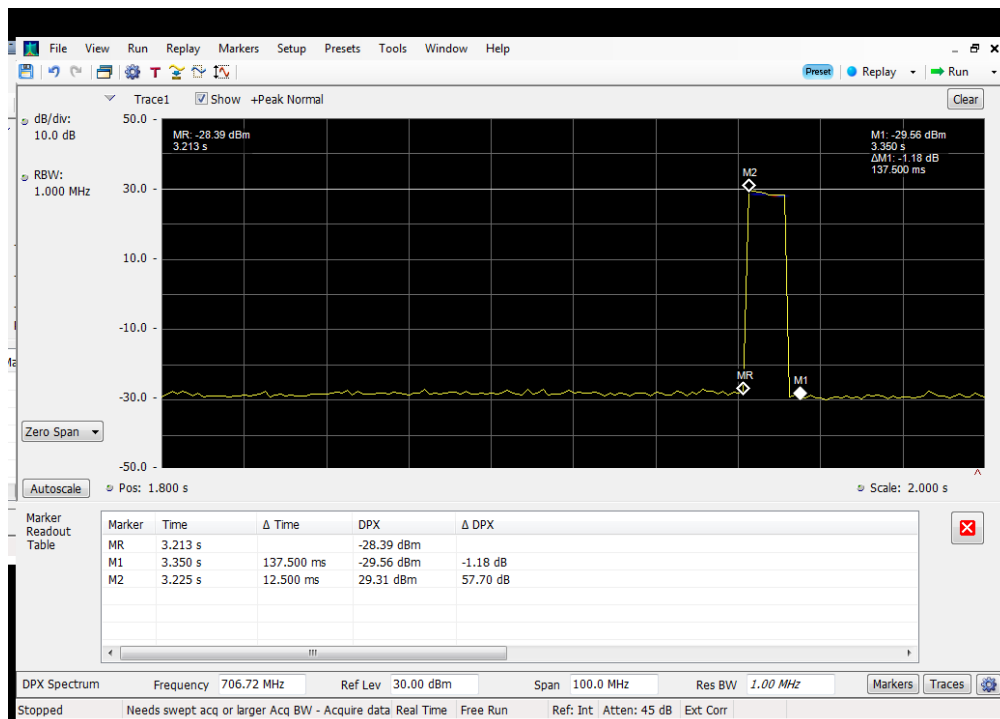




Detection Time_Uplink_698 - 716 MHz_B12_Server 2 to Dedicated Donor Port 3

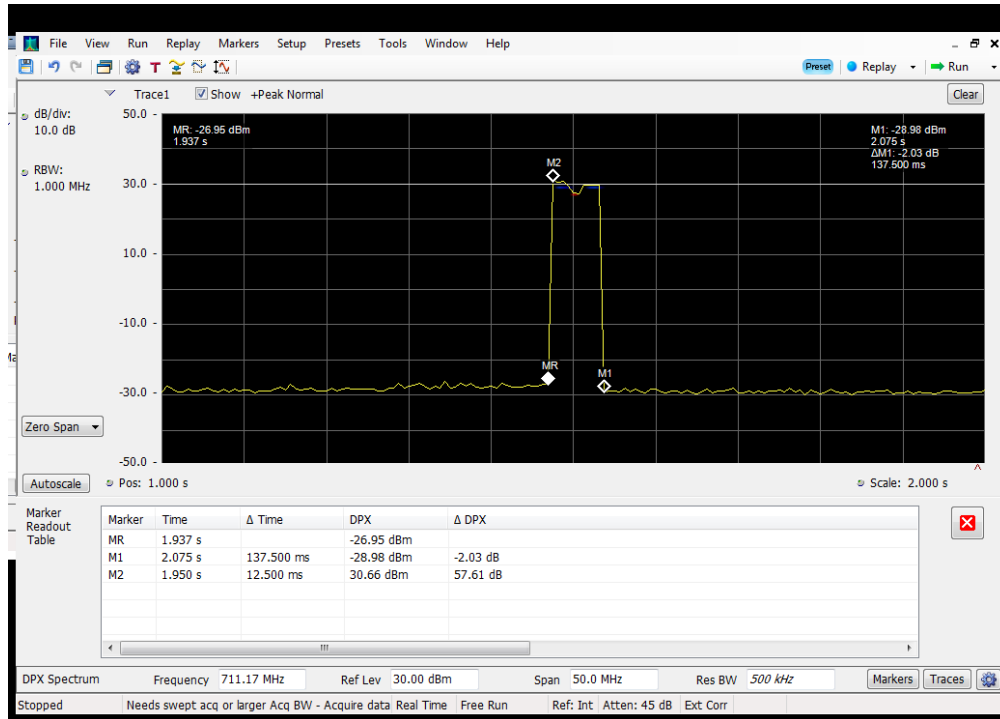


Detection Time_Uplink_698 - 716 MHz_B12_Server 3 to Common Donor Port 1

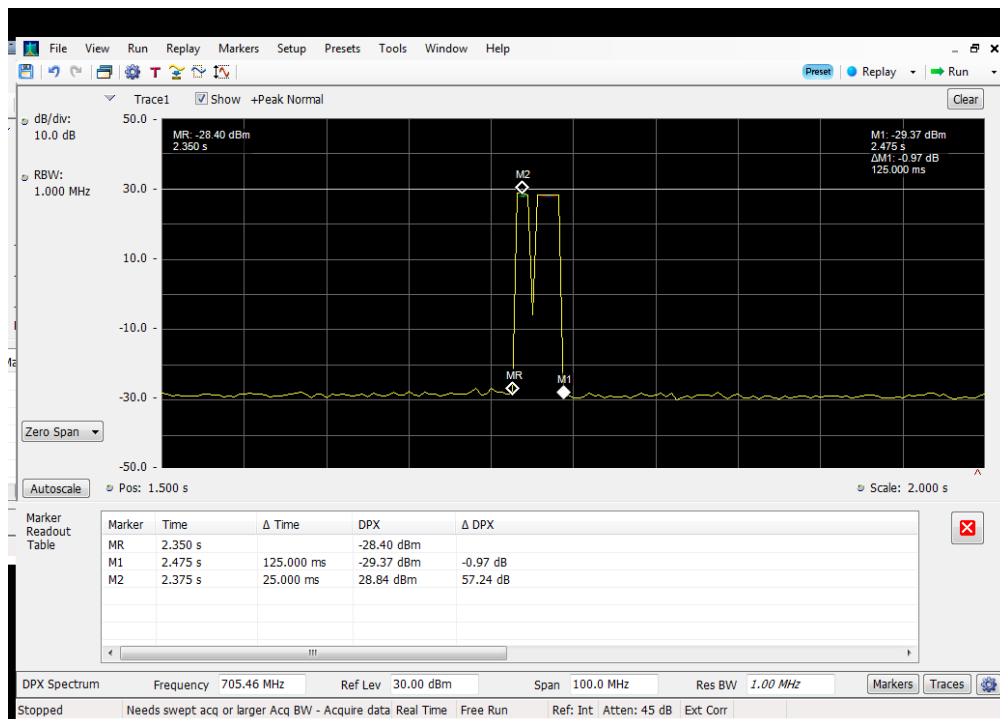




Detection Time_Uplink_698 - 716 MHz_B12_Server 3 to Dedicated Donor Port 3

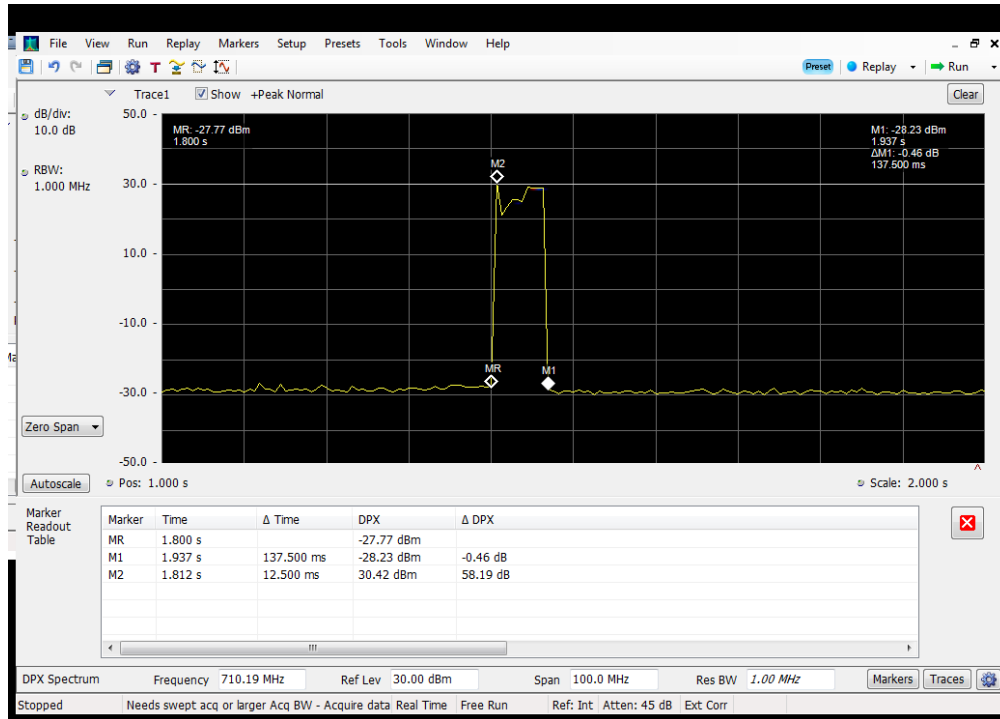


Detection Time_Uplink_698 - 716 MHz_B12_Server 4 to Common Donor Port 1

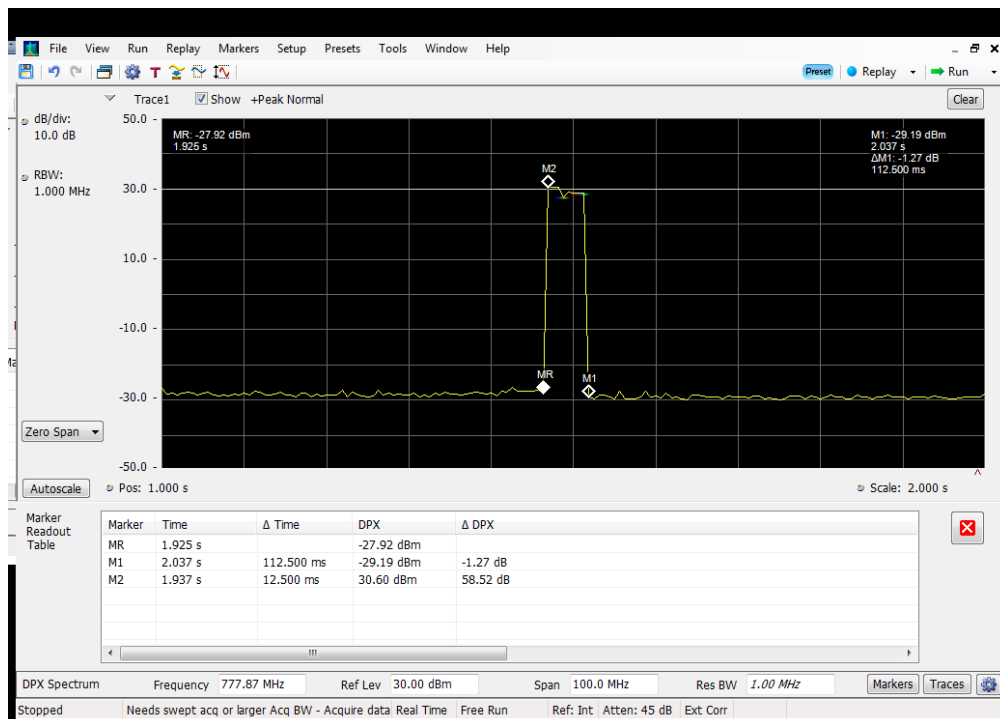




Detection Time_Uplink_698 - 716 MHz_B12_Server 4 to Dedicated Donor Port 3

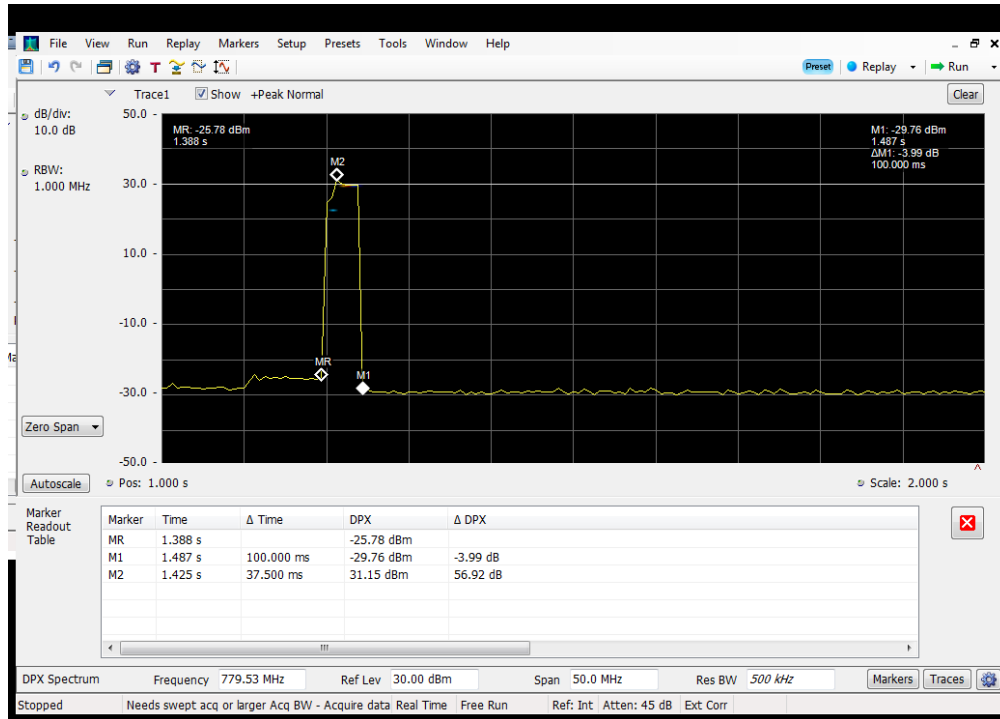


Detection Time_Uplink_776 - 787 MHz_B13_Server 1 to Common Donor Port 1

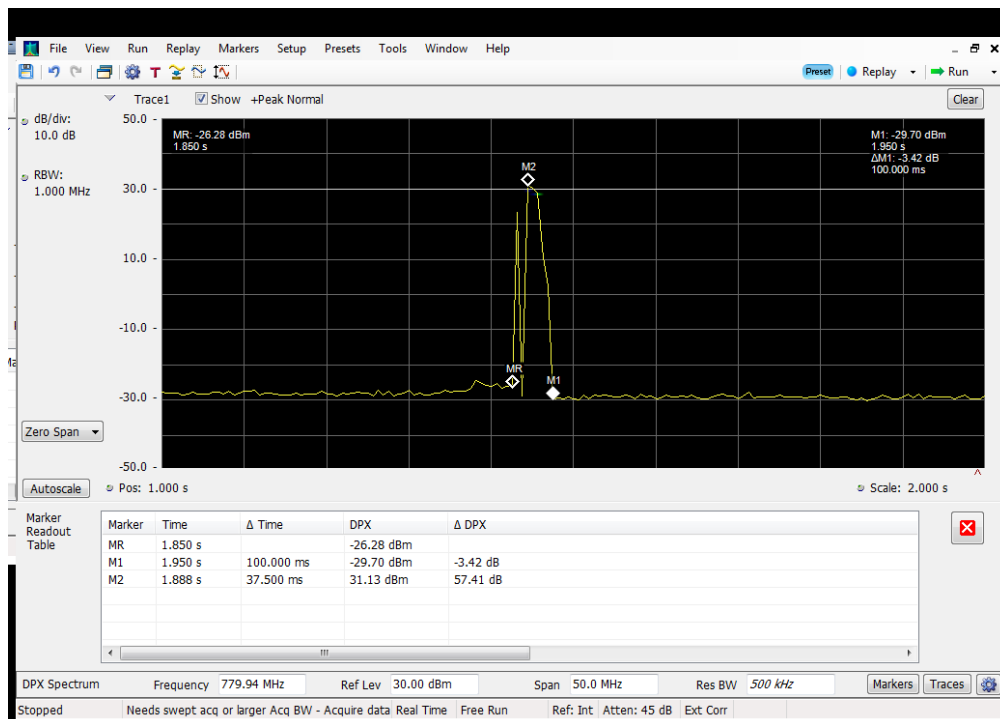




Detection Time_Uplink_776 - 787 MHz_B13_Server 1 to Dedicated Donor Port 3

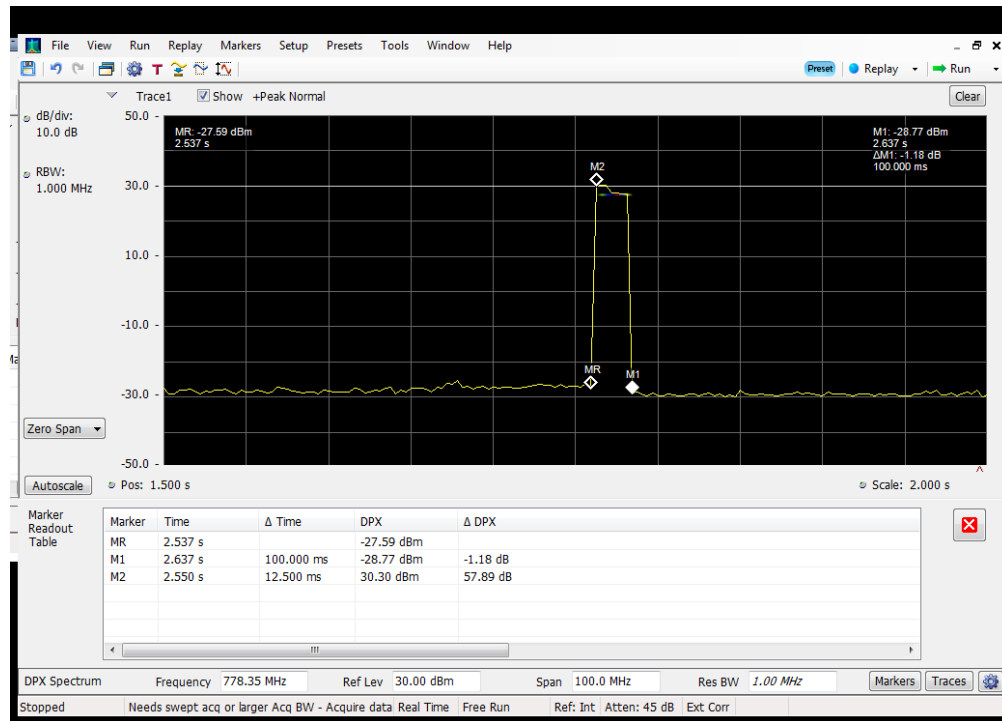


Detection Time_Uplink_776 - 787 MHz_B13_Server 2 to Dedicated Donor Port 3

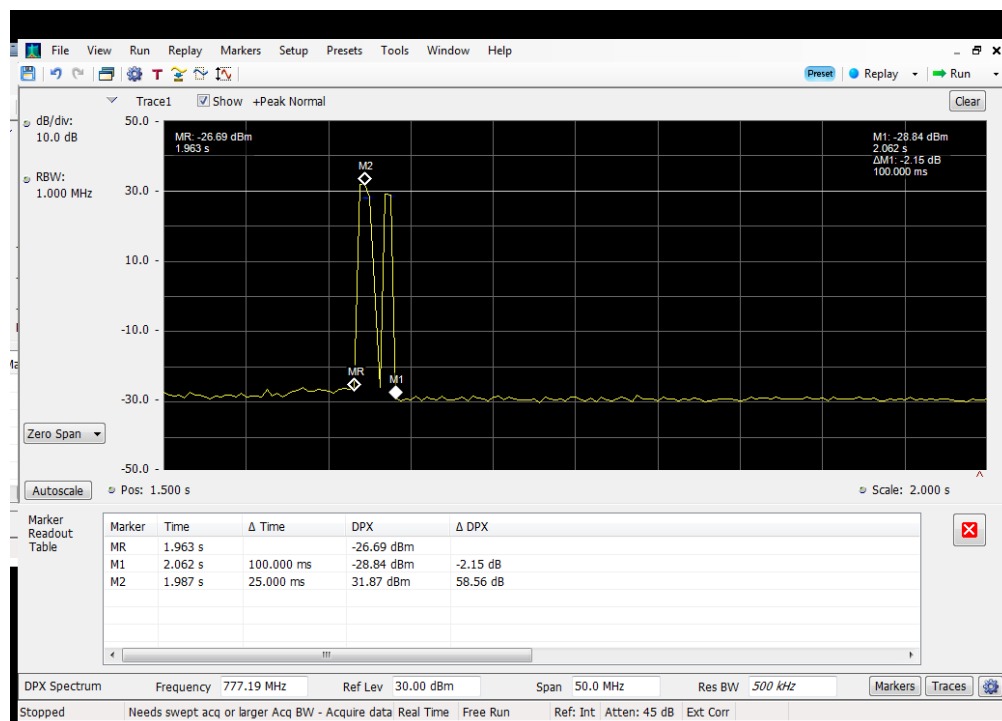




Detection Time_Uplink_776 - 787 MHz_B13_Server 3 to Common Donor Port 1

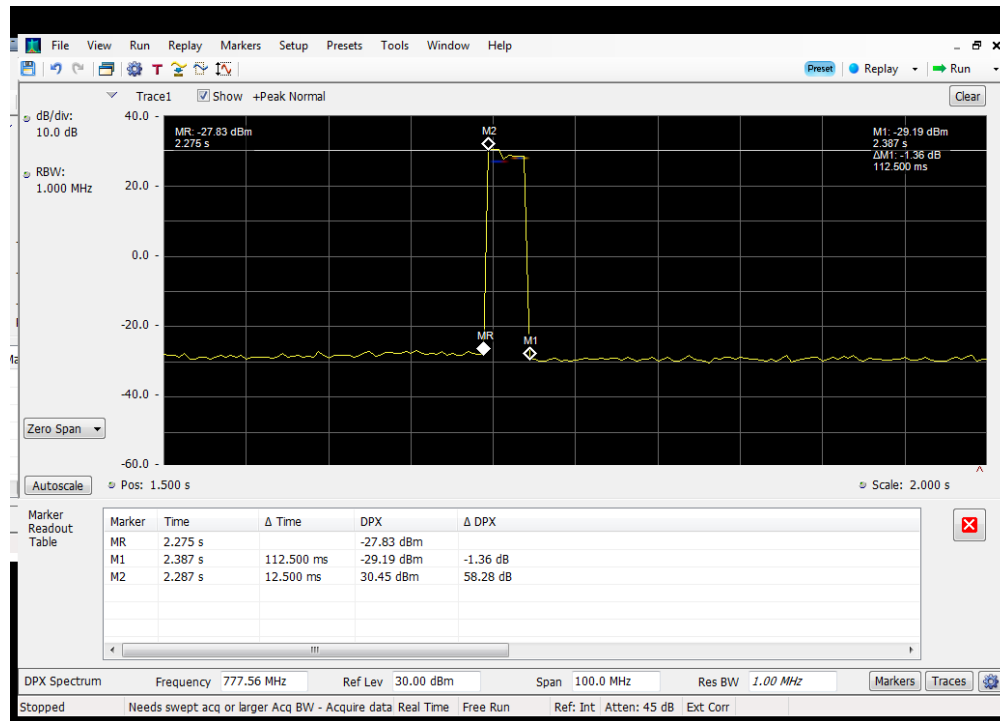


Detection Time_Uplink_776 - 787 MHz_B13_Server 3 to Dedicated Donor Port 3

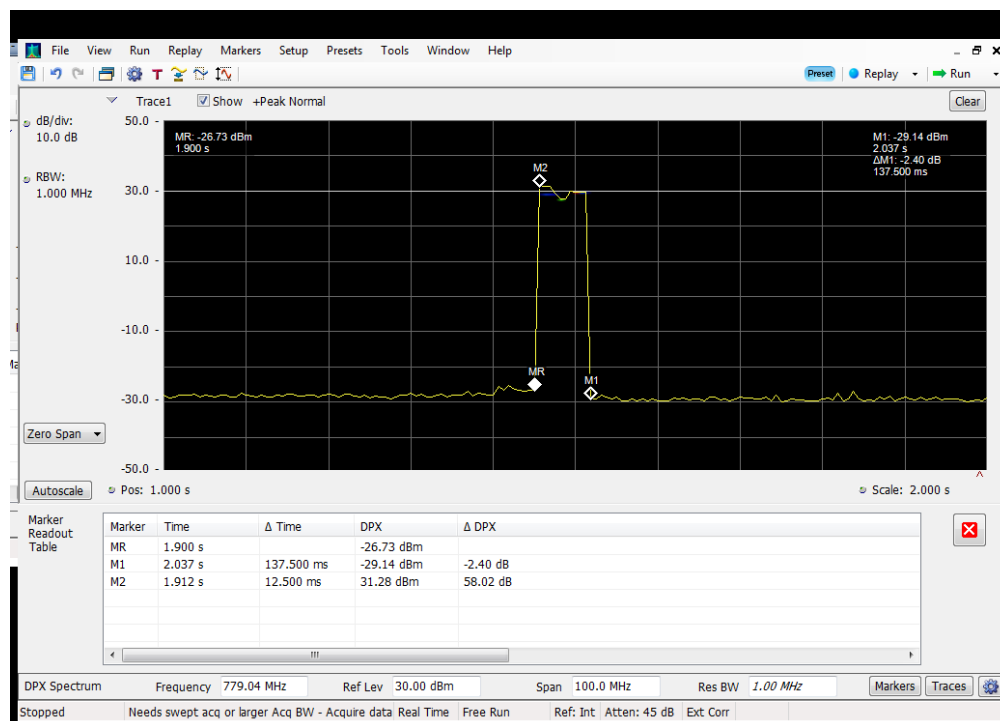




Detection Time_Uplink_776 - 787 MHz_B13_Server 4 to Common Donor Port 1

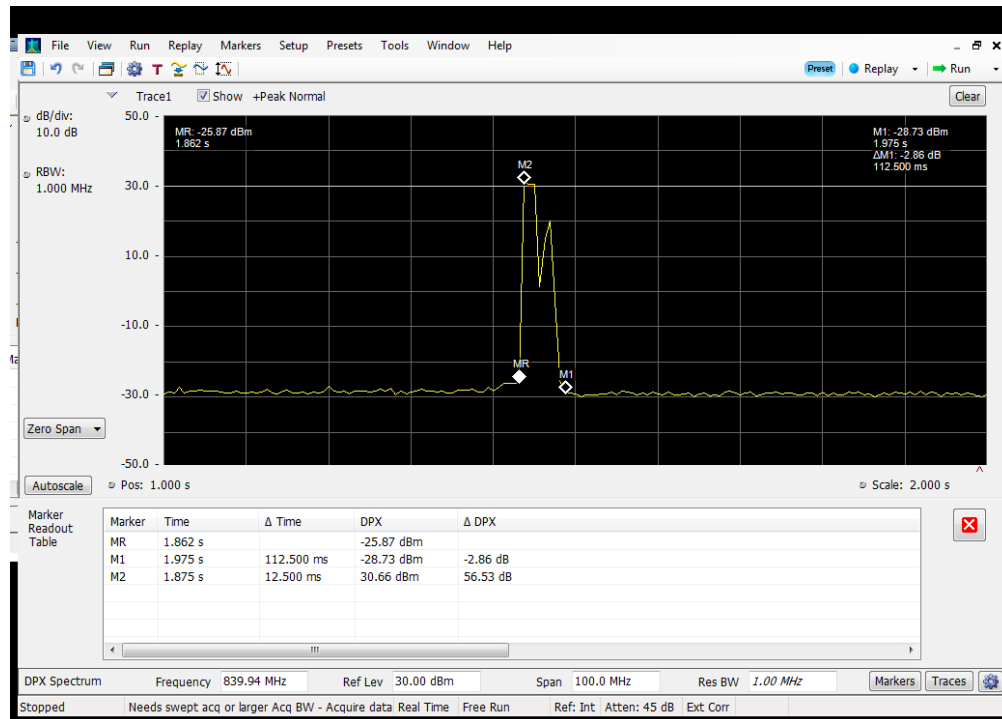


Detection Time_Uplink_776 - 787 MHz_B13_Server 4 to Dedicated Donor Port 3

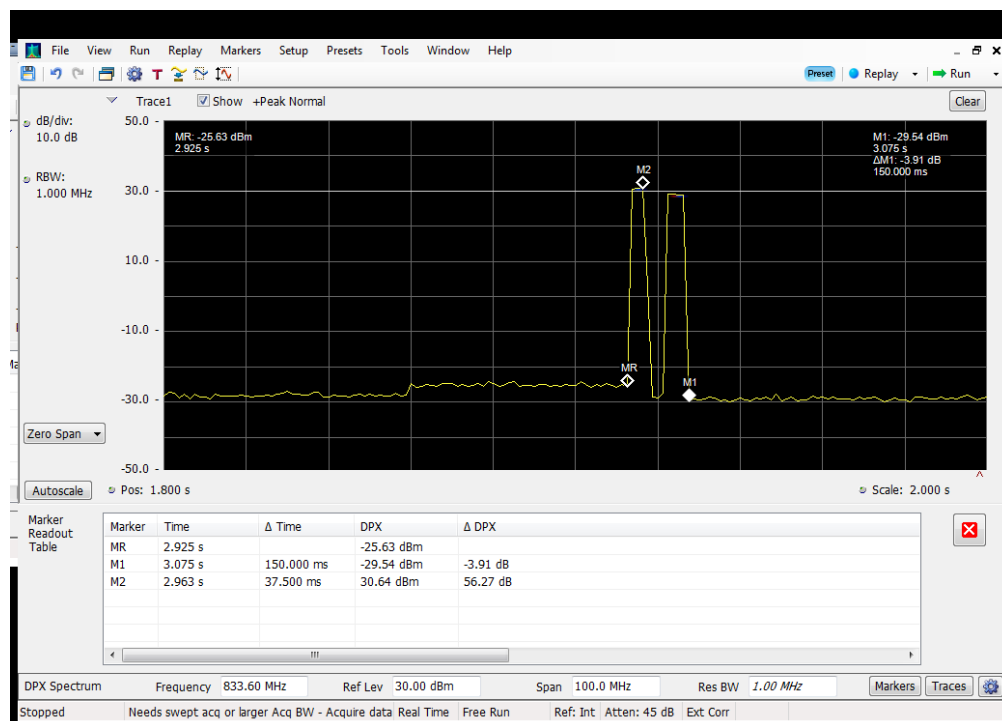




Detection Time_Uplink_824 - 849 MHz_B12_Server 3 to Common Donor Port 1

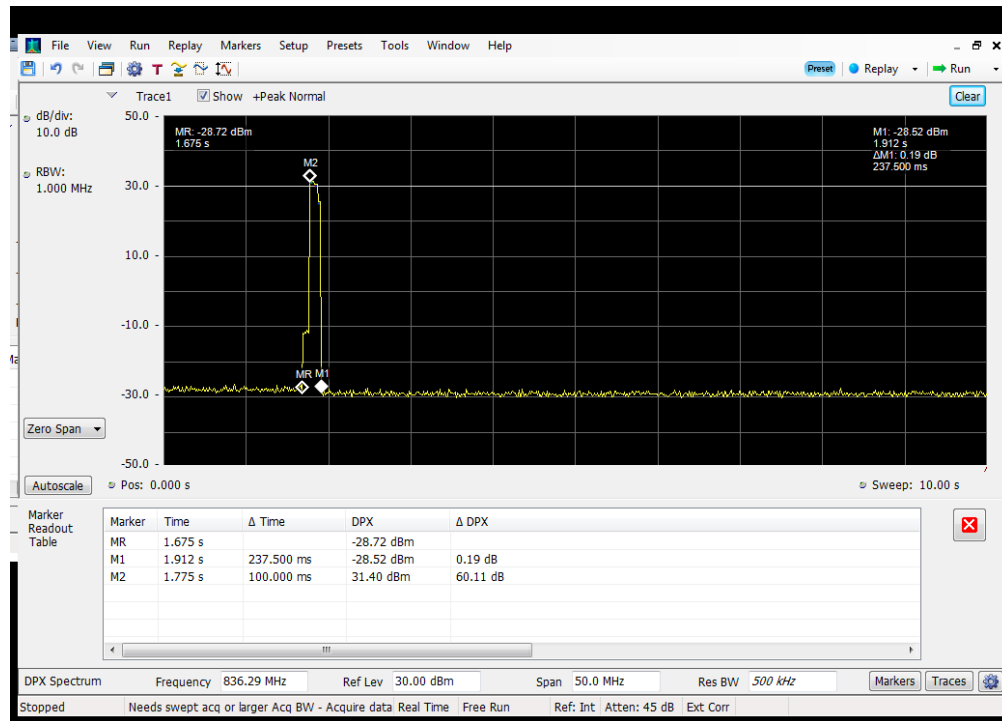


Detection Time_Uplink_824 - 849 MHz_B5_Server 1 to Common Donor Port 1

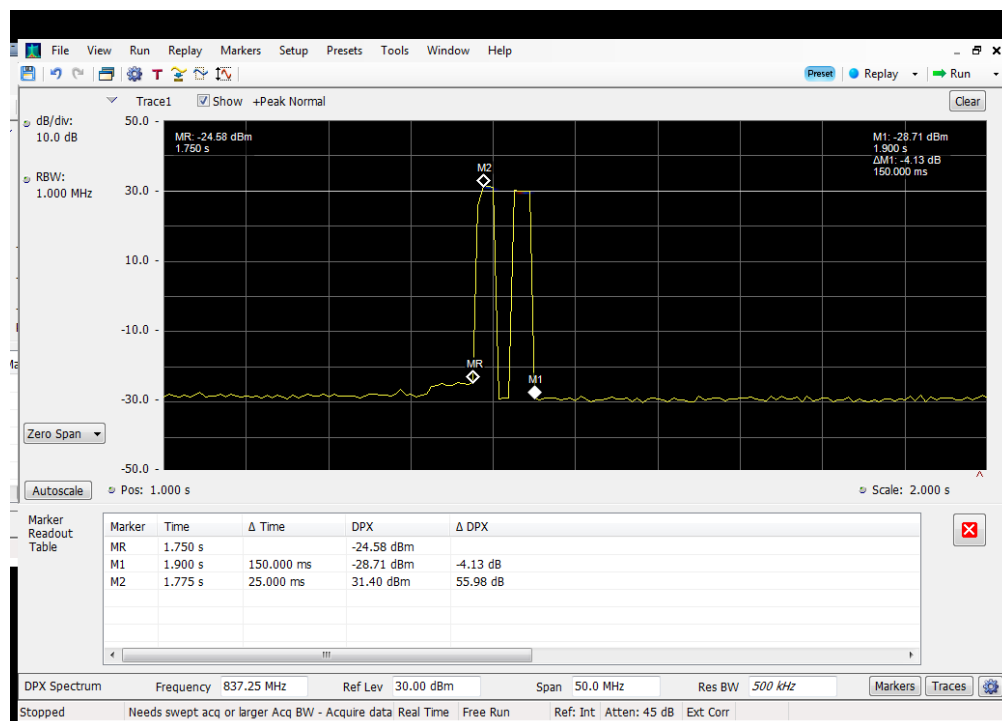




Detection Time_Uplink_824 - 849 MHz_B5_Server 1 to Dedicated Donor Port 2

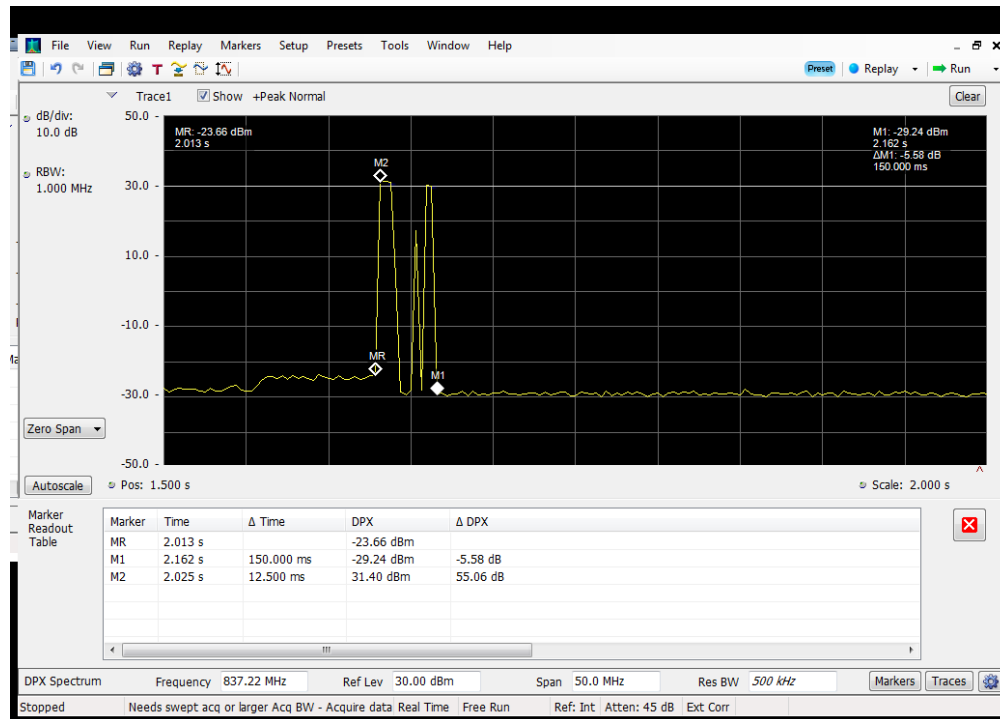


Detection Time_Uplink_824 - 849 MHz_B5_Server 2 to Dedicated Donor Port 2

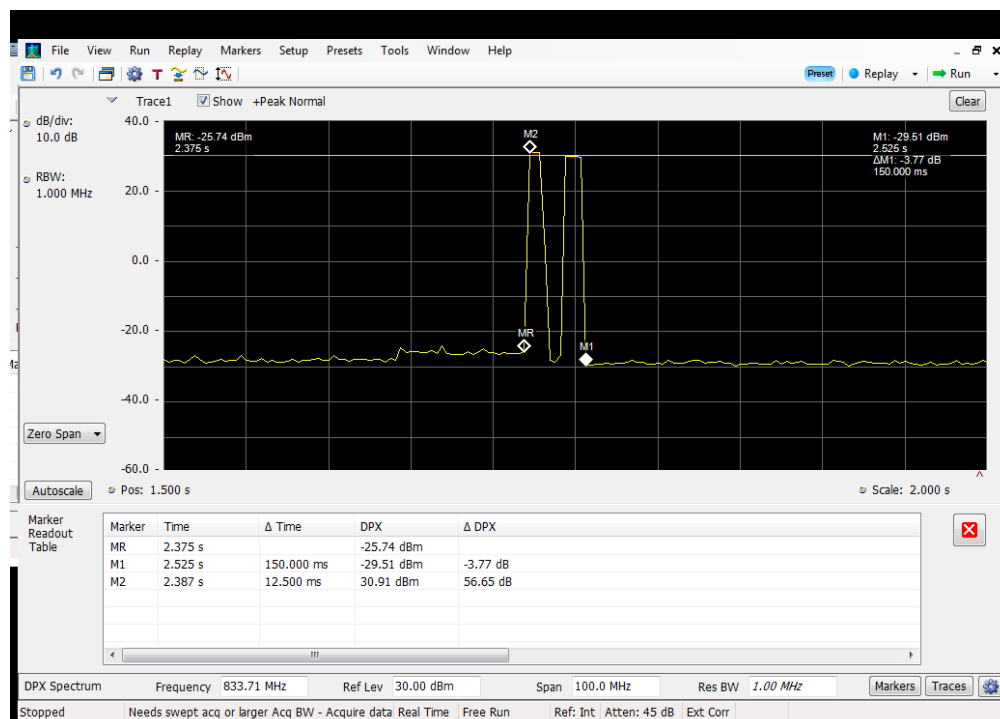




Detection Time_Uplink_824 - 849 MHz_B5_Server 3 to Dedicated Donor Port 2

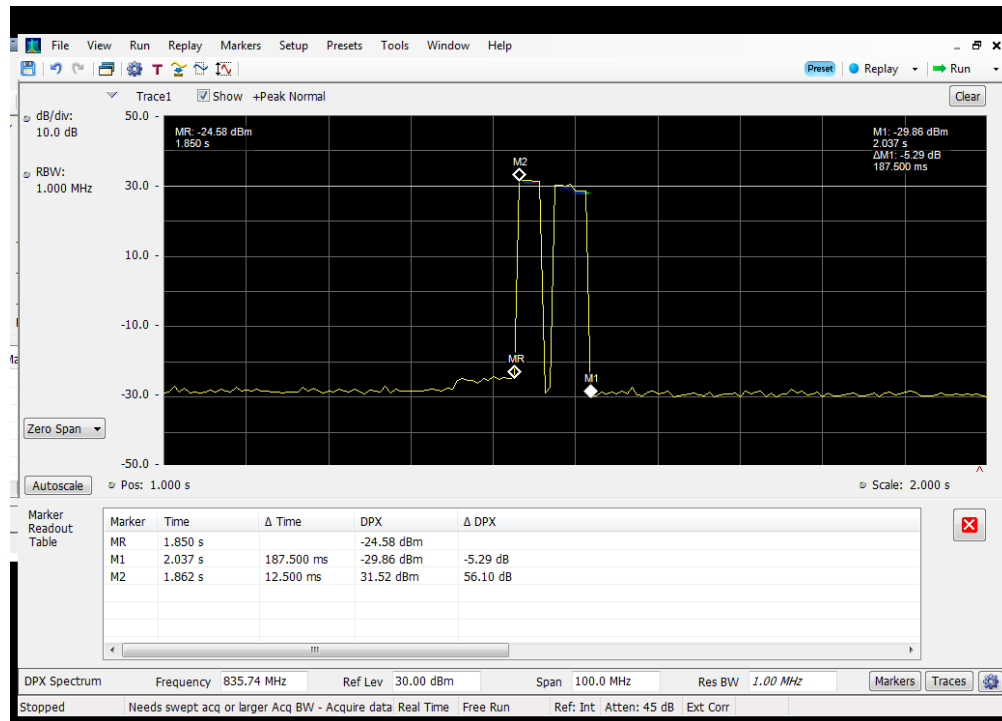


Detection Time_Uplink_824 - 849 MHz_B5_Server 4 to Common Donor Port 1

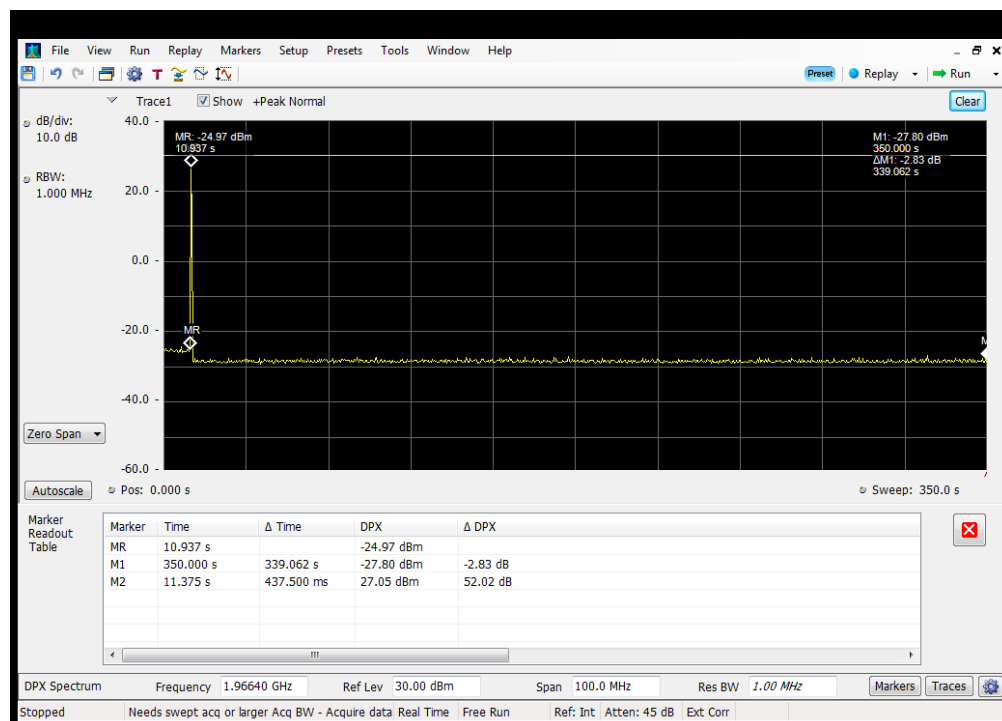




Detection Time_Uplink_824 - 849 MHz_B5_Server 4 to Dedicated Donor Port 2

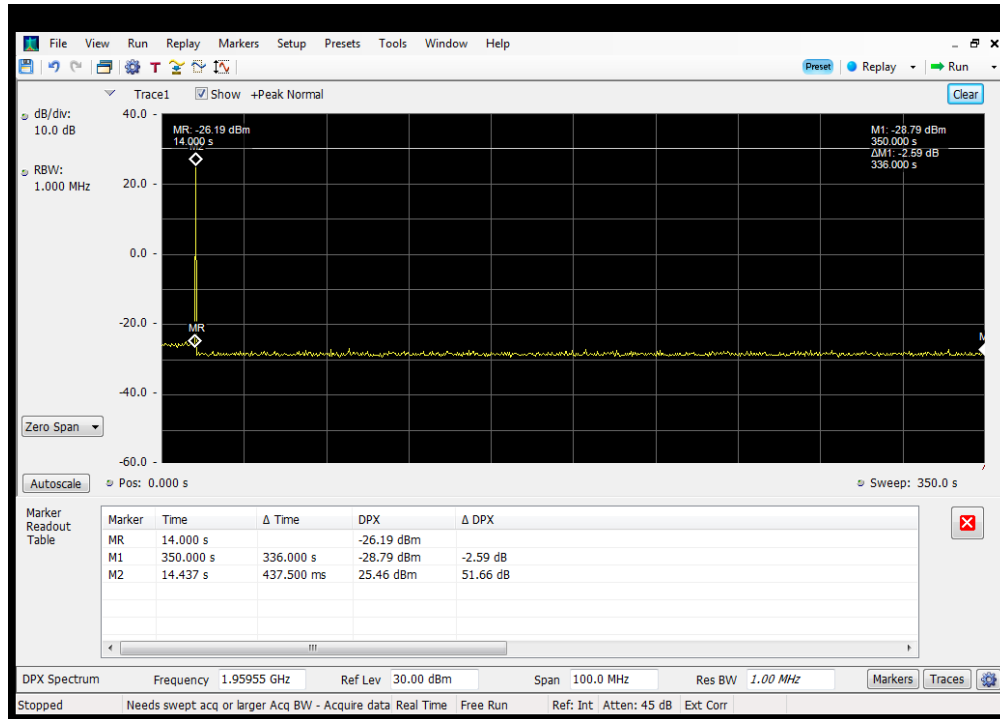


Restart Time_Downlink_1930 - 1995 MHz_B25_Server 3 to Dedicated Donor Port 1

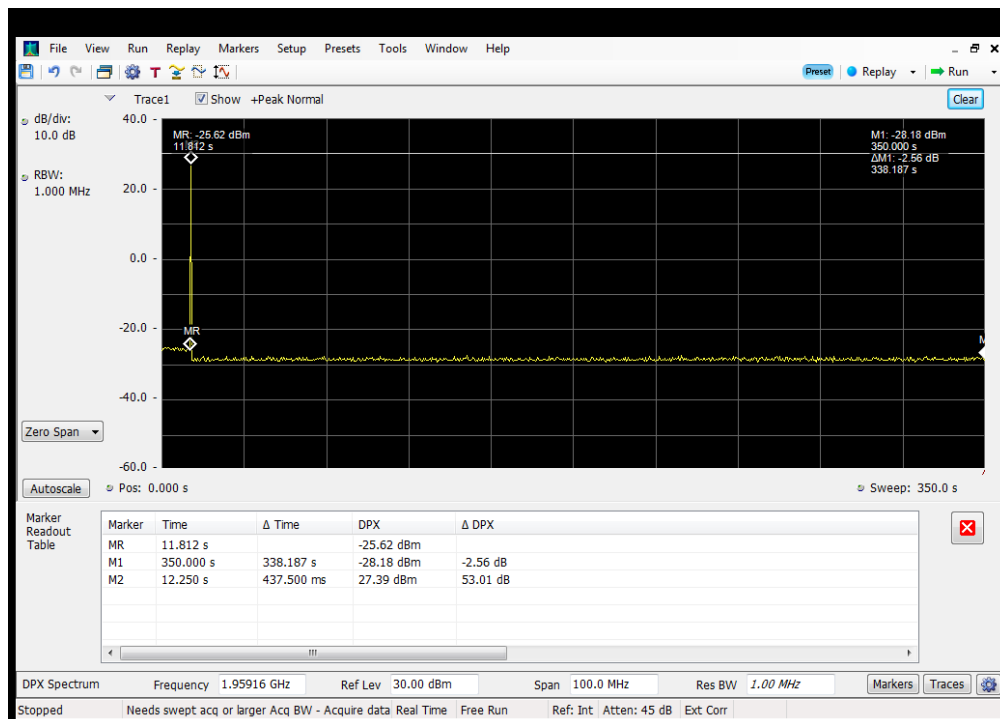




Restart Time_Downlink_1930 - 1995 MHz_B25_Server 4 to Dedicated Donor Port 1

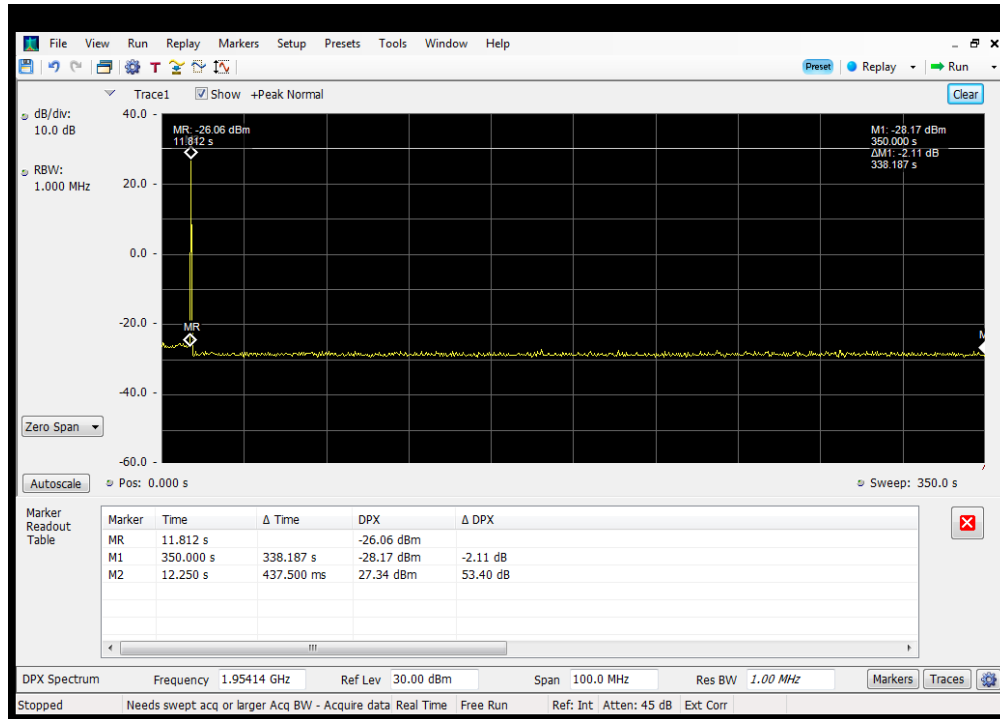


Restart Time_Downlink_1930 - 1995 MHz_B2_Server 1 to Dedicated Donor Port 1

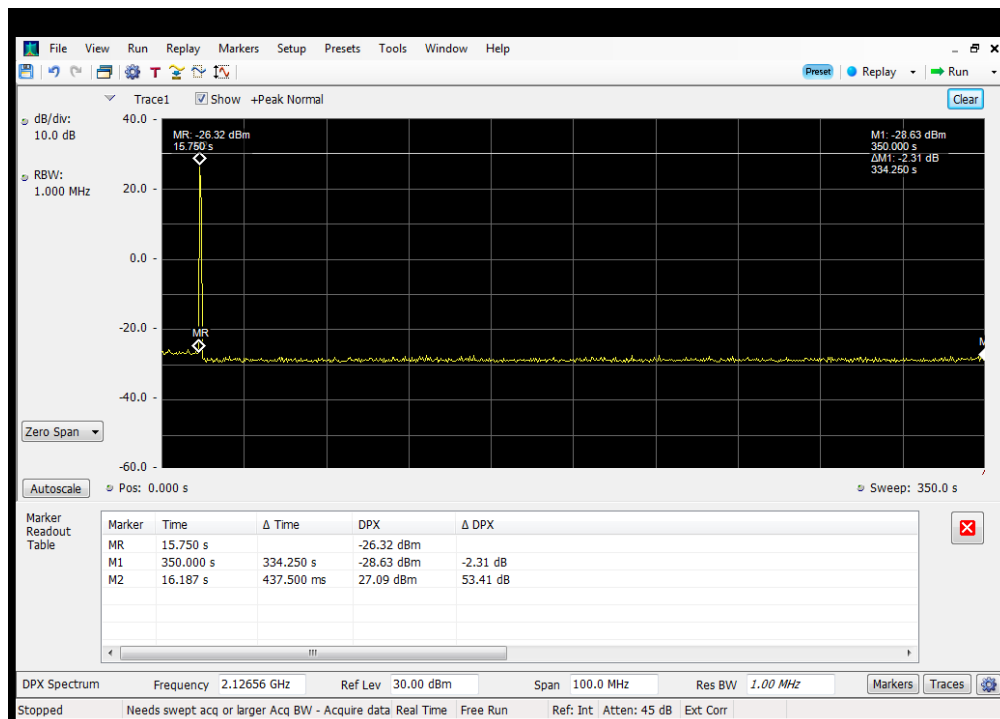




Restart Time_Downlink_1930 - 1995 MHz_B2_Server 2 to Dedicated Donor Port 1

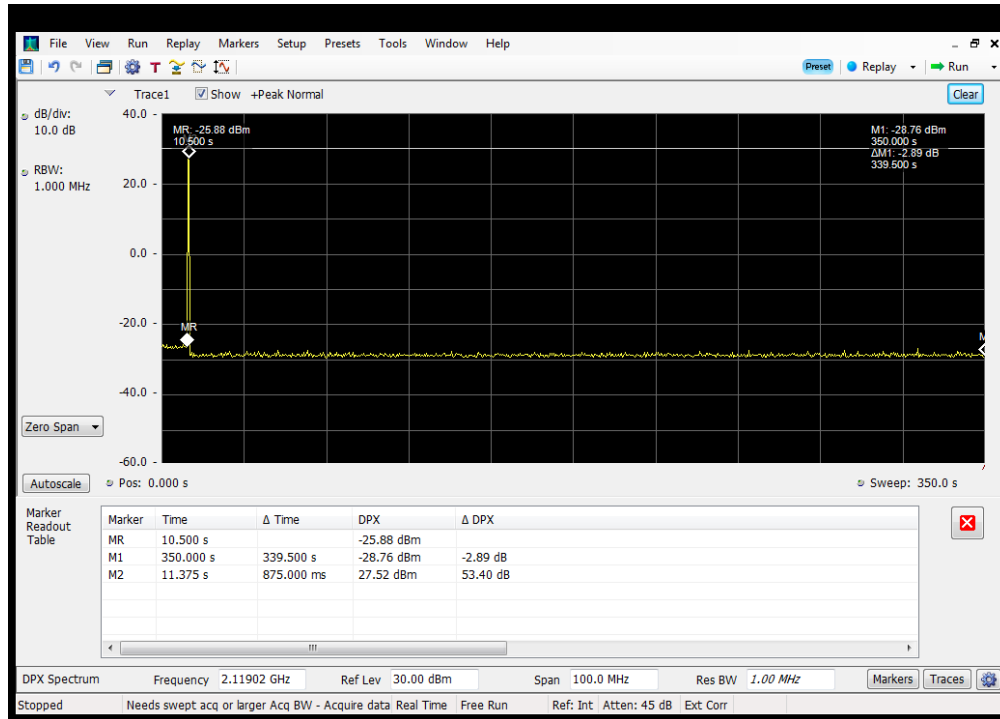


Restart Time_Downlink_2110 - 2155 MHz_B4_Server 1 to Dedicated Donor Port 1

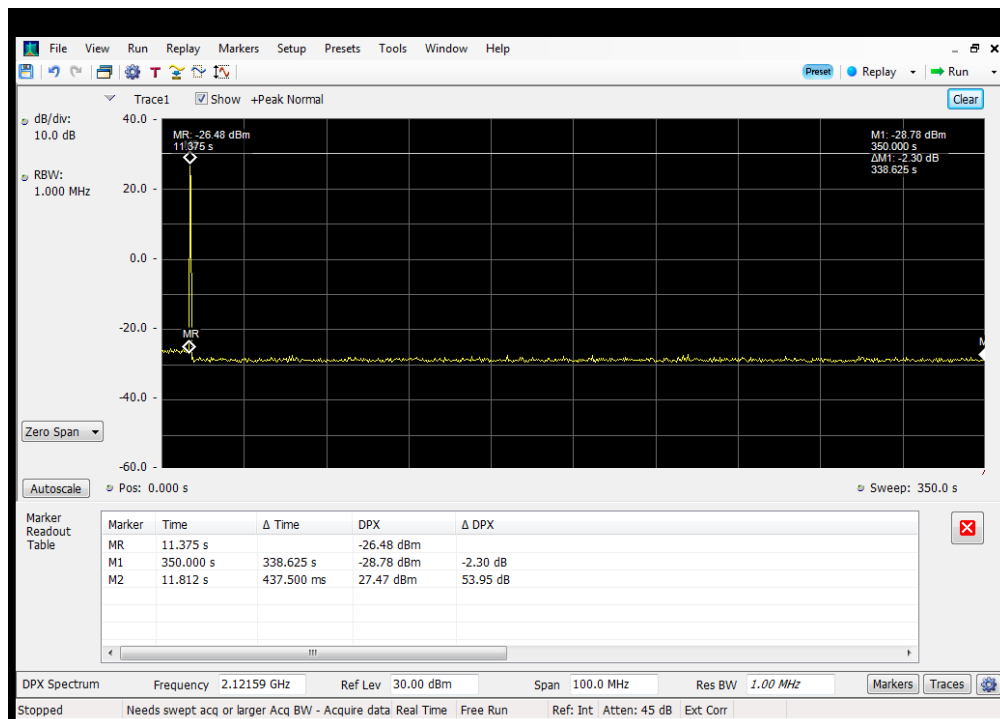




Restart Time_Downlink_2110 - 2155 MHz_B4_Server 2 to Dedicated Donor Port 1

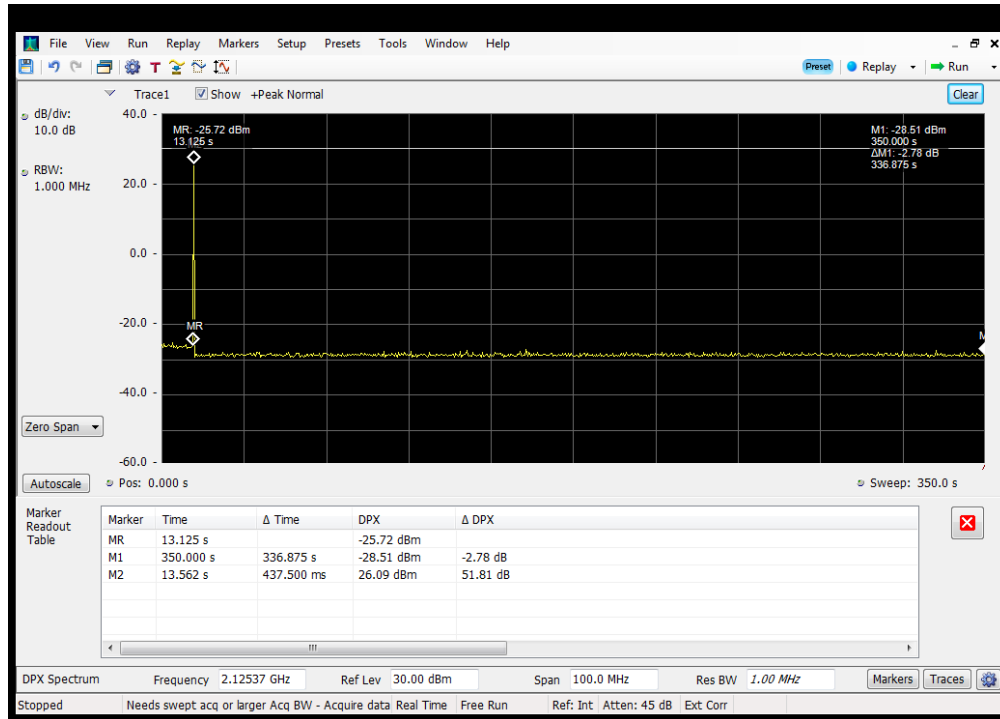


Restart Time_Downlink_2110 - 2155 MHz_B4_Server 3 to Dedicated Donor Port 1

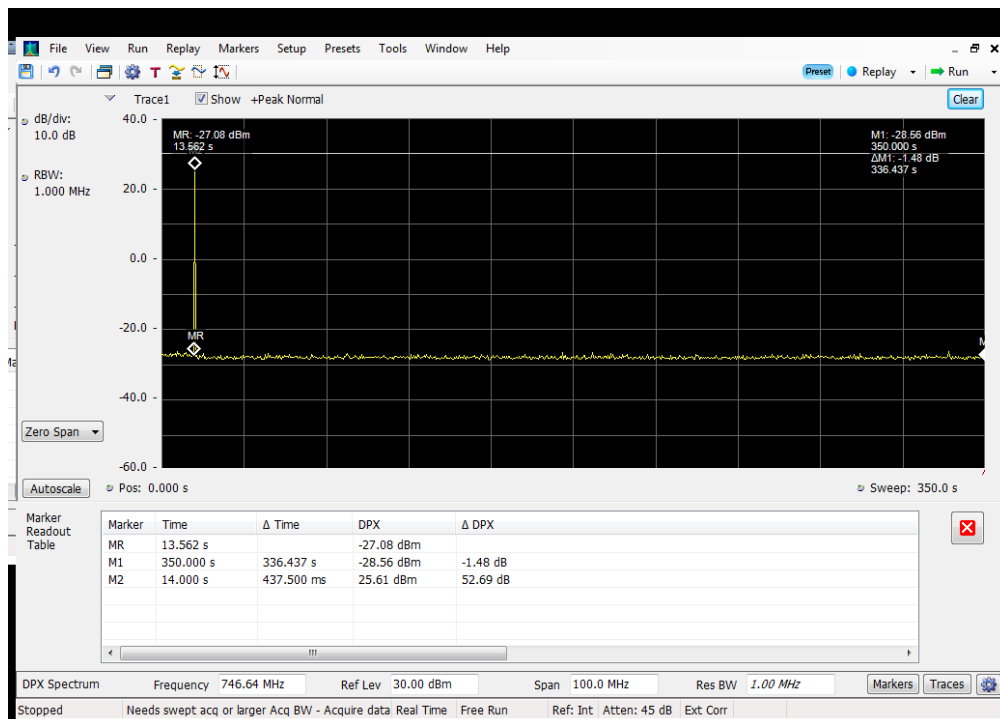




Restart Time_Downlink_2110 - 2155 MHz_B4_Server 4 to Dedicated Donor Port 1

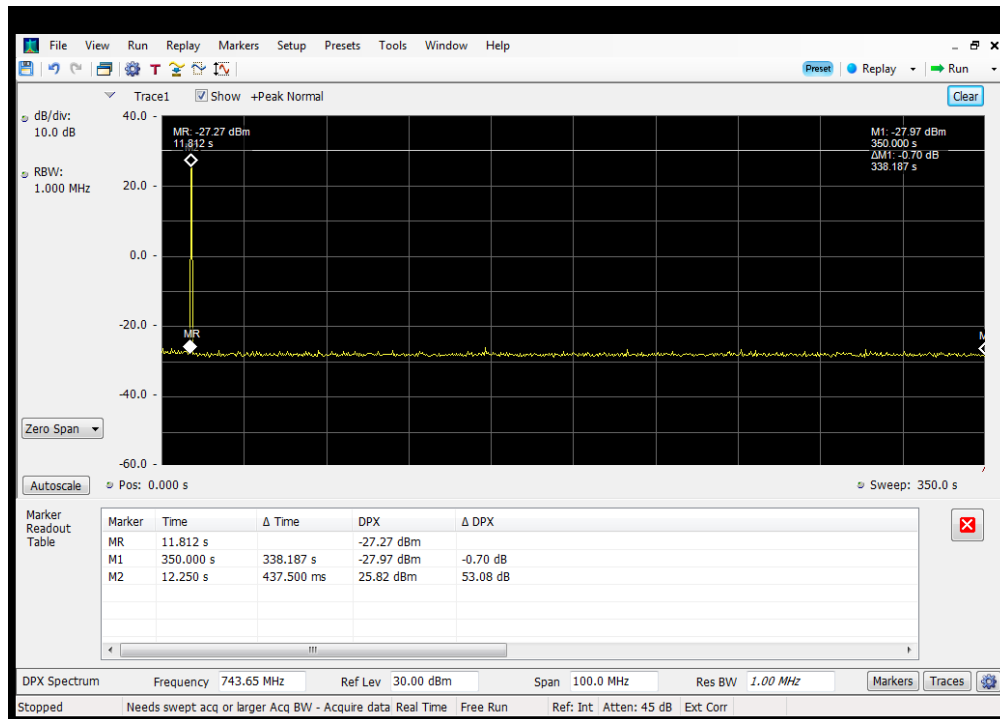


Restart Time_Downlink_728 - 746 MHz_B12_Server 1 to Common Donor Port 1

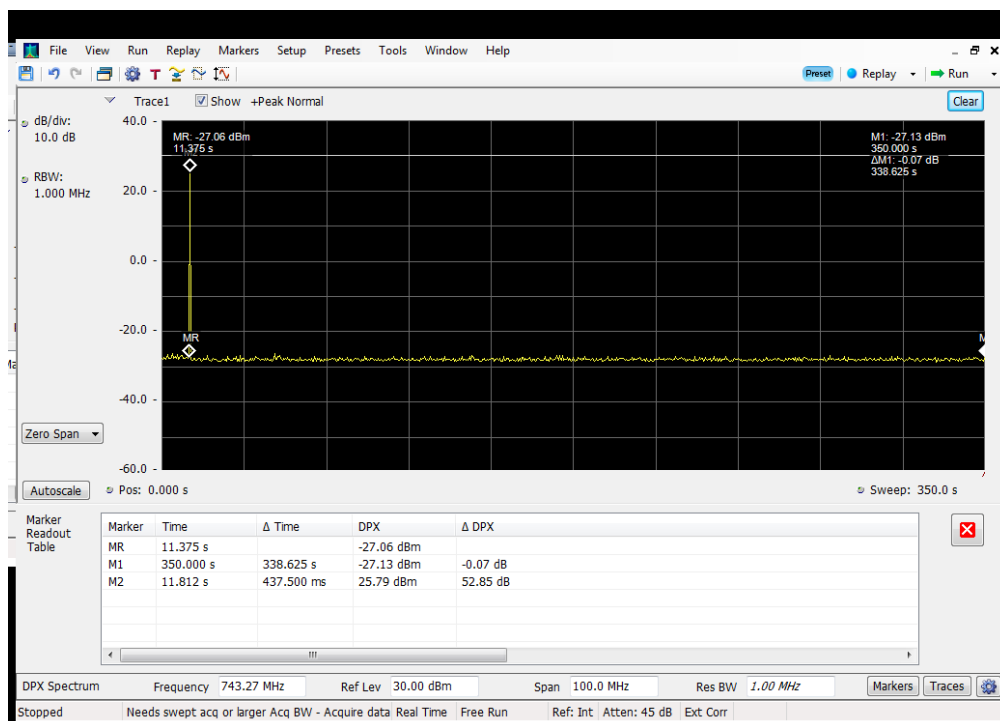




Restart Time_Downlink_728 - 746 MHz_B12_Server 1 to Dedicated Donor Port 3

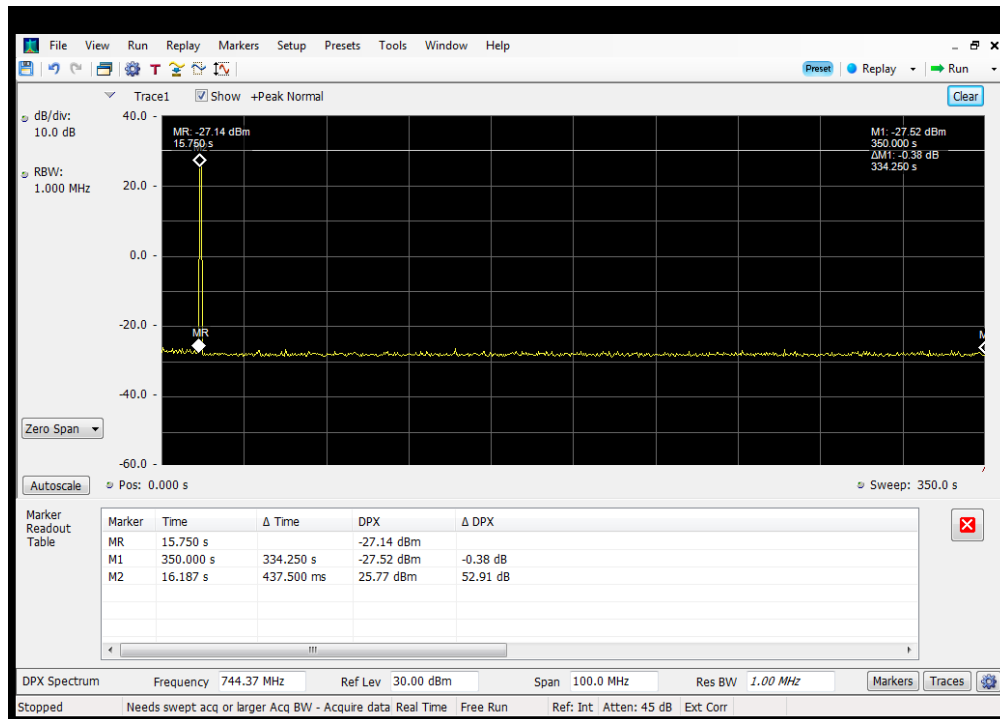


Restart Time_Downlink_728 - 746 MHz_B12_Server 2 to Common Donor Port 1

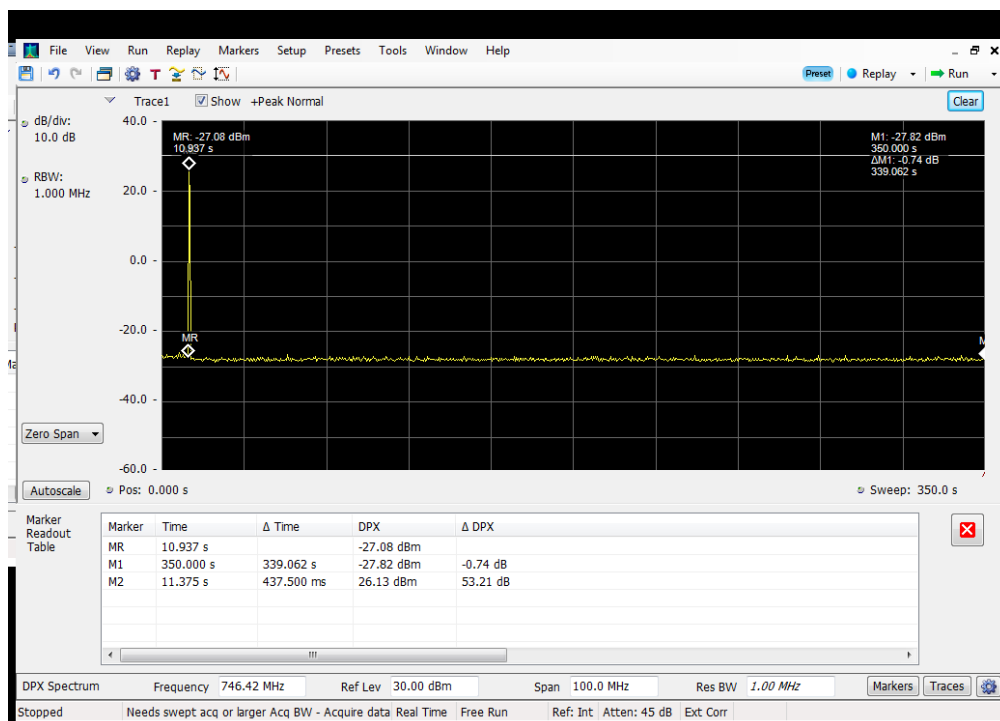




Restart Time_Downlink_728 - 746 MHz_B12_Server 2 to Dedicated Donor Port 3

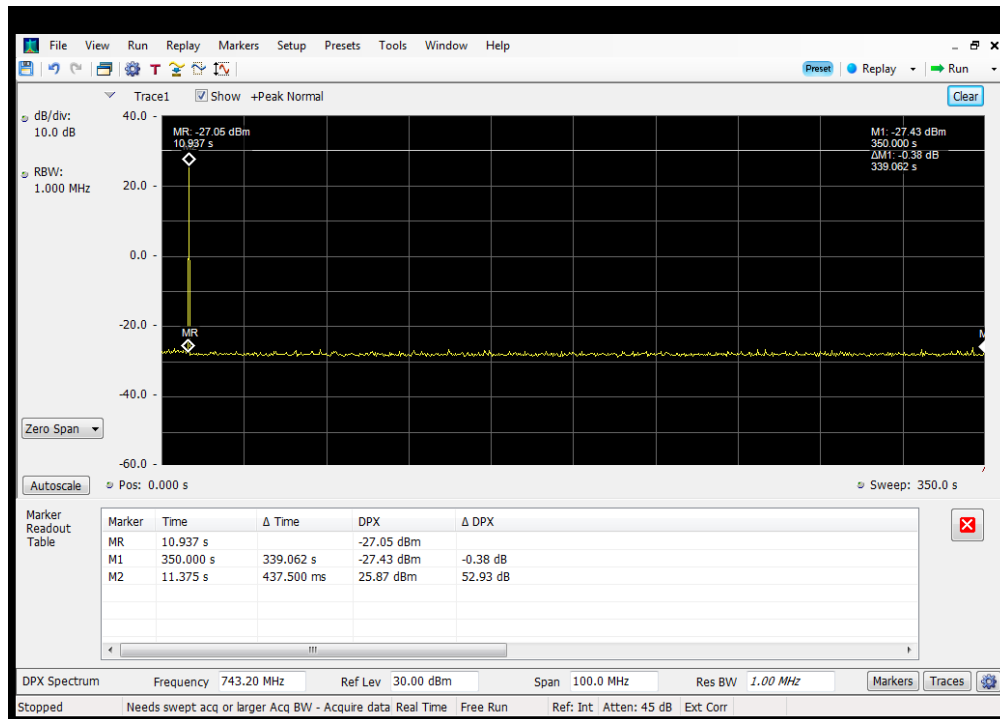


Restart Time_Downlink_728 - 746 MHz_B12_Server 3 to Common Donor Port 1

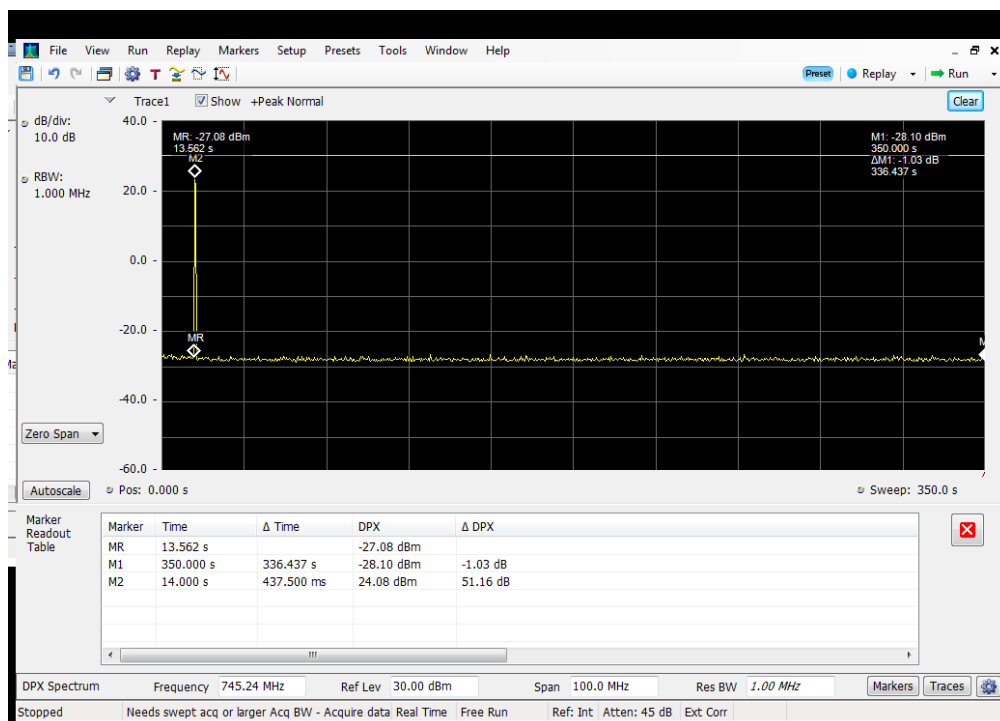




Restart Time_Downlink_728 - 746 MHz_B12_Server 3 to Dedicated Donor Port 3

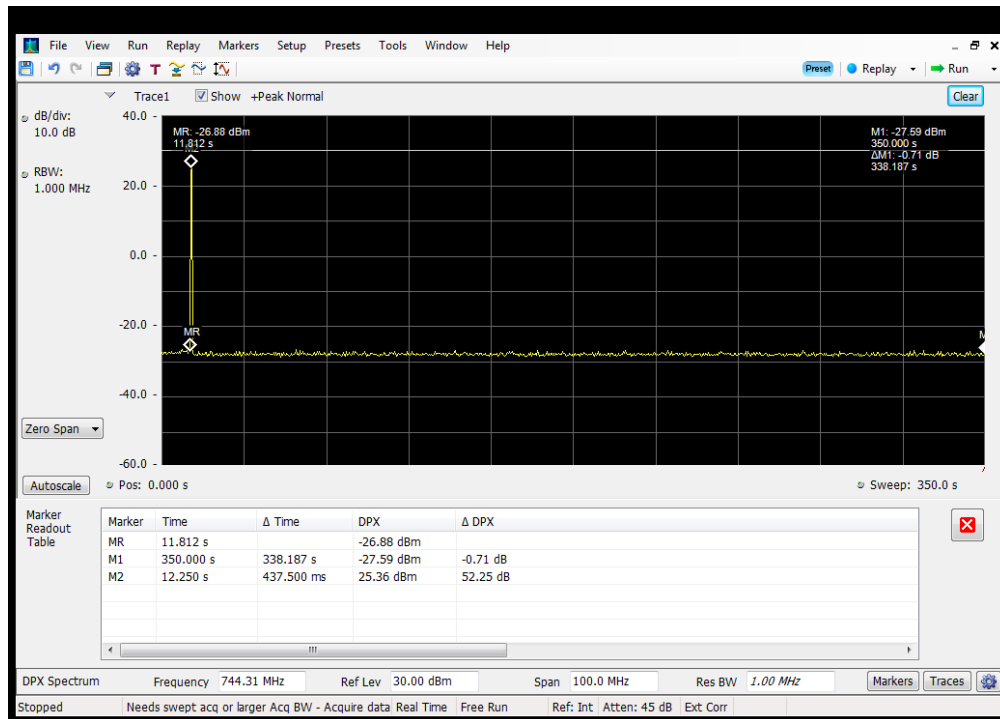


Restart Time_Downlink_728 - 746 MHz_B12_Server 4 to Common Donor Port 1

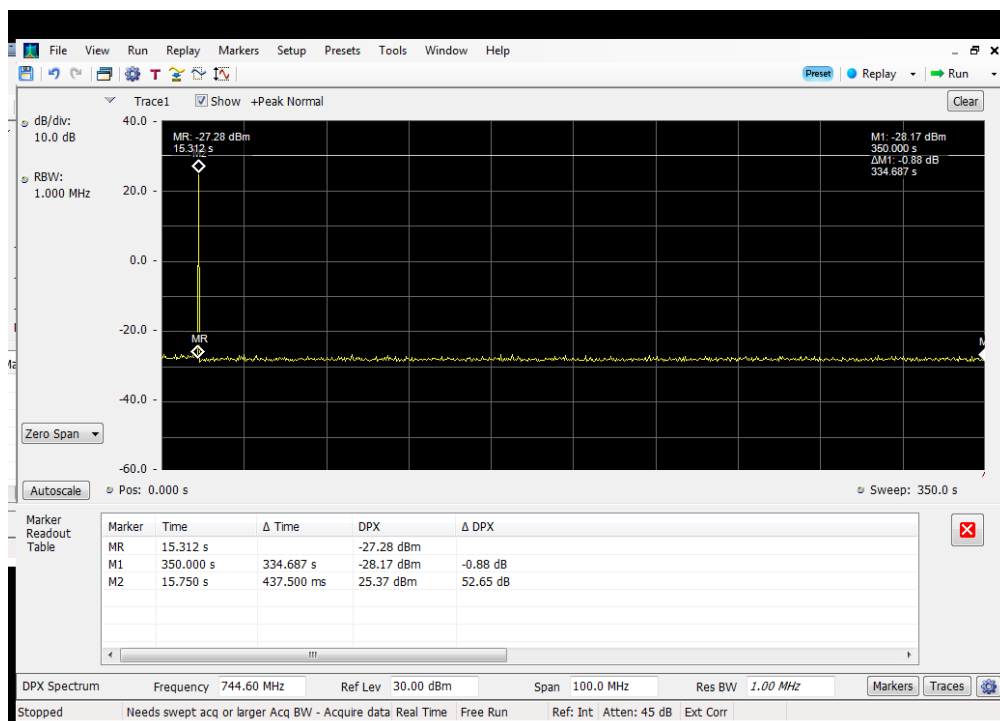




Restart Time_Downlink_728 - 746 MHz_B12_Server 4 to Dedicated Donor Port 3

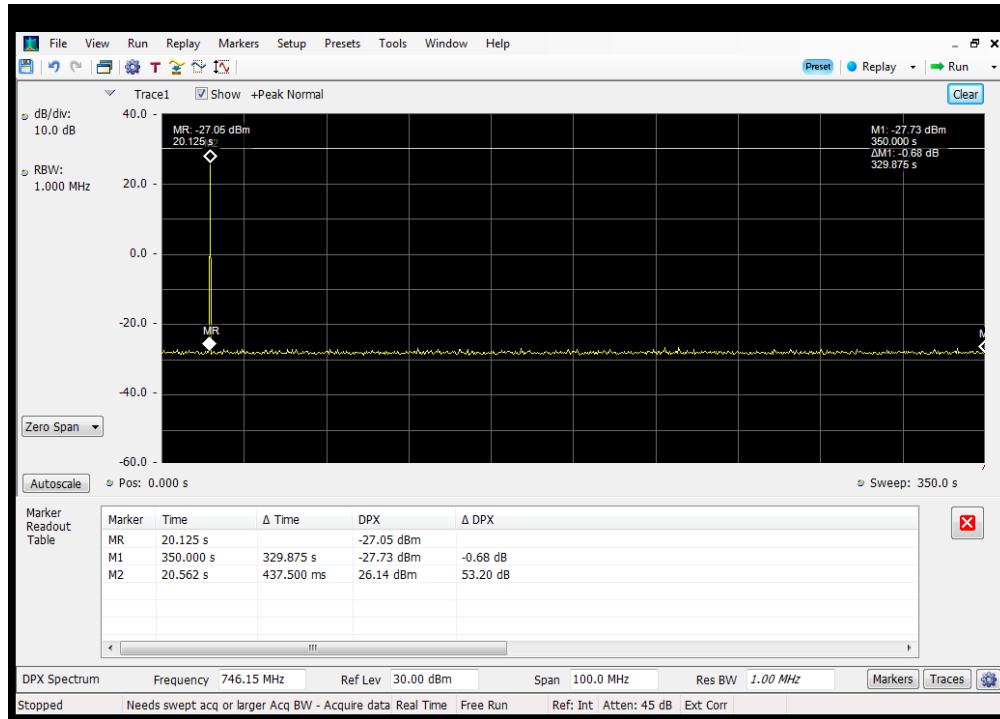


Restart Time_Downlink_746 - 757 MHz_B13_Server 1 to Common Donor Port 1

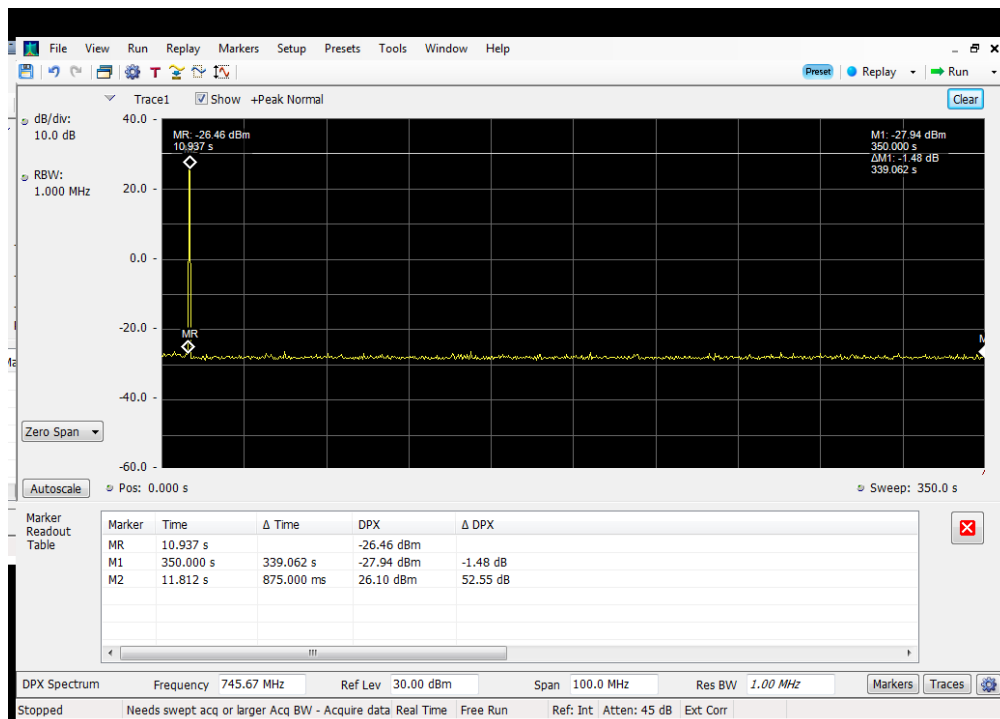




Restart Time_Downlink_746 - 757 MHz_B13_Server 1 to Dedicated Donor Port 3

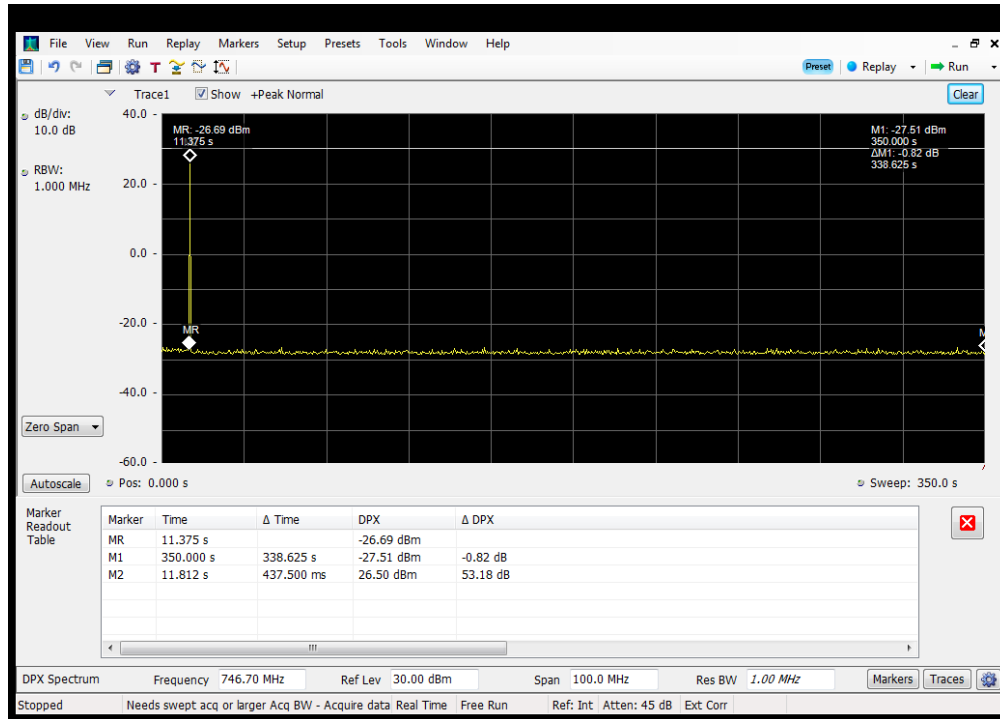


Restart Time_Downlink_746 - 757 MHz_B13_Server 2 to Common Donor Port 1

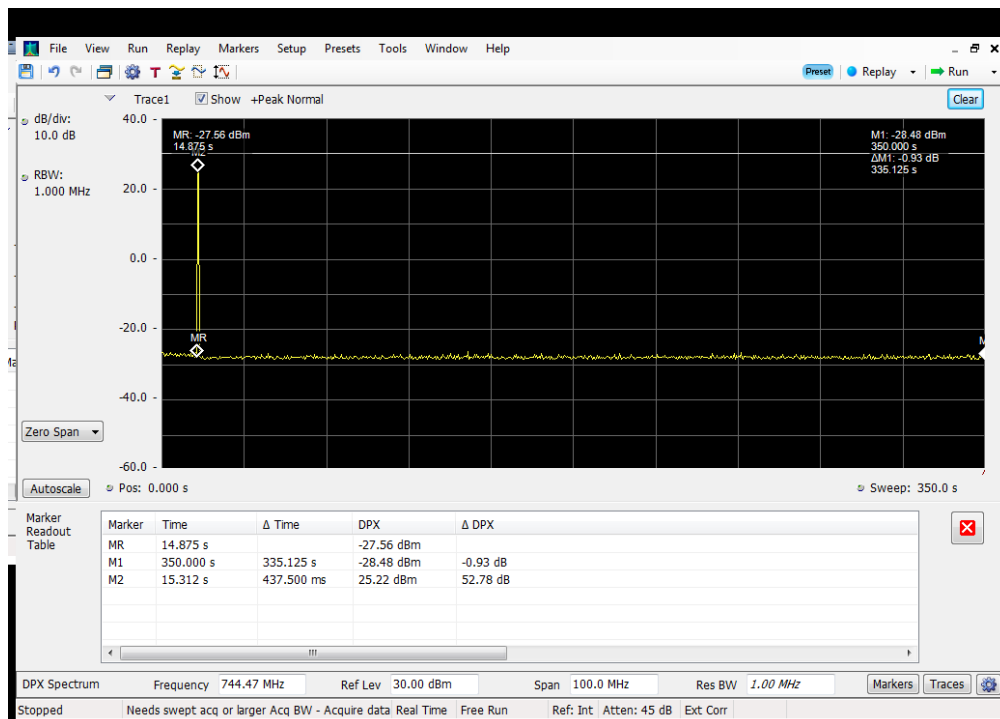




Restart Time_Downlink_746 - 757 MHz_B13_Server 2 to Dedicated Donor Port 3

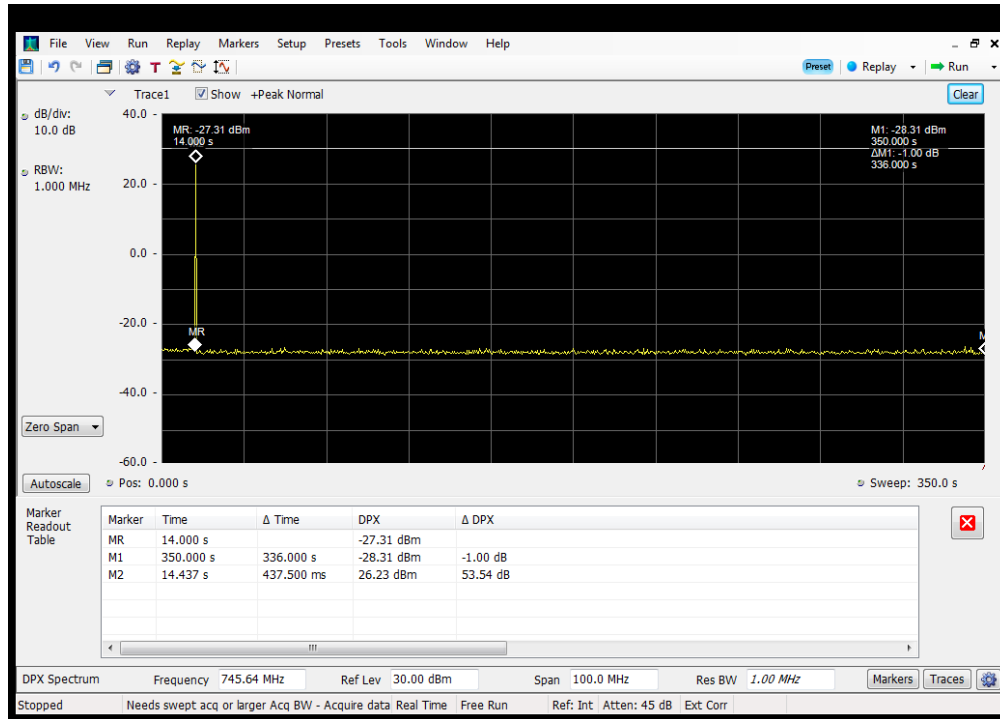


Restart Time_Downlink_746 - 757 MHz_B13_Server 3 to Common Donor Port 1

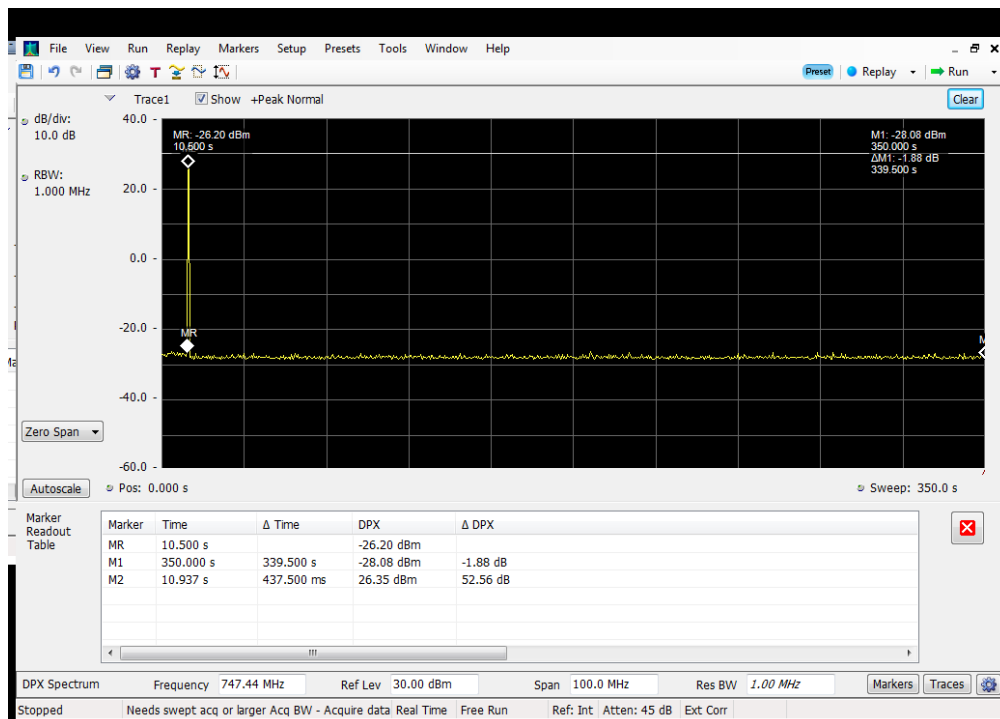




Restart Time_Downlink_746 - 757 MHz_B13_Server 3 to Dedicated Donor Port 3

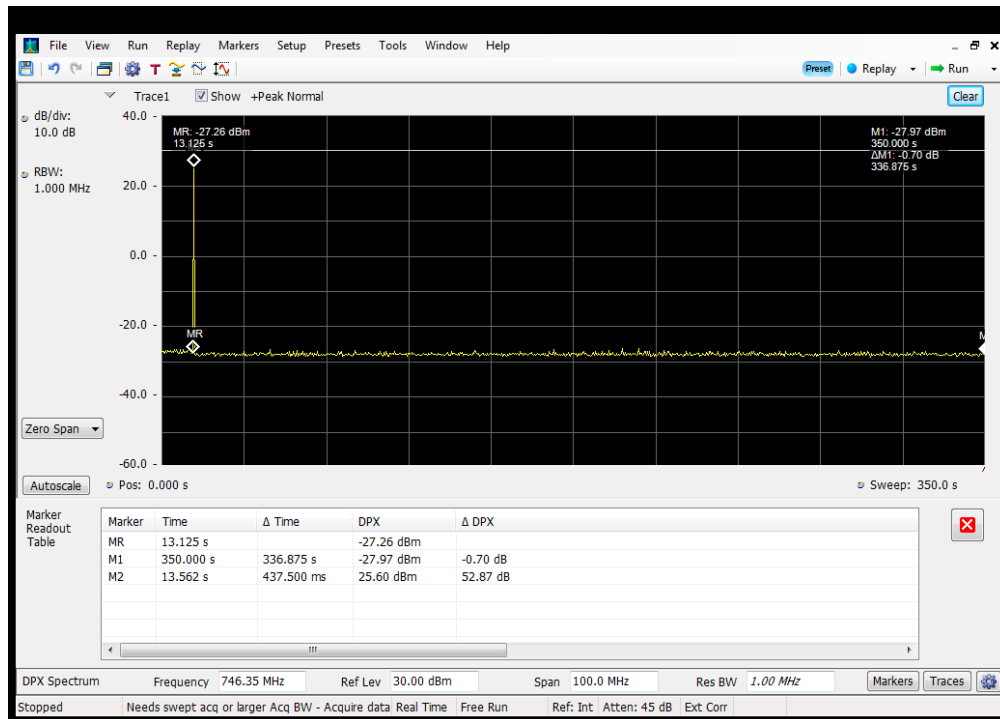


Restart Time_Downlink_746 - 757 MHz_B13_Server 4 to Common Donor Port 1

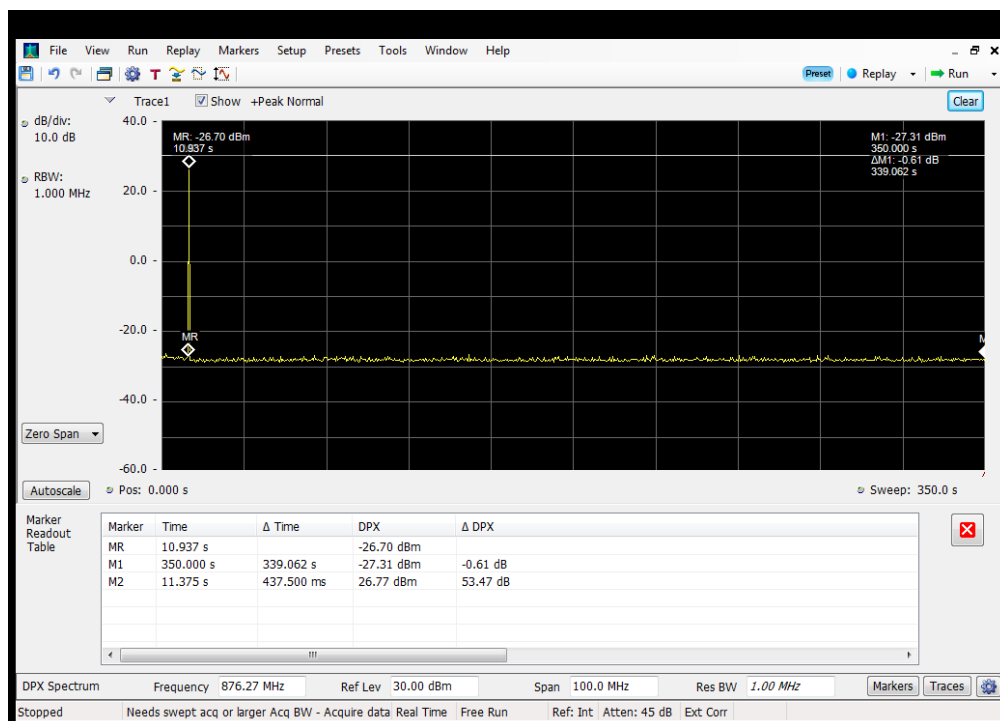




Restart Time_Downlink_746 - 757 MHz_B13_Server 4 to Dedicated Donor Port 3

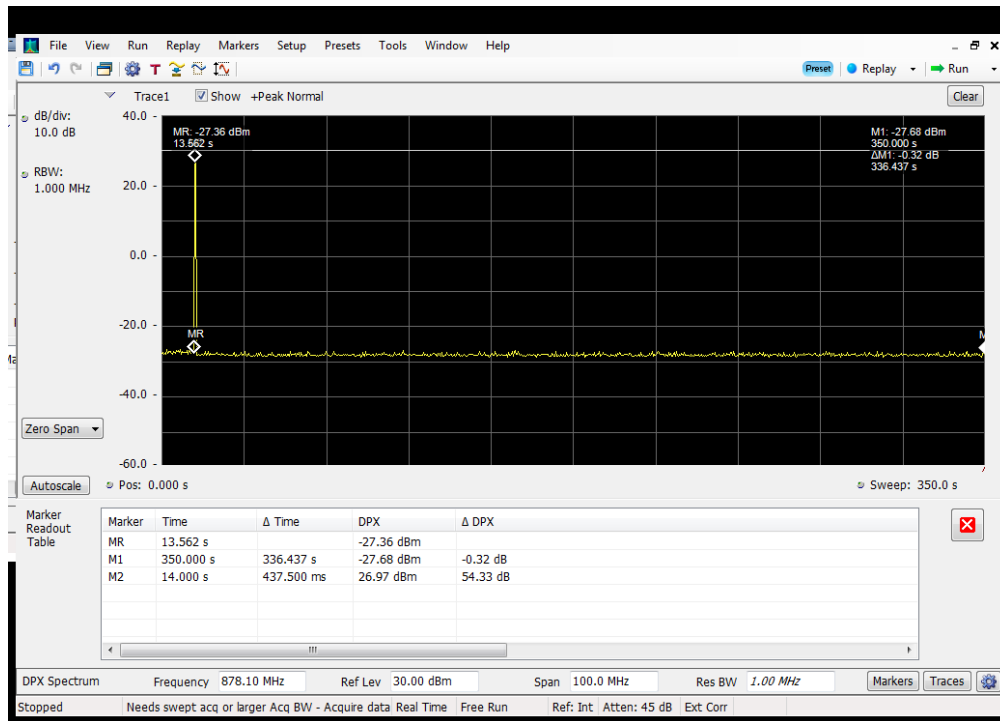


Restart Time_Downlink_869 - 894 MHz_B4_Server 2 to Dedicated Donor Port 3

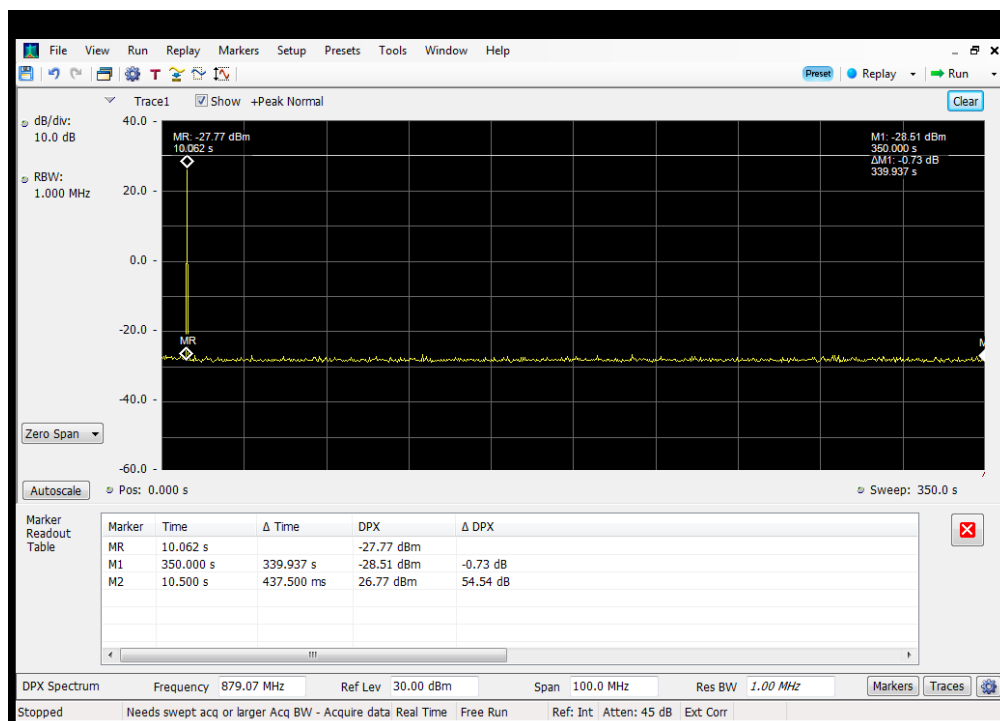




Restart Time_Downlink_869 - 894 MHz_B5_Server 1 to Common Donor Port 1

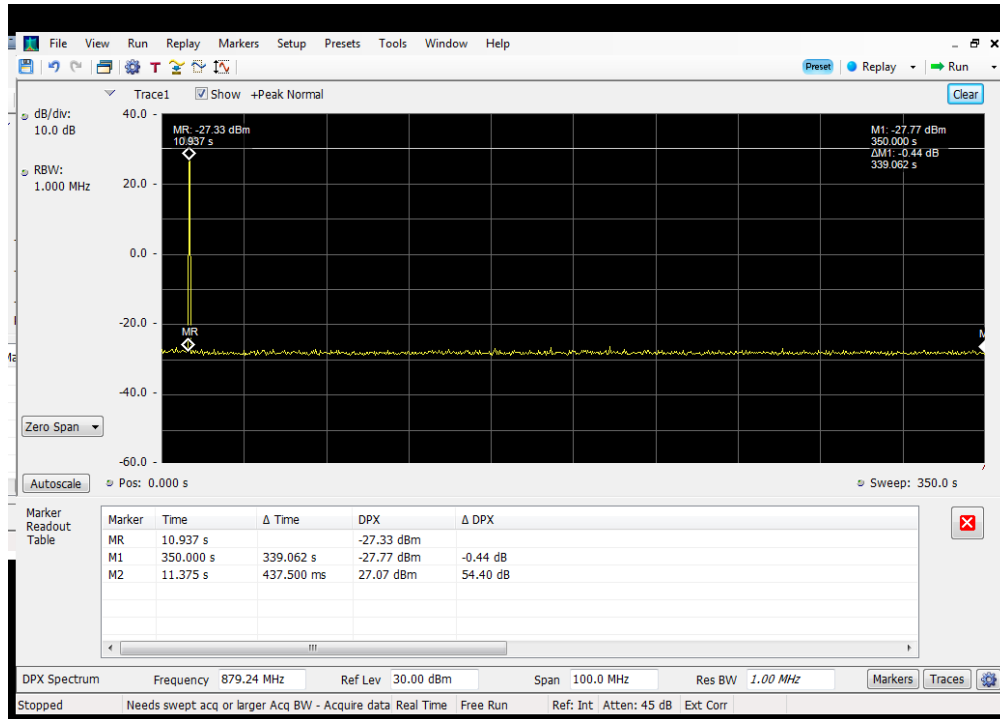


Restart Time_Downlink_869 - 894 MHz_B5_Server 1 to Dedicated Donor Port 2

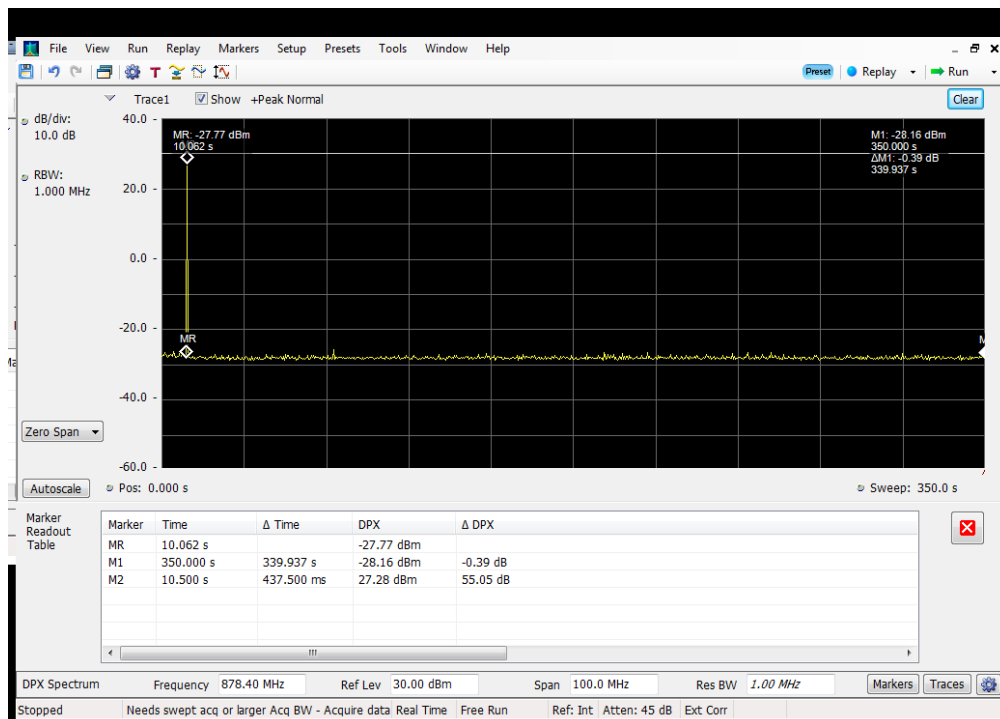




Restart Time_Downlink_869 - 894 MHz_B5_Server 2 to Common Donor Port 1

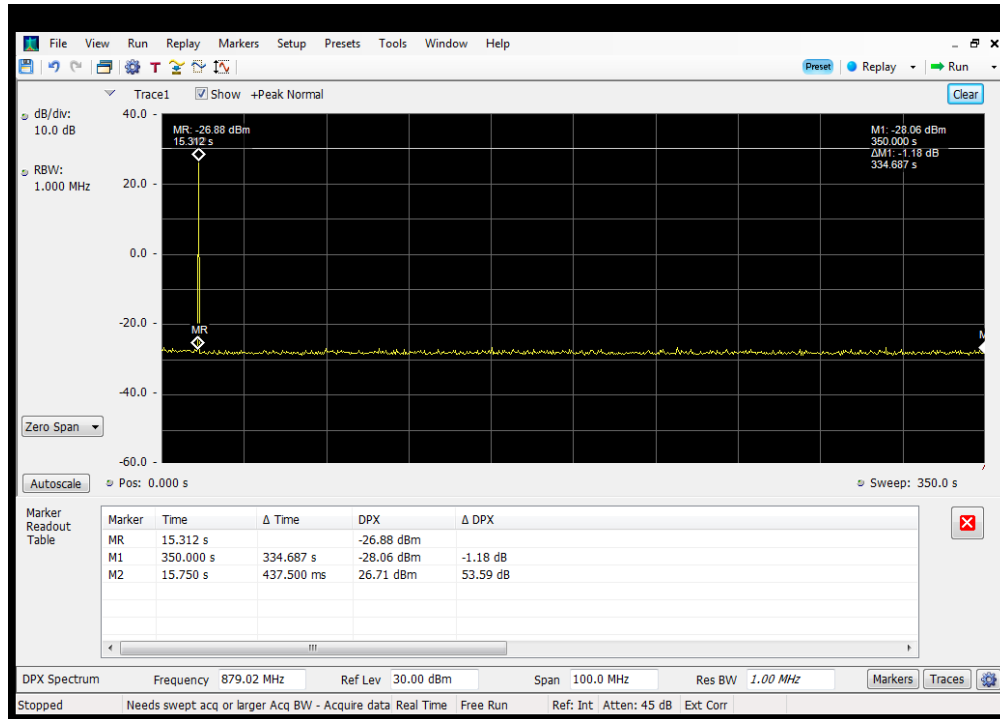


Restart Time_Downlink_869 - 894 MHz_B5_Server 3 to Common Donor Port 1

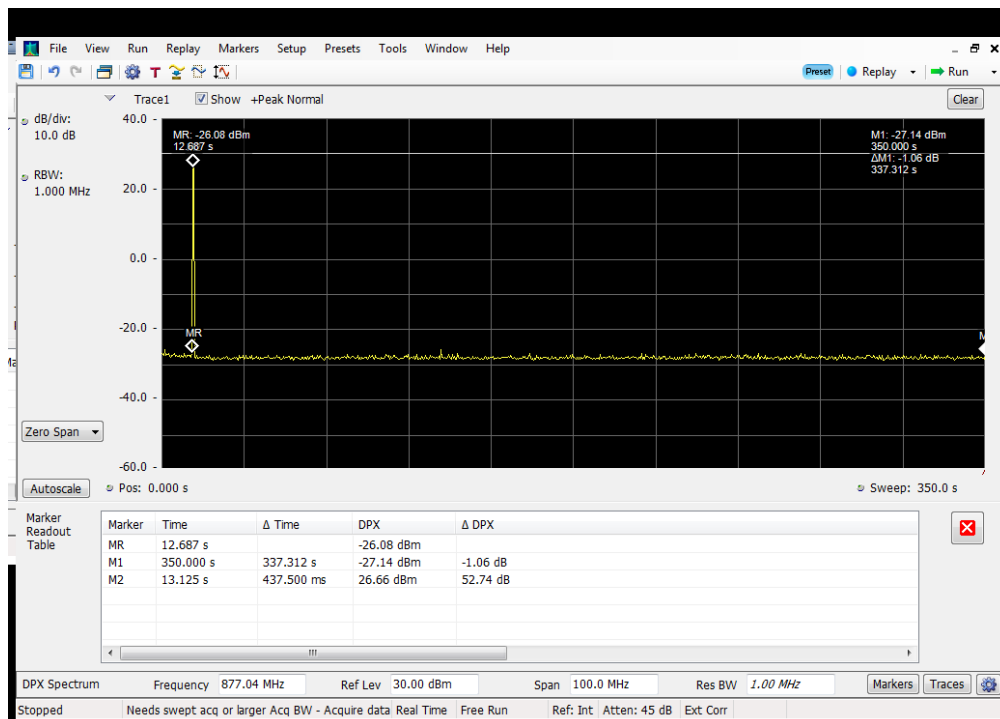




Restart Time_Downlink_869 - 894 MHz_B5_Server 3 to Dedicated Donor Port 2

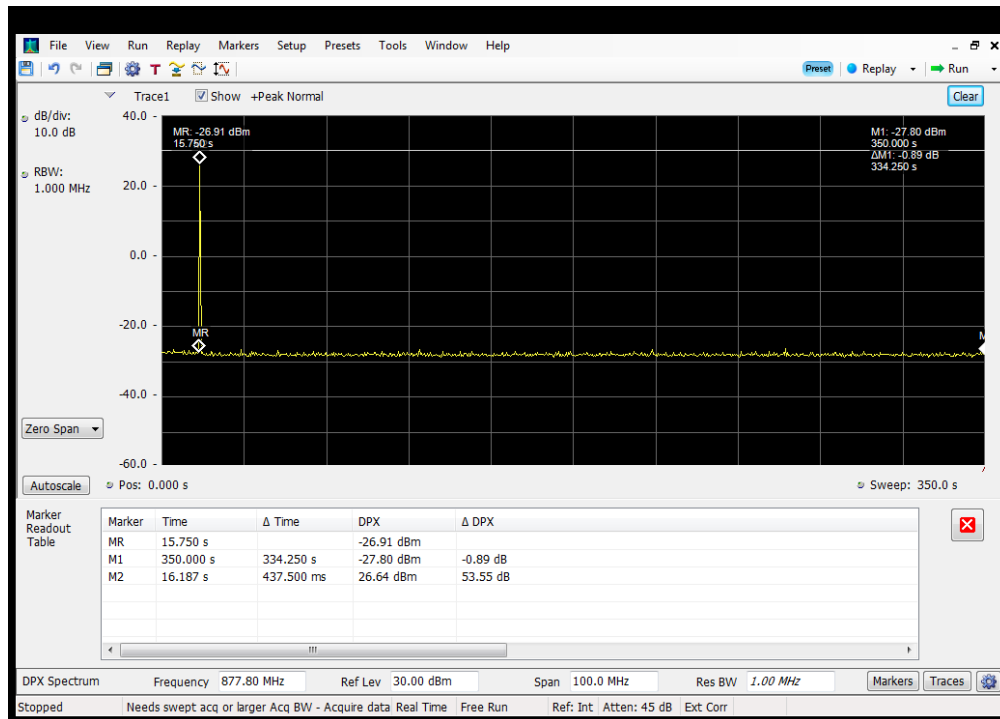


Restart Time_Downlink_869 - 894 MHz_B5_Server 4 to Common Donor Port 1





Restart Time_Downlink_869 - 894 MHz_B5_Server 4 to Dedicated Donor Port 2

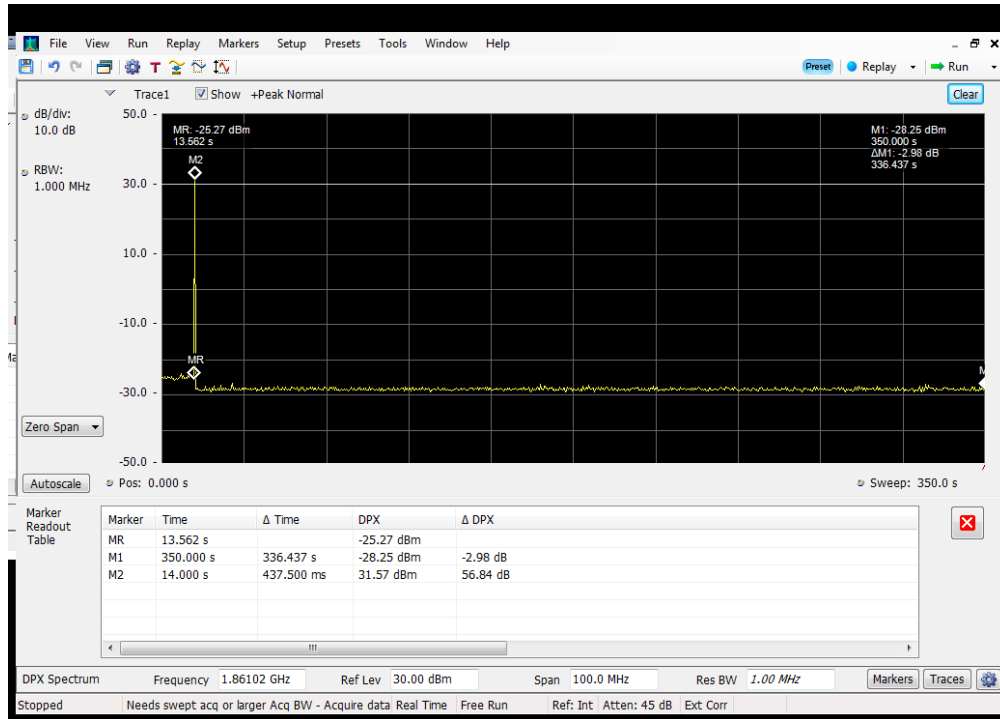


Restart Time_Uplink_1710 - 1755 MHz_B4_Server 4 to Dedicated Donor Port 1

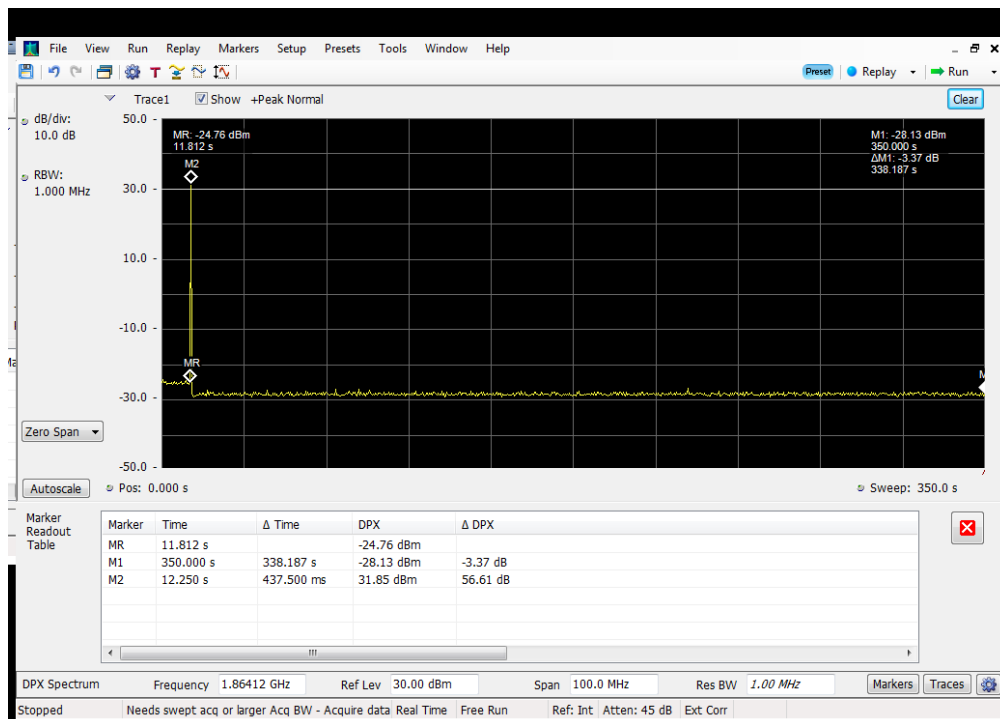




Restart Time_Uplink_1850 - 1915 MHz_B25_Server 3 to Dedicated Donor Port 1

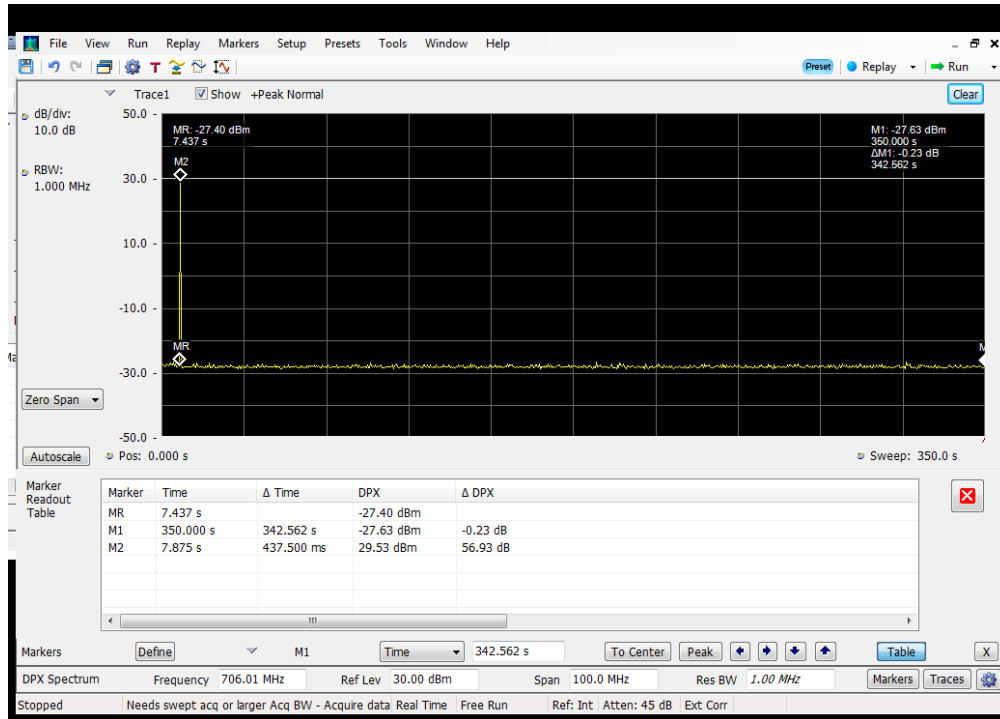


Restart Time_Uplink_1850 - 1915 MHz_B25_Server 4 to Dedicated Donor Port 1

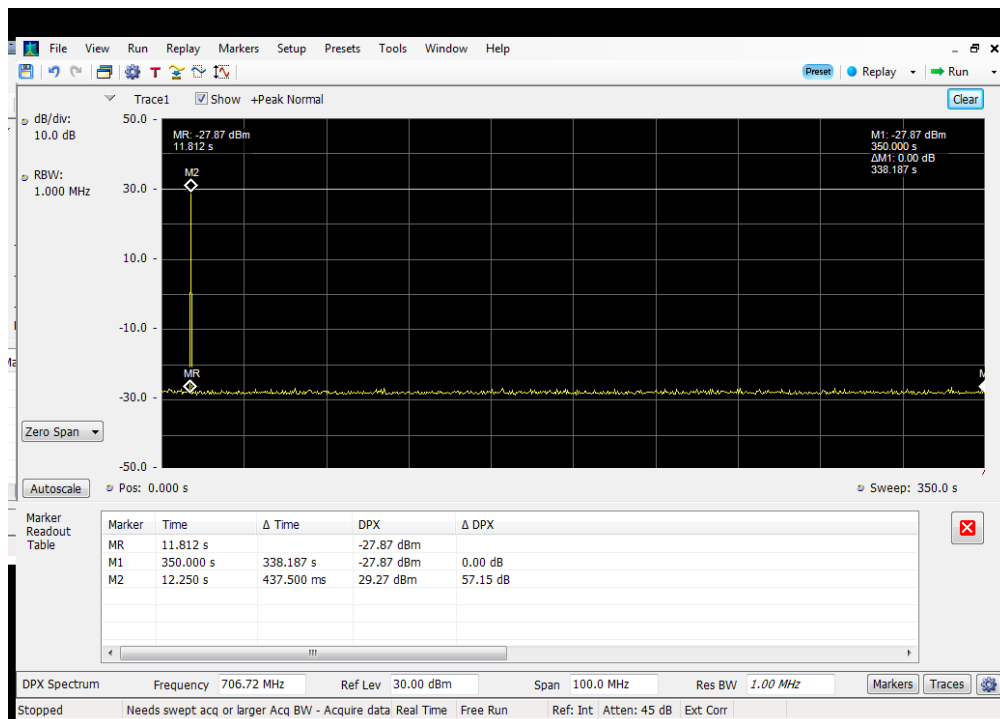




Restart Time_Uplink_698 - 716 MHz_B12_Server 1 to Common Donor Port 1

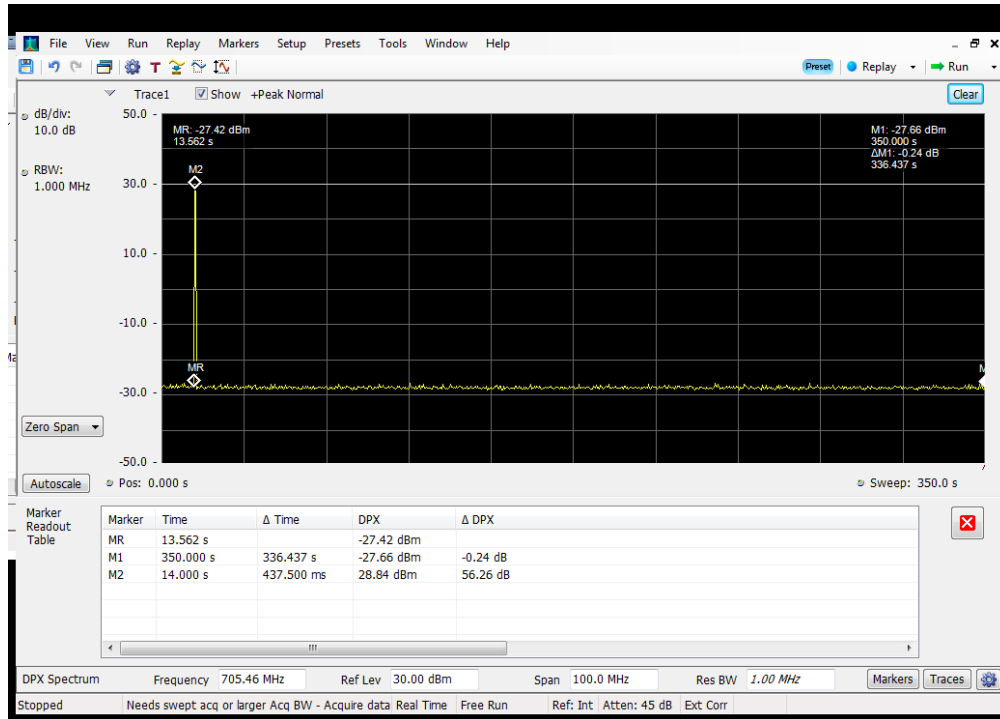


Restart Time_Uplink_698 - 716 MHz_B12_Server 3 to Common Donor Port 1





Restart Time_Uplink_698 - 716 MHz_B12_Server 4 to Common Donor Port 1

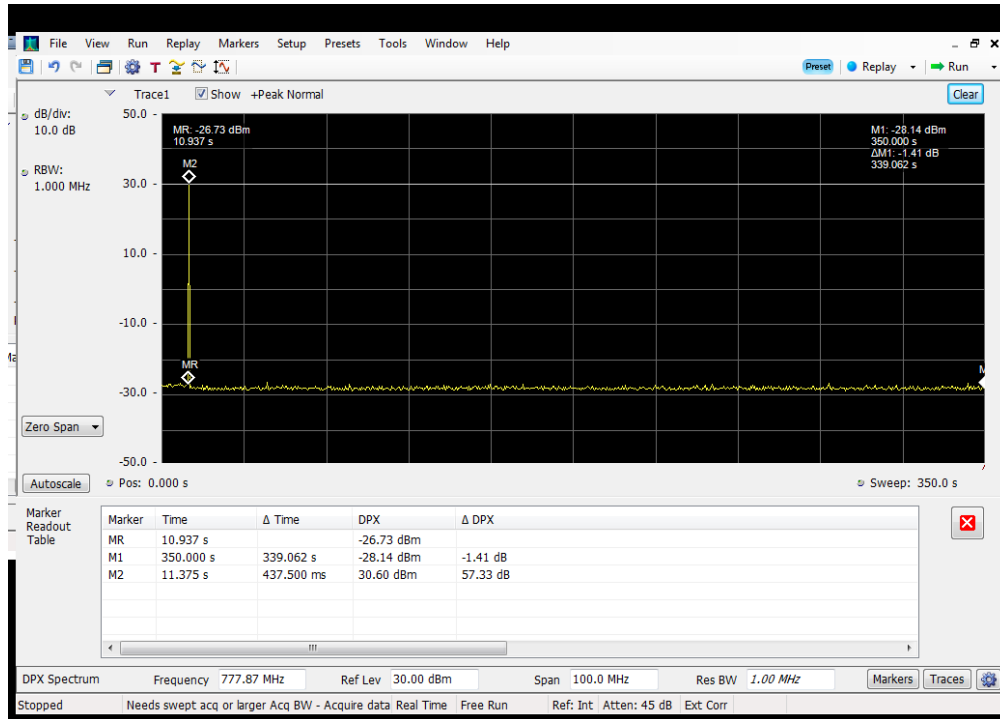


Restart Time_Uplink_698 - 716 MHz_B12_Server 4 to Dedicated Donor Port 3

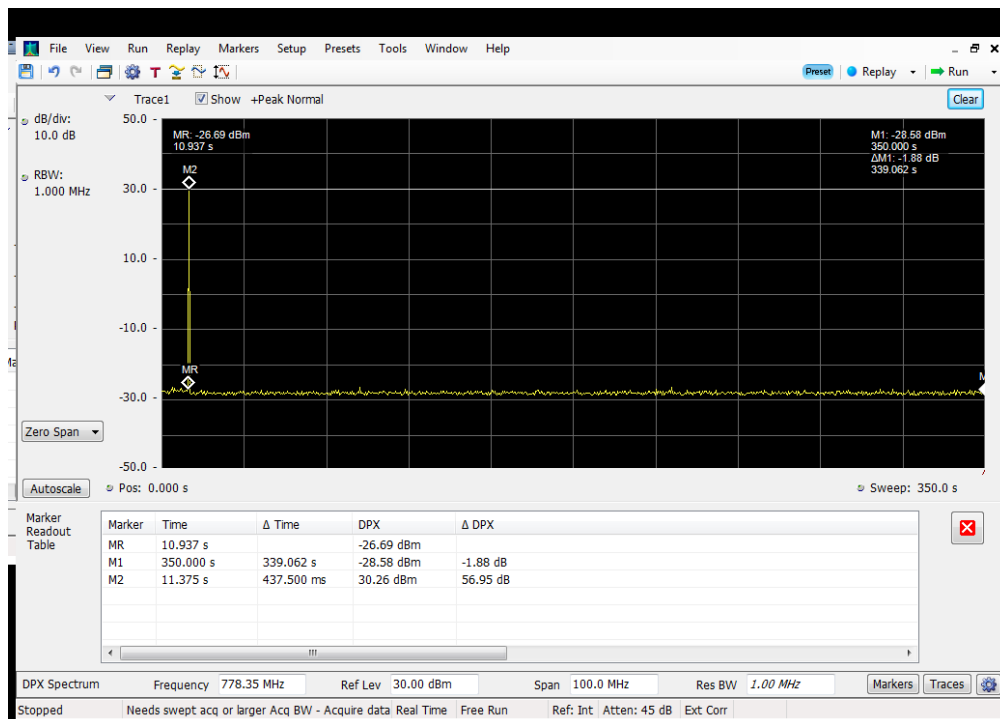




Restart Time_Uplink_776 - 787 MHz_B13_Server 1 to Common Donor Port 1

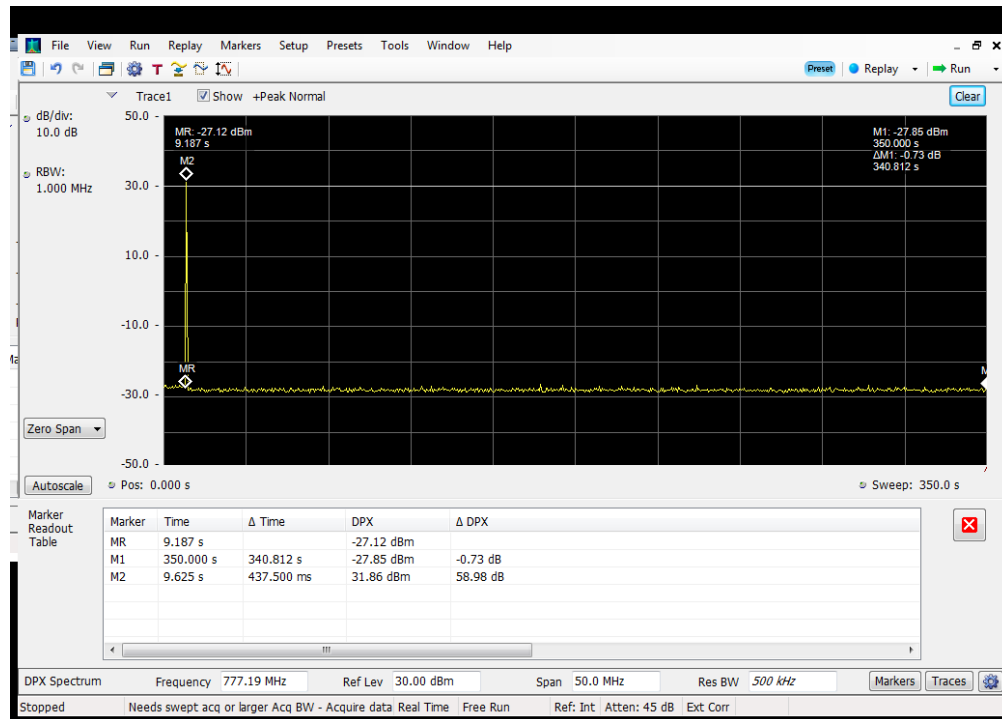


Restart Time_Uplink_776 - 787 MHz_B13_Server 3 to Common Donor Port 1

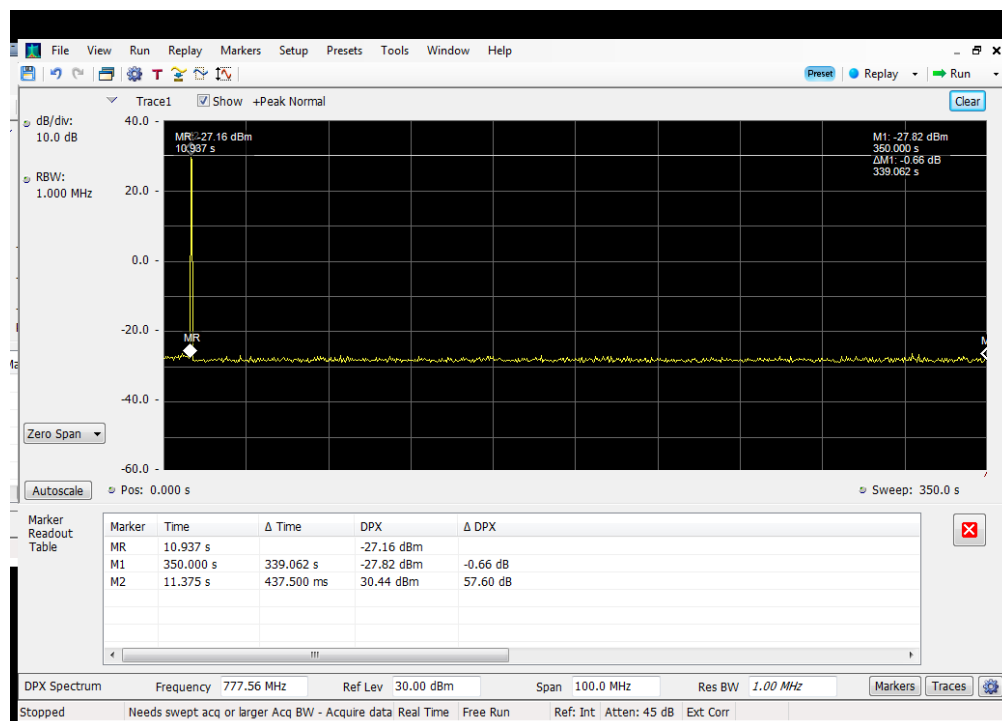




Restart Time_Uplink_776 - 787 MHz_B13_Server 3 to Dedicated Donor Port 3

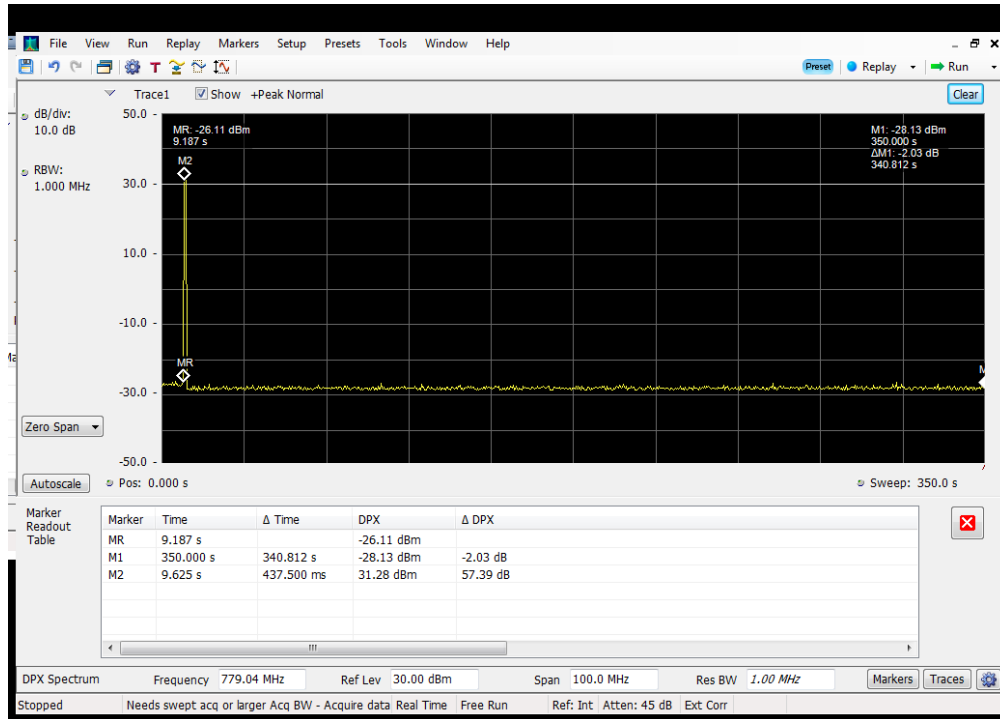


Restart Time_Uplink_776 - 787 MHz_B13_Server 4 to Common Donor Port 1

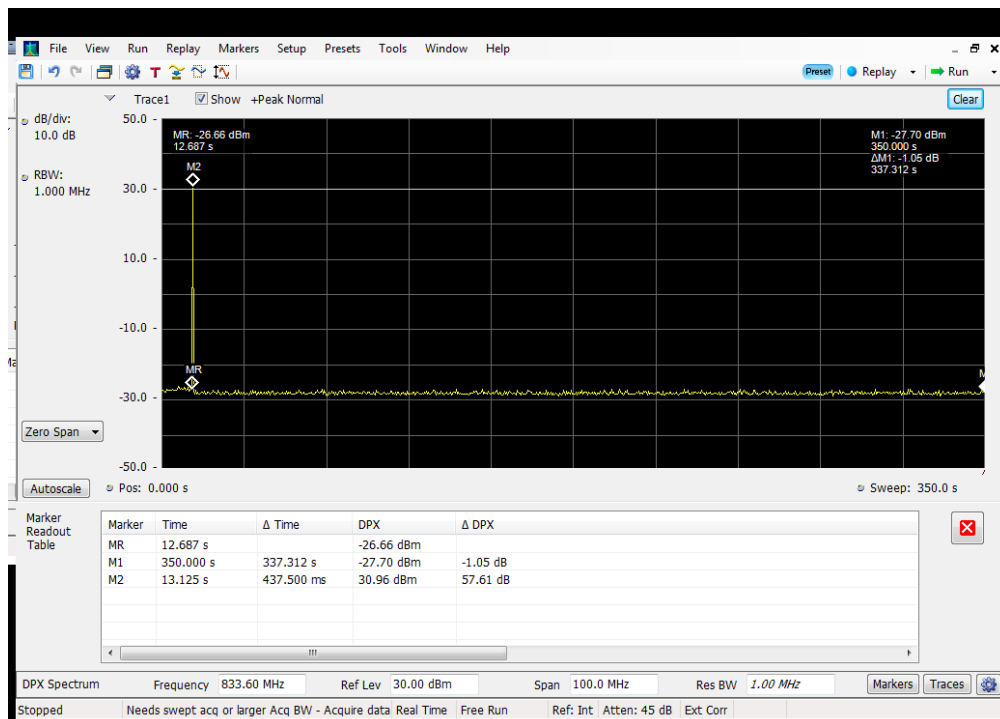




Restart Time_Uplink_776 - 787 MHz_B13_Server 4 to Dedicated Donor Port 3

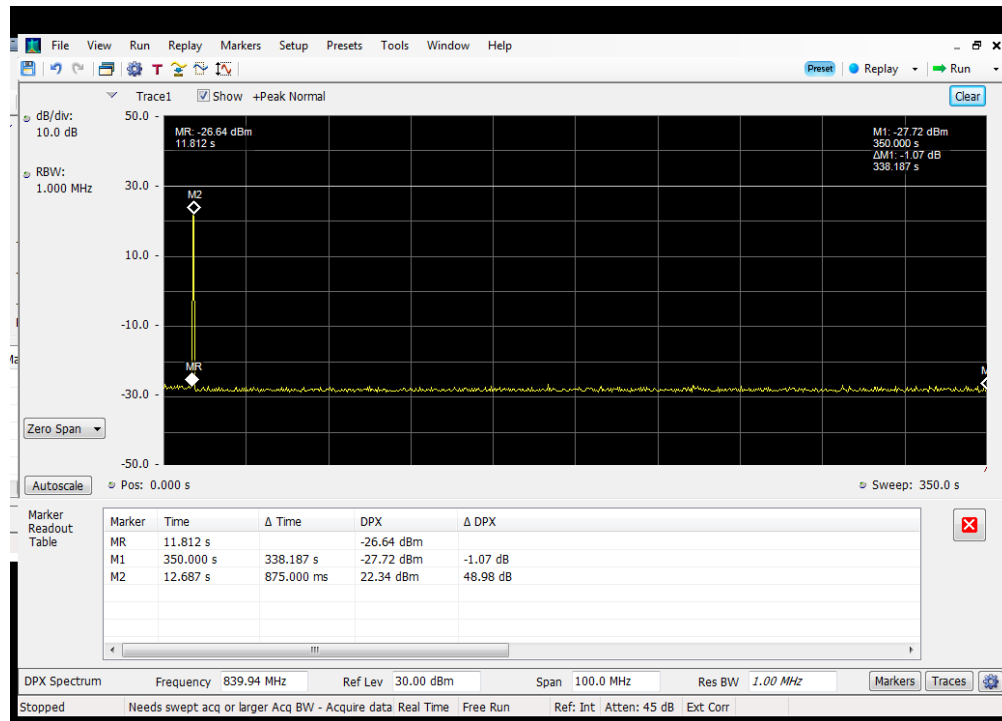


Restart Time_Uplink_824 - 849 MHz_B5_Server 1 to Common Donor Port 1

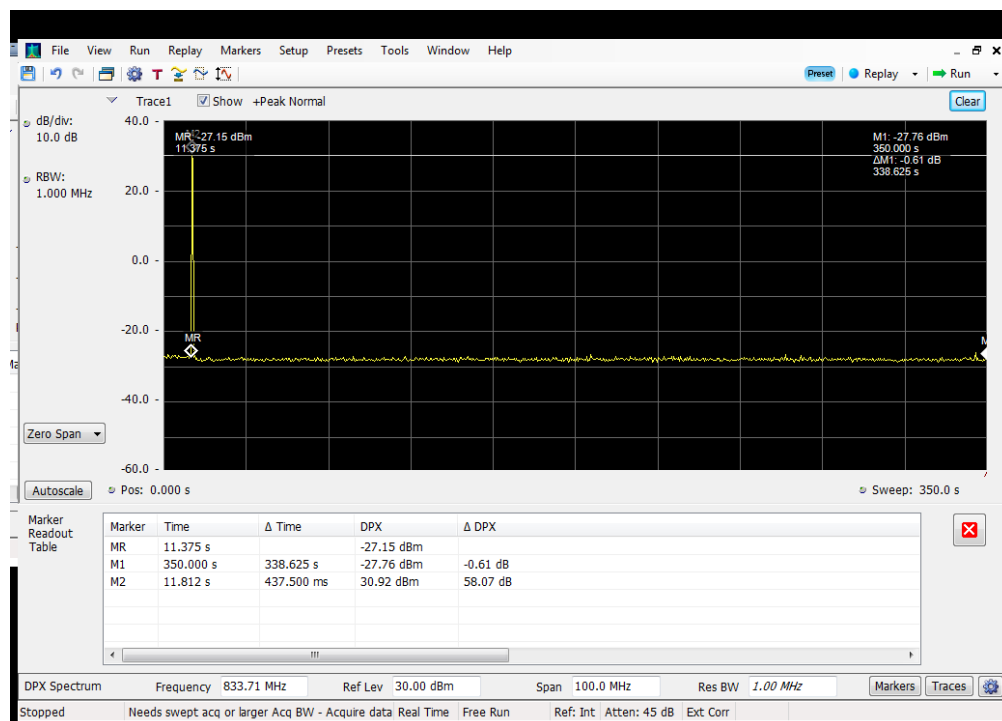




Restart Time_Uplink_824 - 849 MHz_B5_Server 3 to Common Donor Port 1

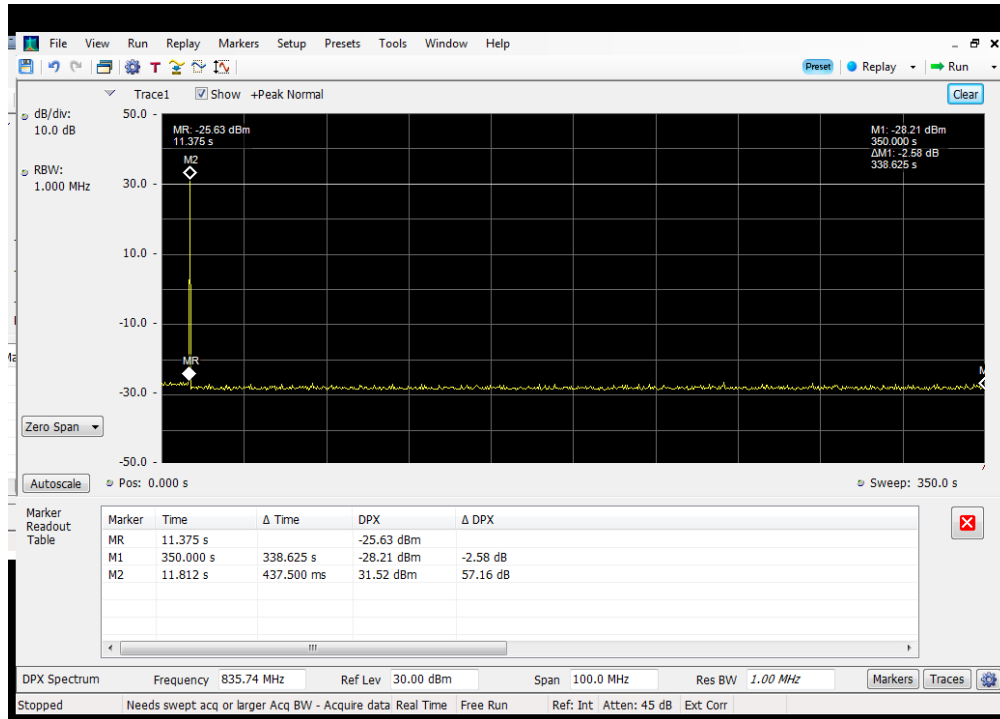


Restart Time_Uplink_824 - 849 MHz_B5_Server 4 to Common Donor Port 1

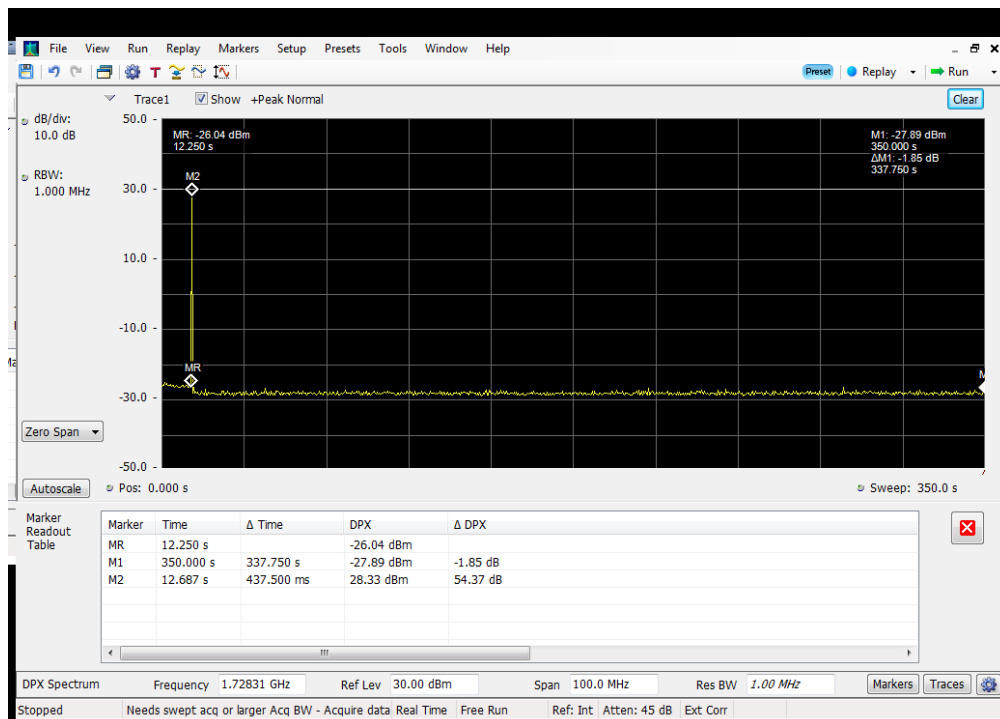




Restart Time_Uplink_824 - 849 MHz_B5_Server 4 to Dedicated Donor Port 2

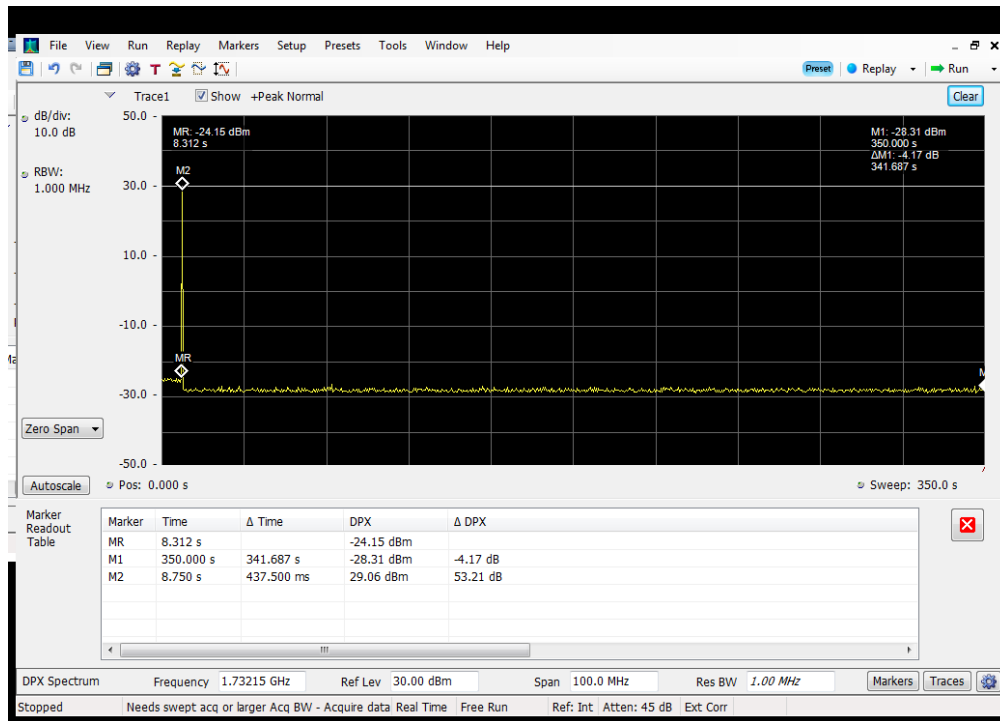


Restart Timing_Uplink_1710 - 1755 MHz_B4_Server 1 to Dedicated Donor Port 1





Restart Timing_Uplink_1710 - 1755 MHz_B4_Server 2 to Dedicated Donor Port 1

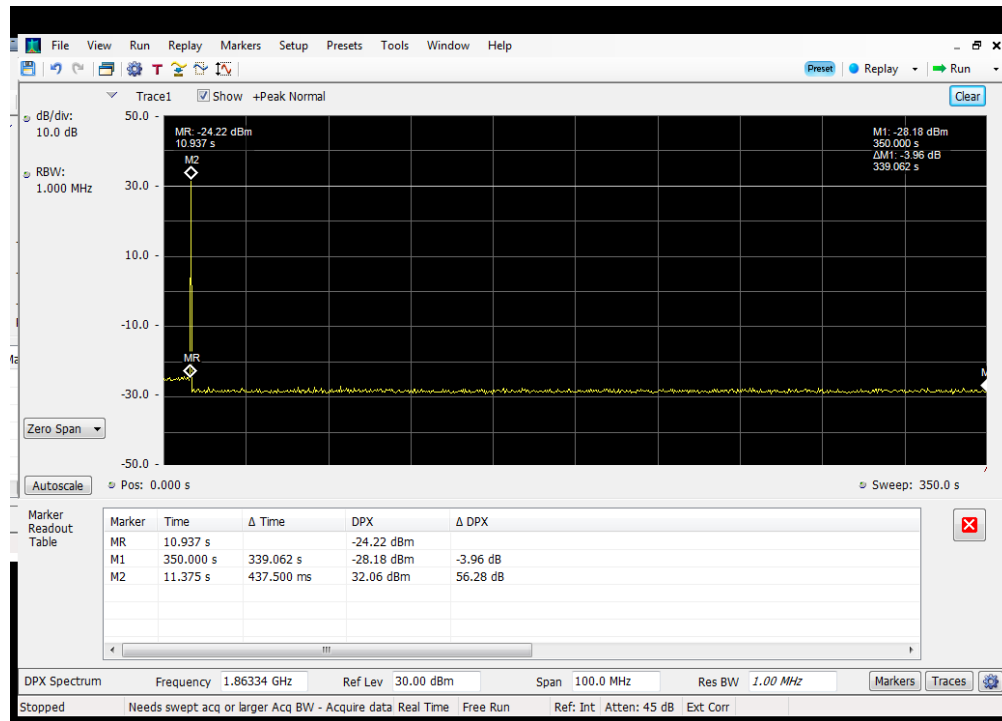


Restart Timing_Uplink_1710 - 1755 MHz_B4_Server 3 to Dedicated Donor Port 1

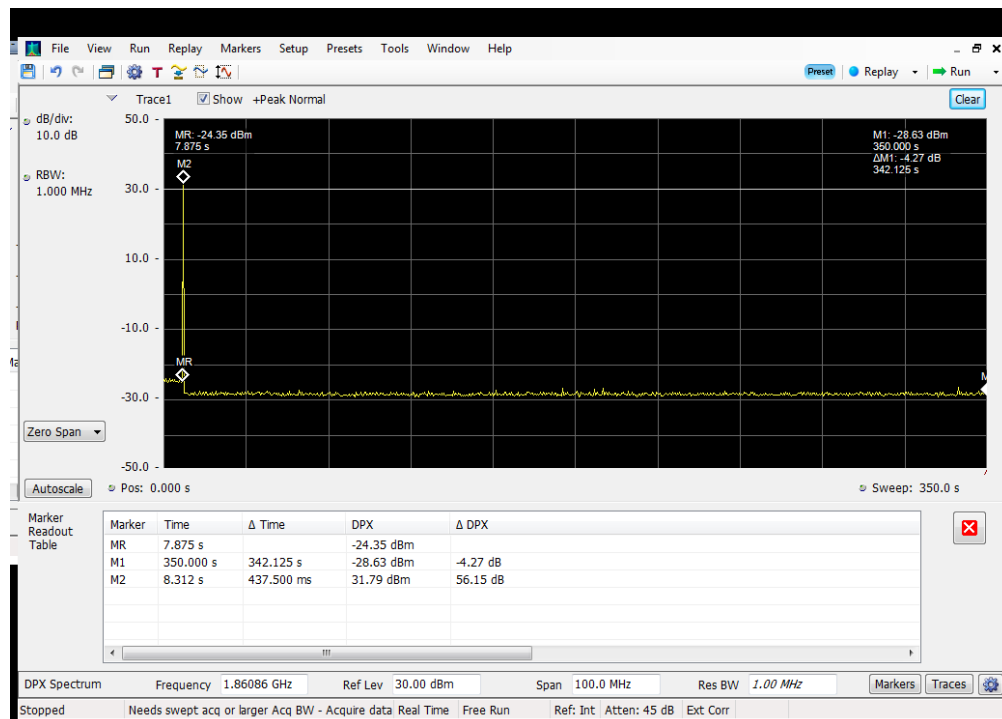




Restart Timing_Uplink_1850 - 1915 MHz_B25_Server 1 to Dedicated Donor Port 1

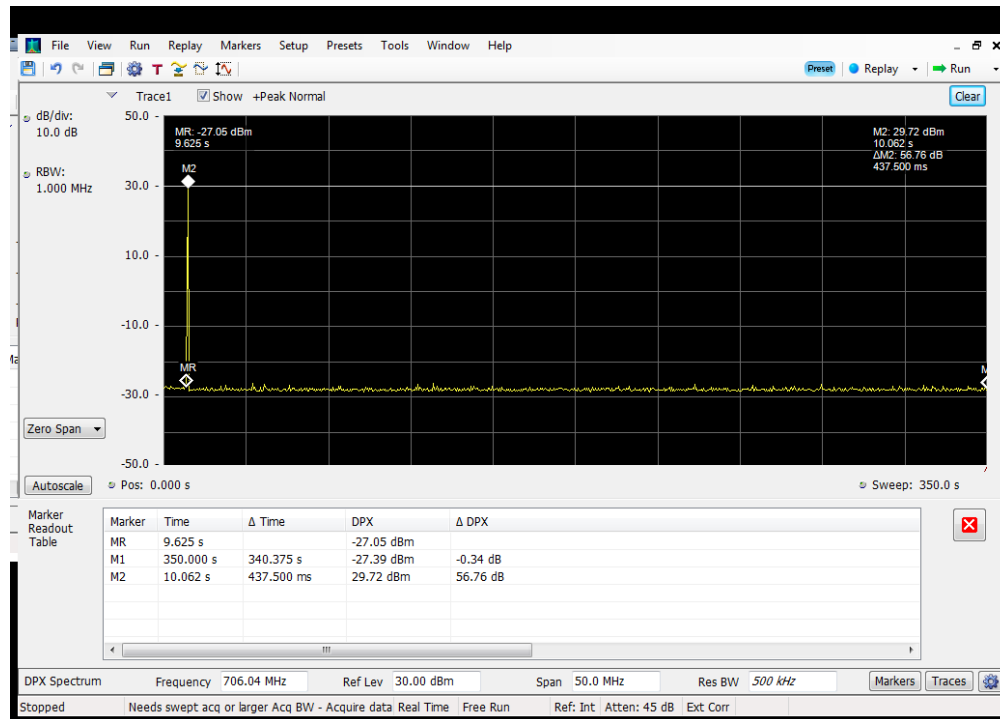


Restart Timing_Uplink_1850 - 1915 MHz_B25_Server 2 to Dedicated Donor Port 1





Restart Timing_Uplink_698 - 716 MHz_B12_Server 1 to Dedicated Donor Port 3

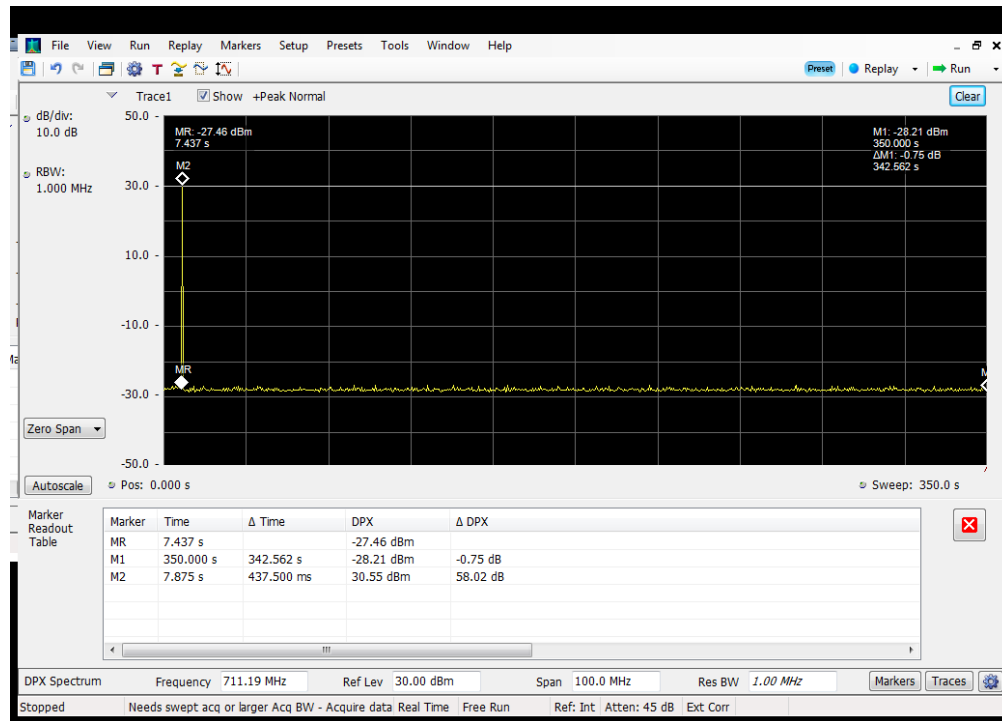


Restart Timing_Uplink_698 - 716 MHz_B12_Server 2 to Common Donor Port 1

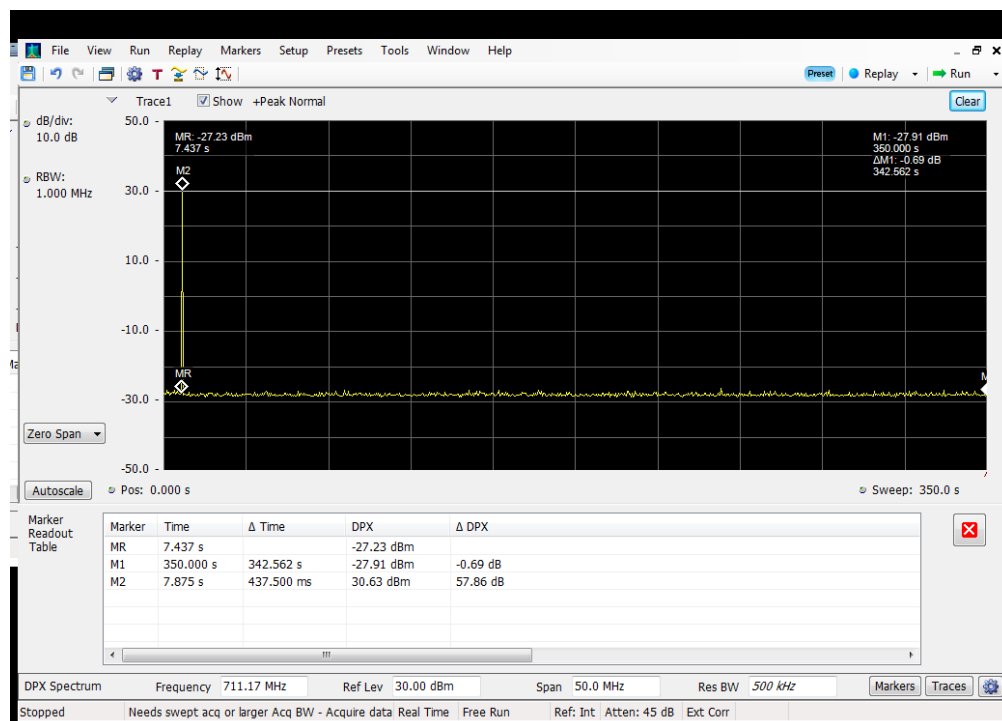




Restart Timing_Uplink_698 - 716 MHz_B12_Server 2 to Dedicated Donor Port 3

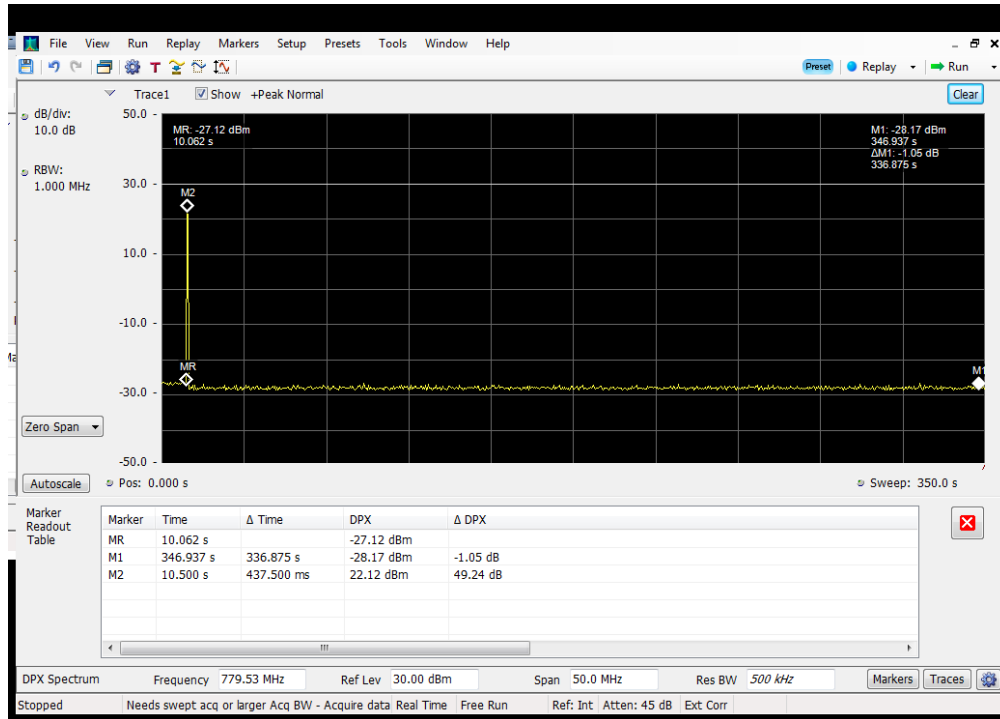


Restart Timing_Uplink_698 - 716 MHz_B12_Server 3 to Dedicated Donor Port 3





Restart Timing_Uplink_776 - 787 MHz_B13_Server 1 to Dedicated Donor Port 3

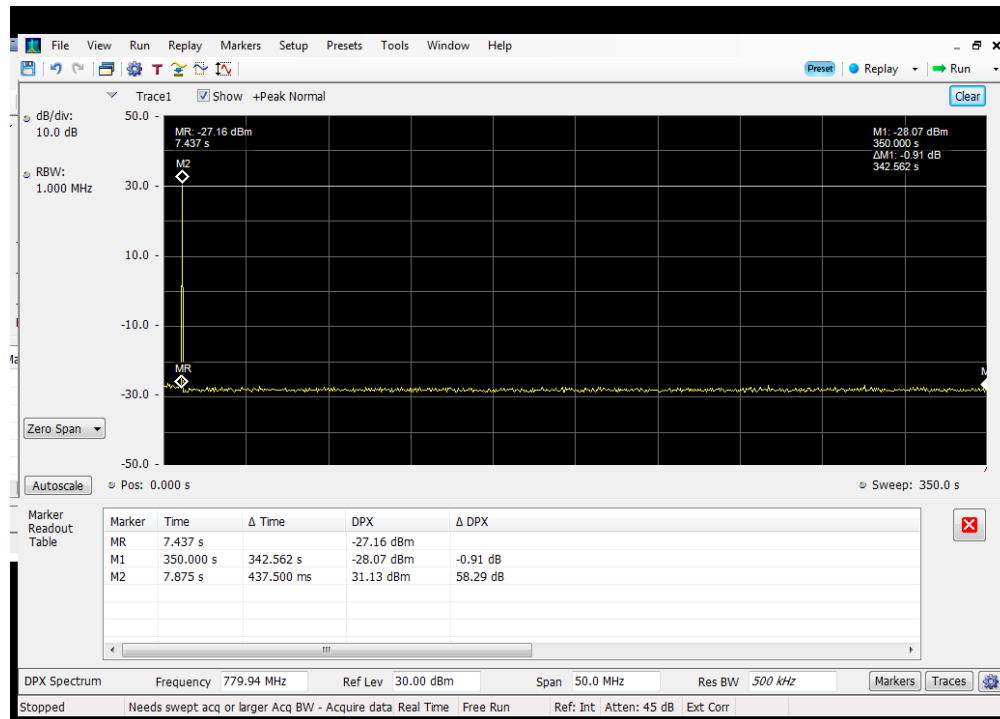


Restart Timing_Uplink_776 - 787 MHz_B13_Server 2 to Common Donor Port 1

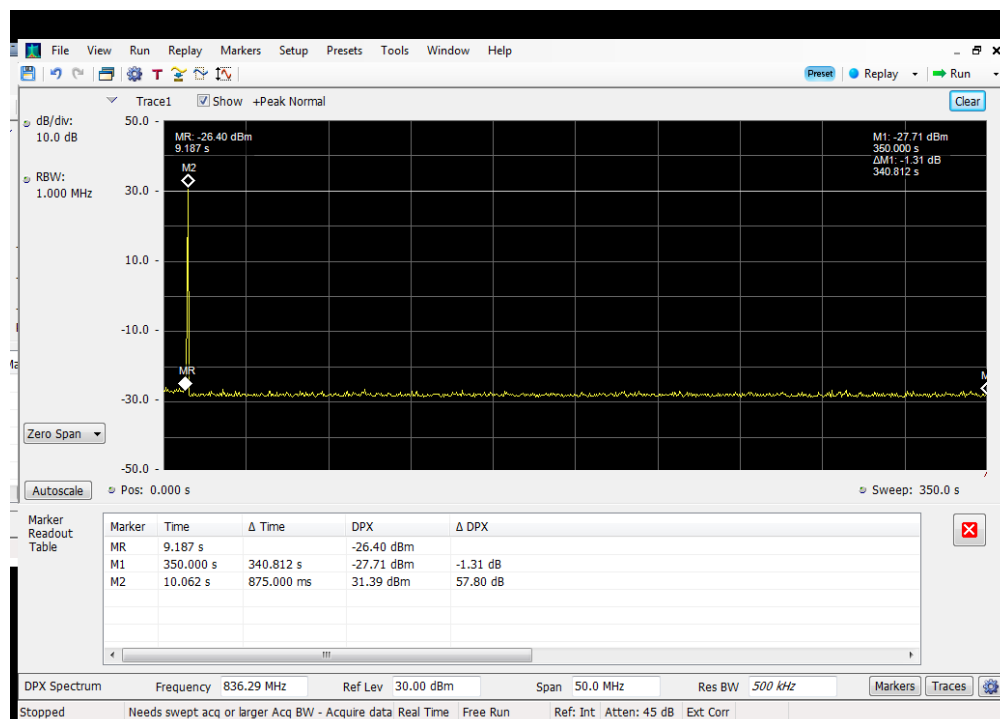




Restart Timing_Uplink_776 - 787 MHz_B13_Server 2 to Dedicated Donor Port 3

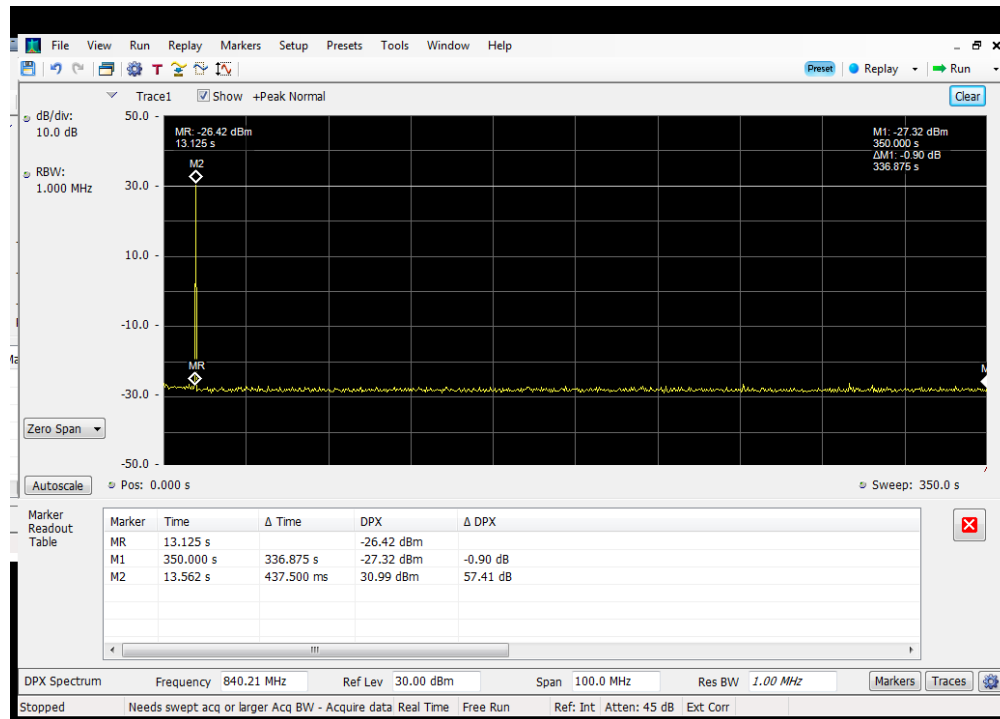


Restart Timing_Uplink_824 - 849 MHz_B5_Server 1 to Dedicated Donor Port 2





Restart Timing_Uplink_824 - 849 MHz_B5_Server 2 to Common Donor Port 1



Restart Timing_Uplink_824 - 849 MHz_B5_Server 2 to Dedicated Donor Port 2





Restart Timing_Uplink_824 - 849 MHz_B5_Server 3 to Dedicated Donor Port 2

