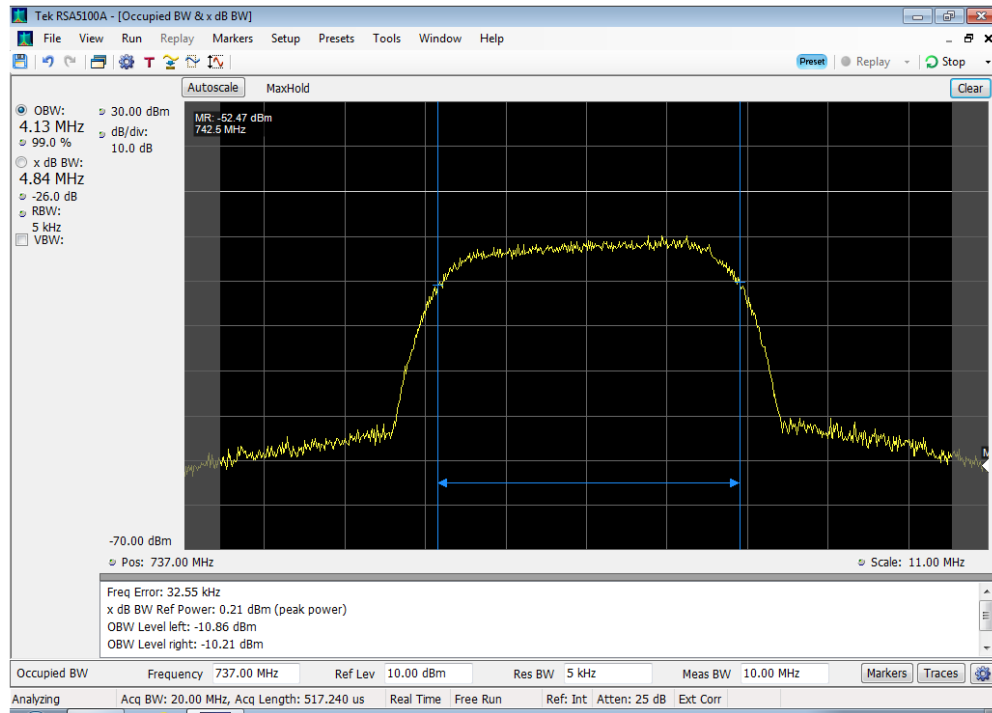


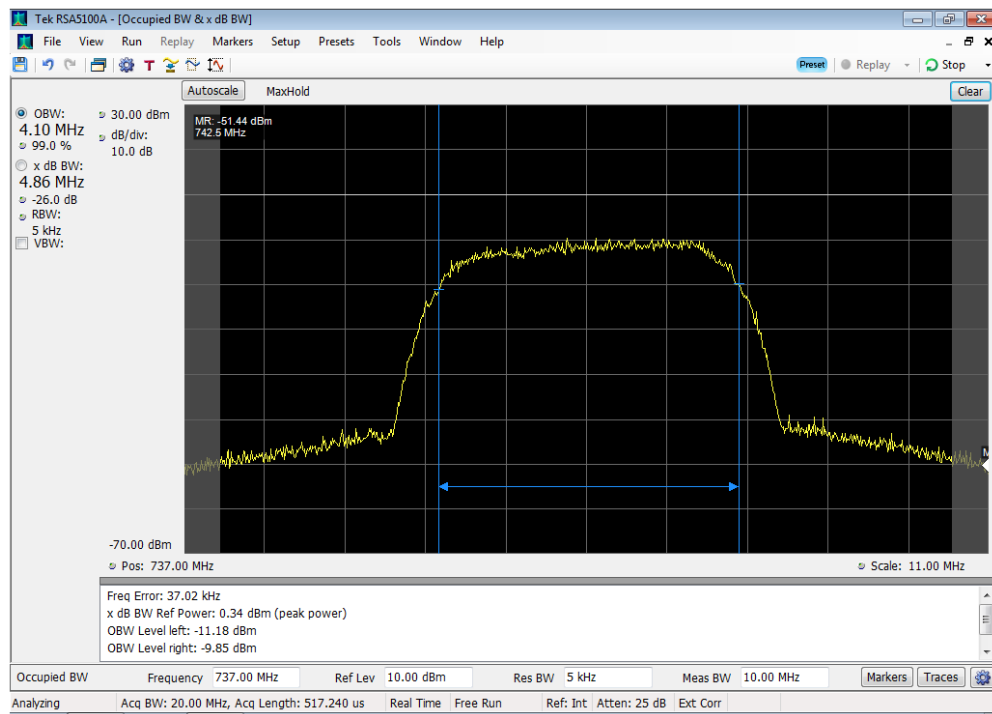
Annex B

Occupied Bandwidth AWGN

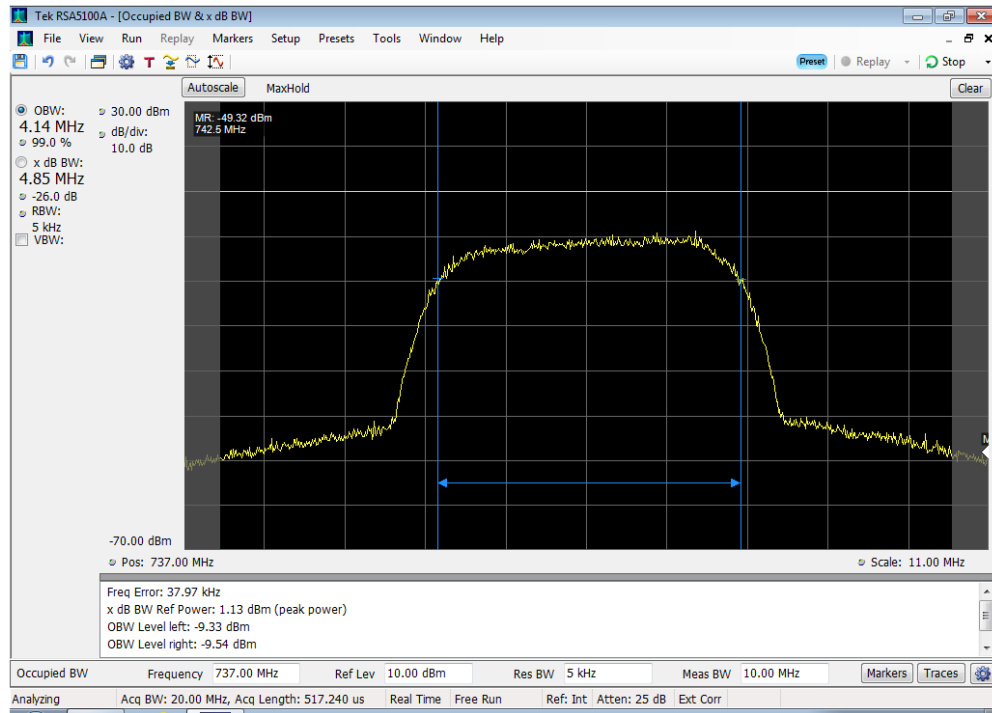
OCC BW_AWGN_B12_DL_Common_D2 to S2



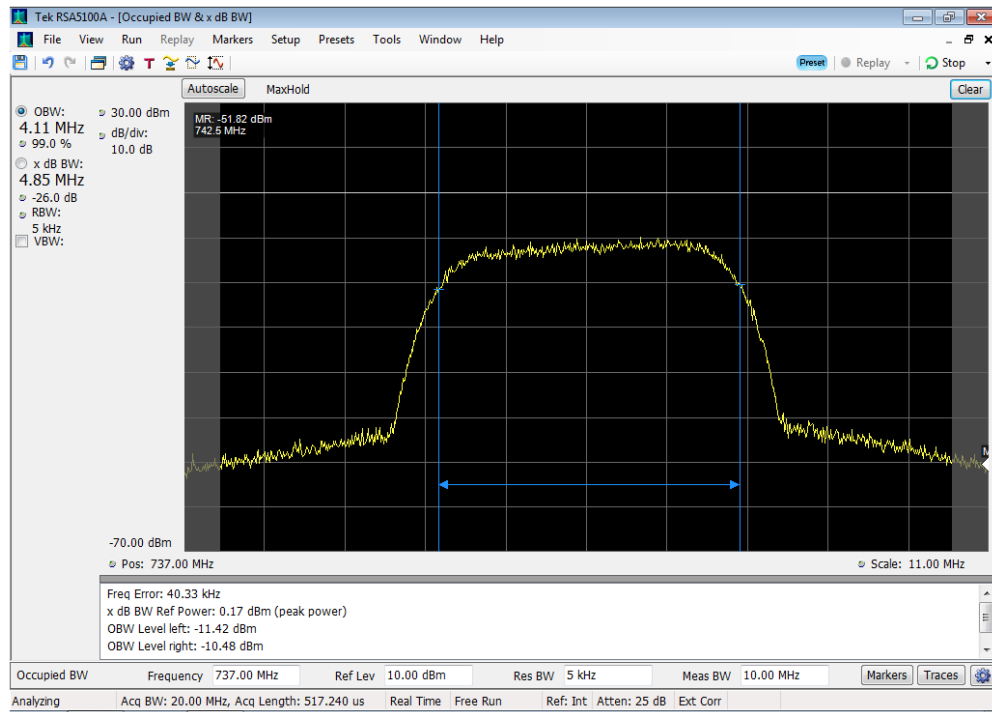
OCC BW_AWGN_B12_DL_Common_D2 to S2_Pin + 3 dB



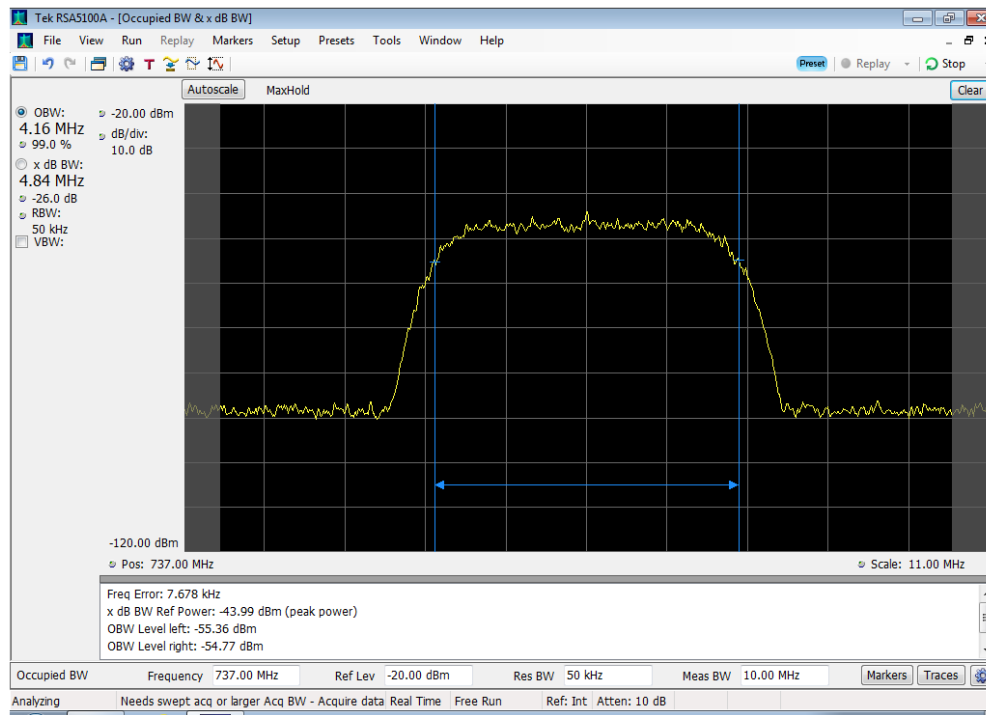
OCC BW_AWGN_B12_DL_Dedicated_D3 to S2



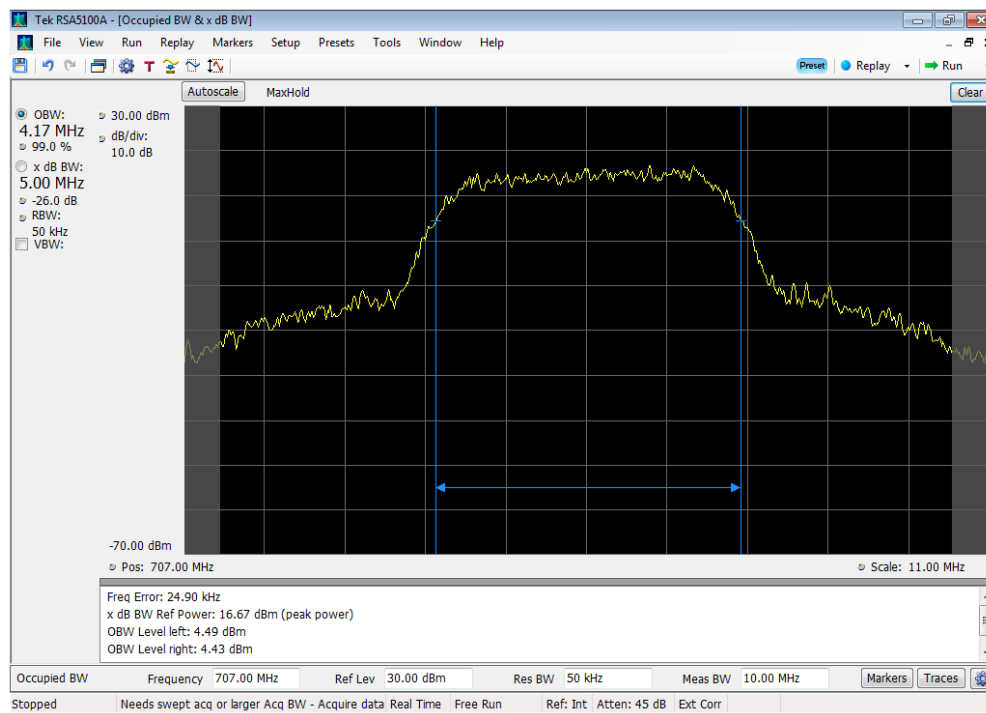
OCC BW_AWGN_B12_DL_Dedicated_D3 to S2_Pin + 3 dB



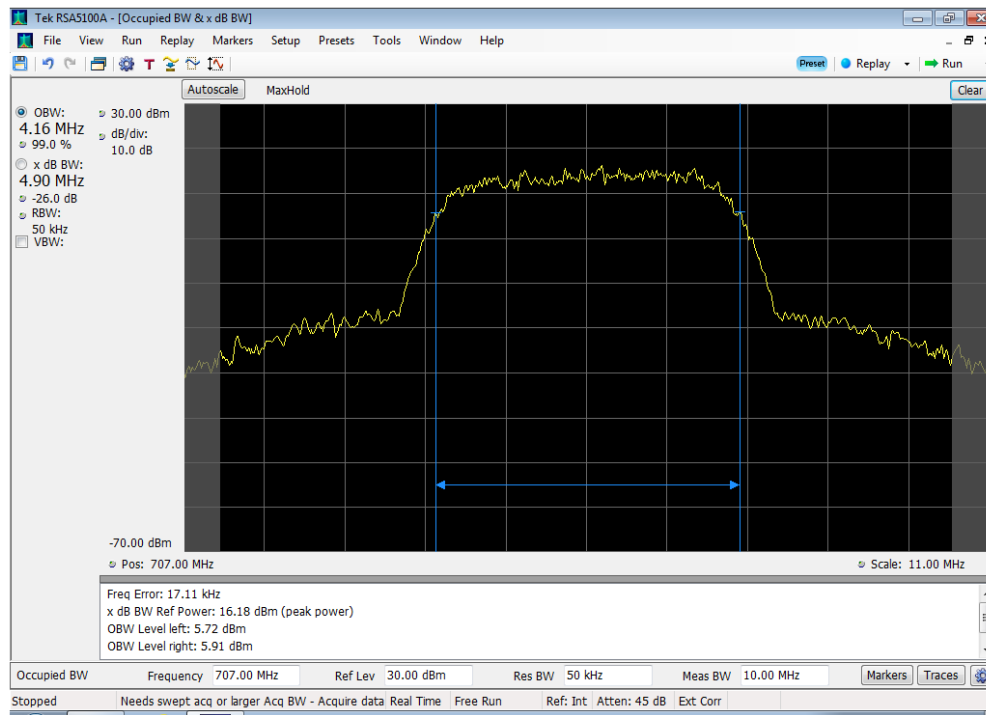
OCC BW_AWGN_B12_DL_Input Signal



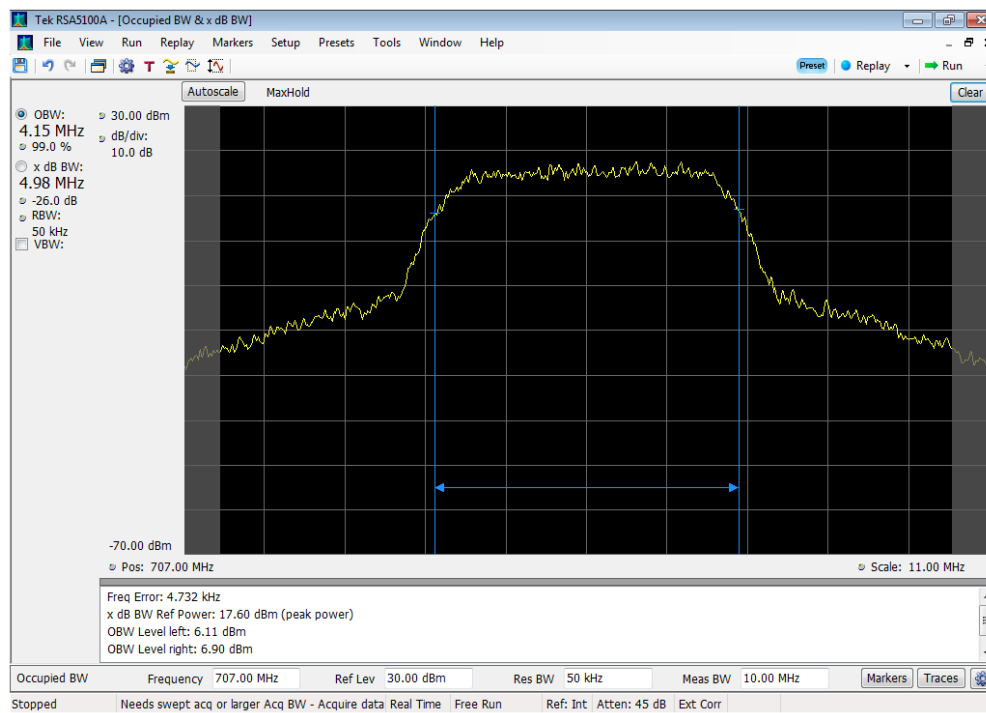
OCC BW_AWGN_B12_UL_Common_S2 to D2



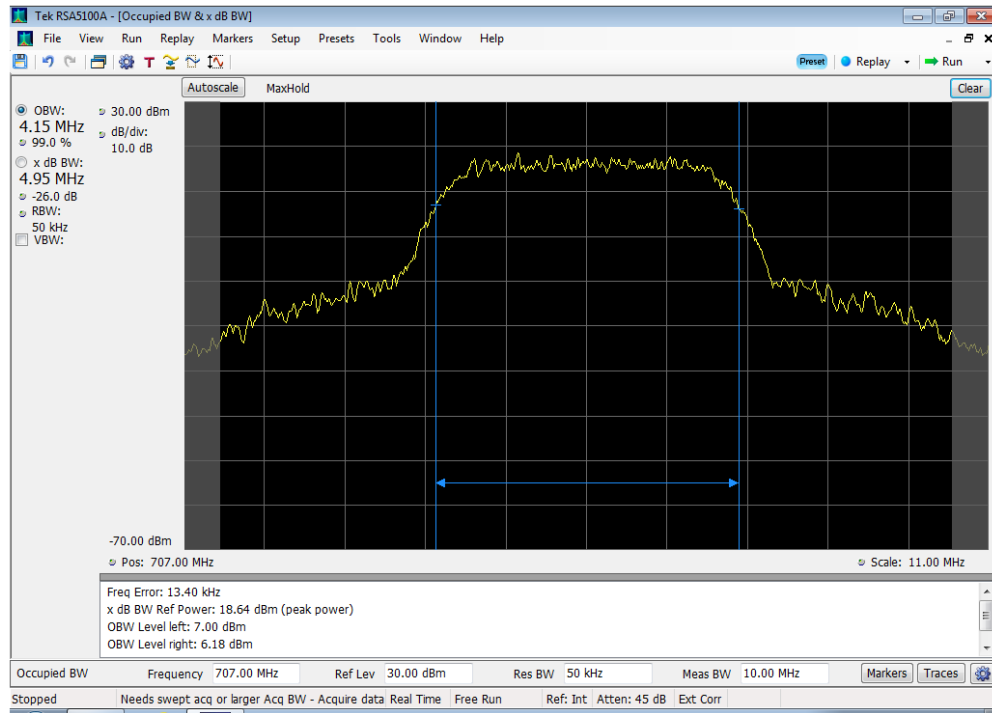
OCC BW_AWGN_B12_UL_Common_S2 to D2_Pin + 3 dB



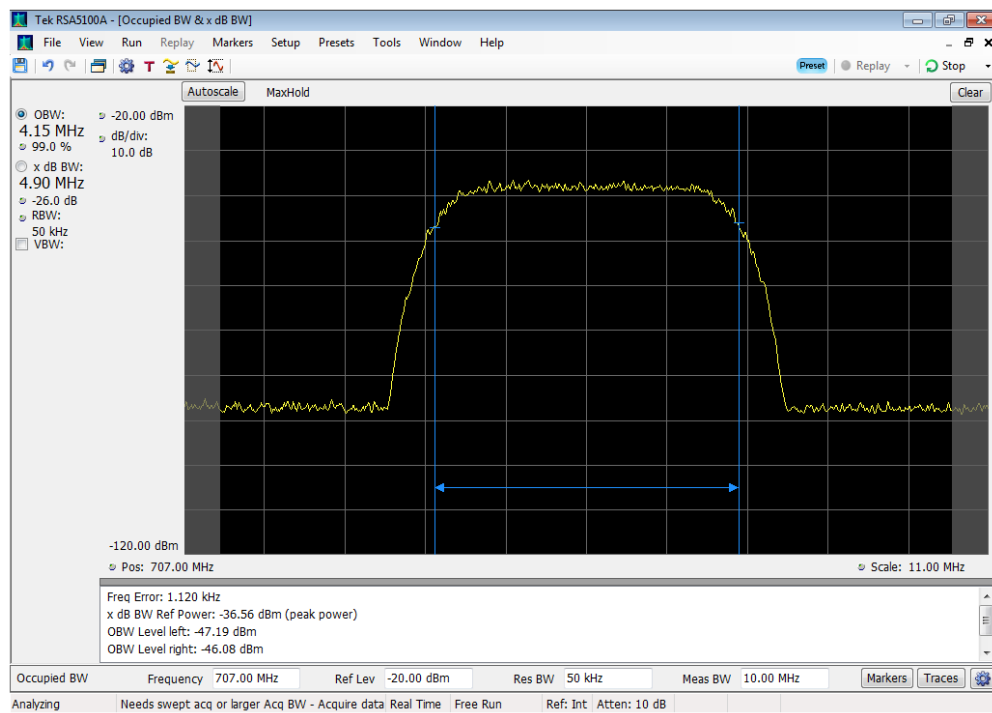
OCC BW_AWGN_B12_UL_Dedicated_S2 to D3



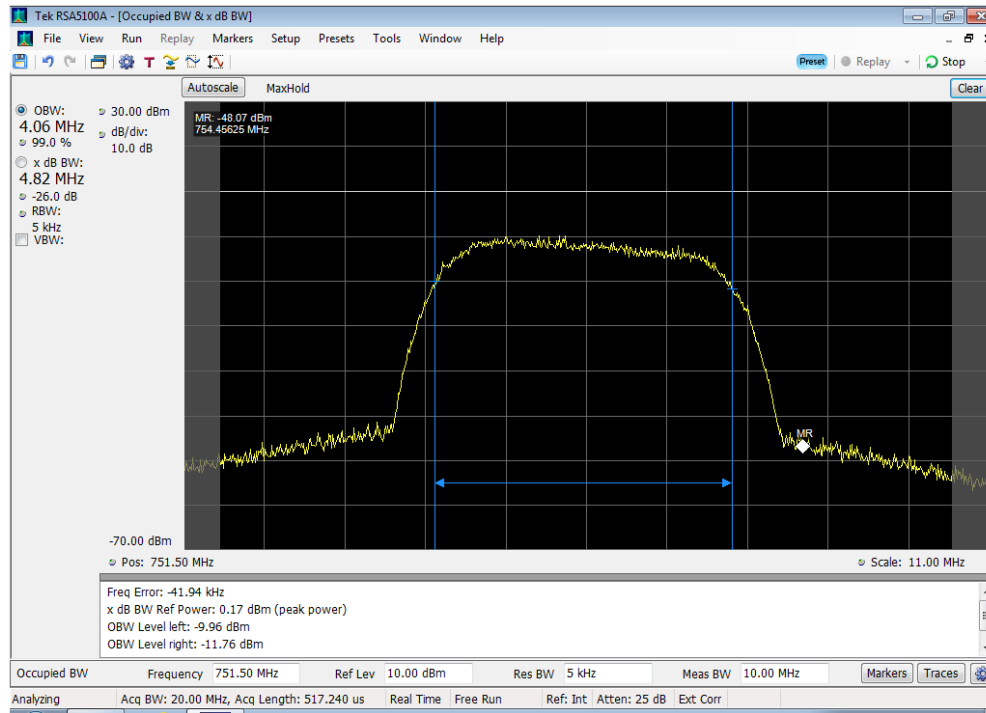
OCC BW_AWGN_B12_UL_Dedicated_S2 to D3_Pin + 3 dB



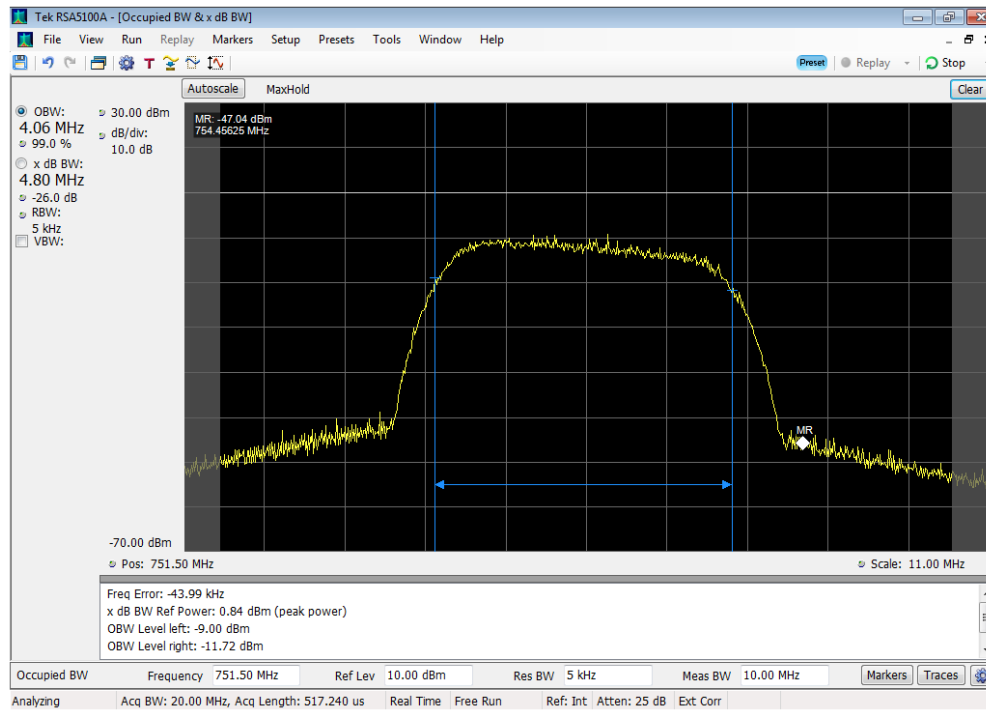
OCC BW_AWGN_B12_UL_Input Signal



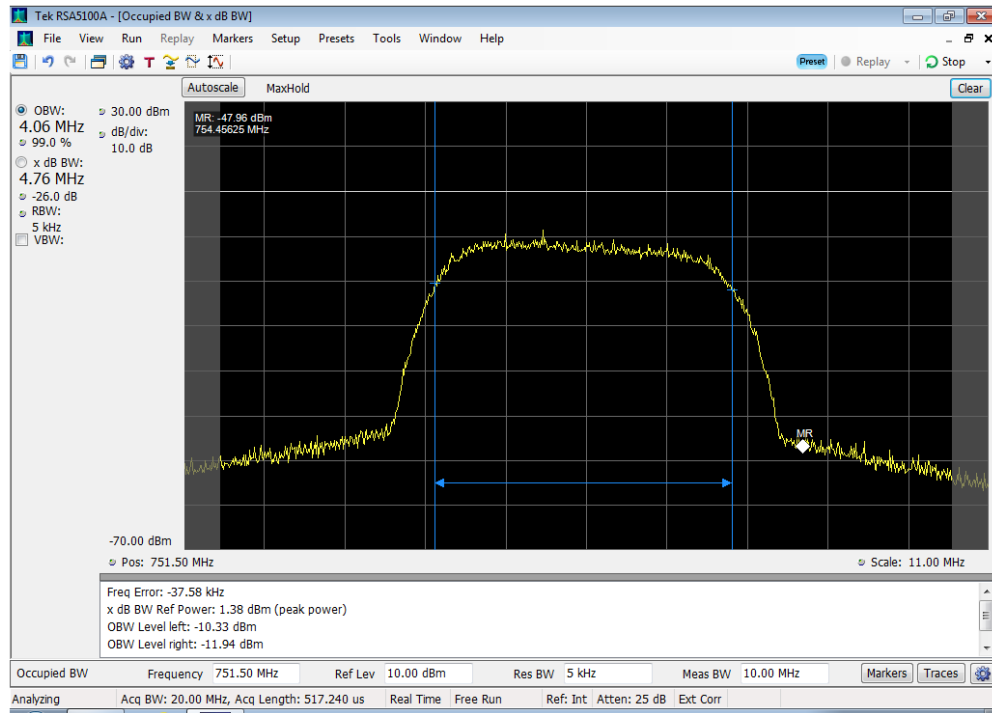
OCC BW_AWGN_B13_DL_Common_D2 to S2



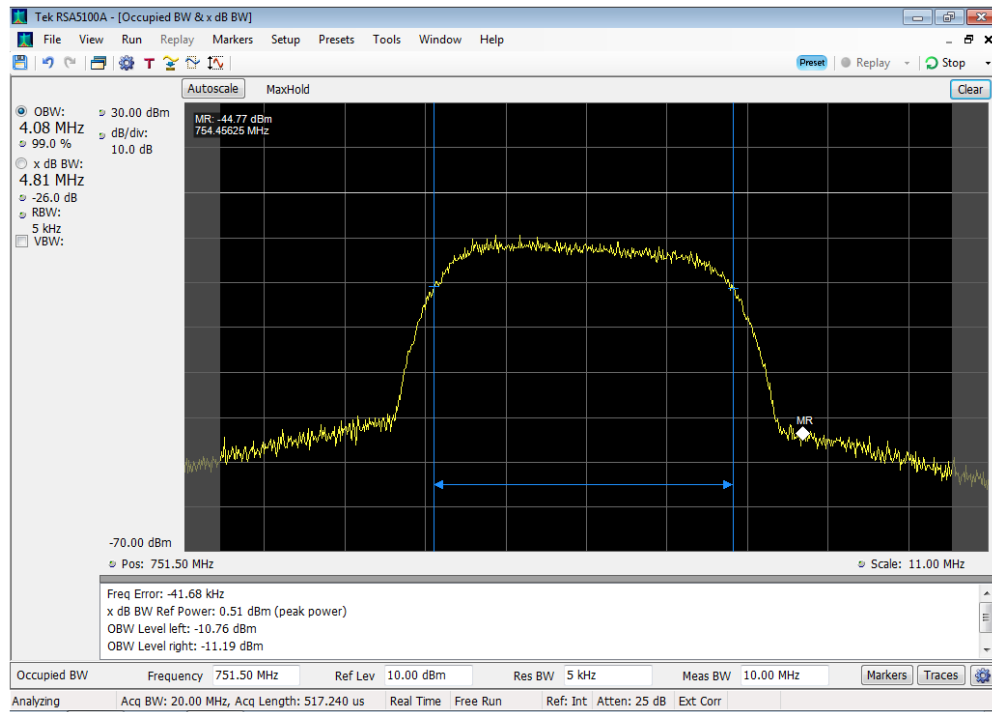
OCC BW_AWGN_B13_DL_Common_D2 to S2_Pin + 3 dB



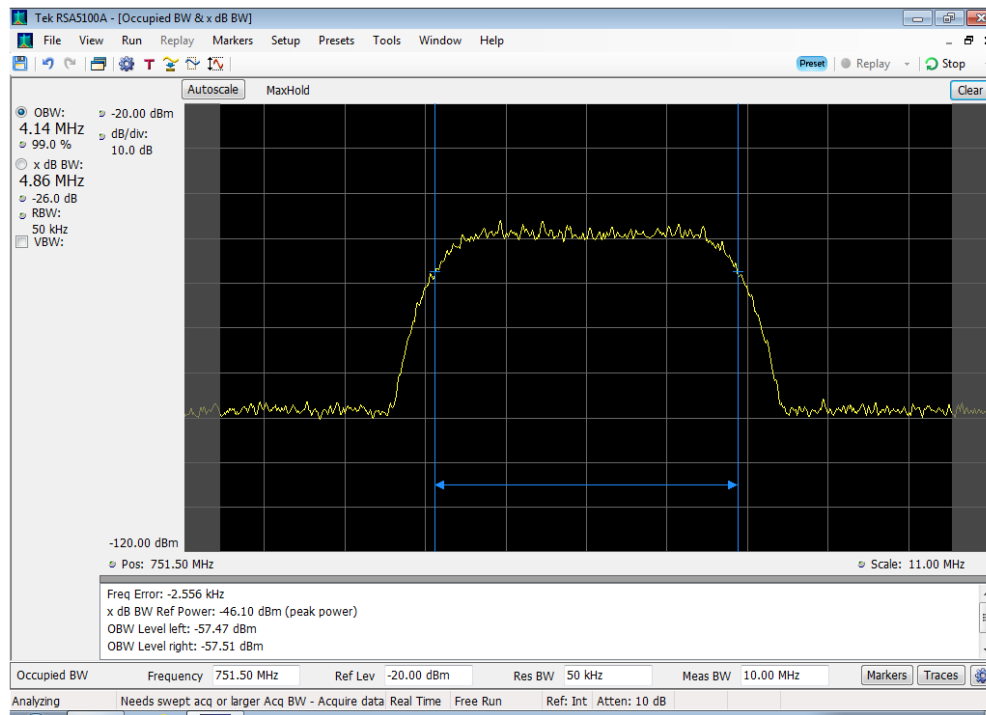
OCC BW_AWGN_B13_DL_Dedicated_D3 to S2



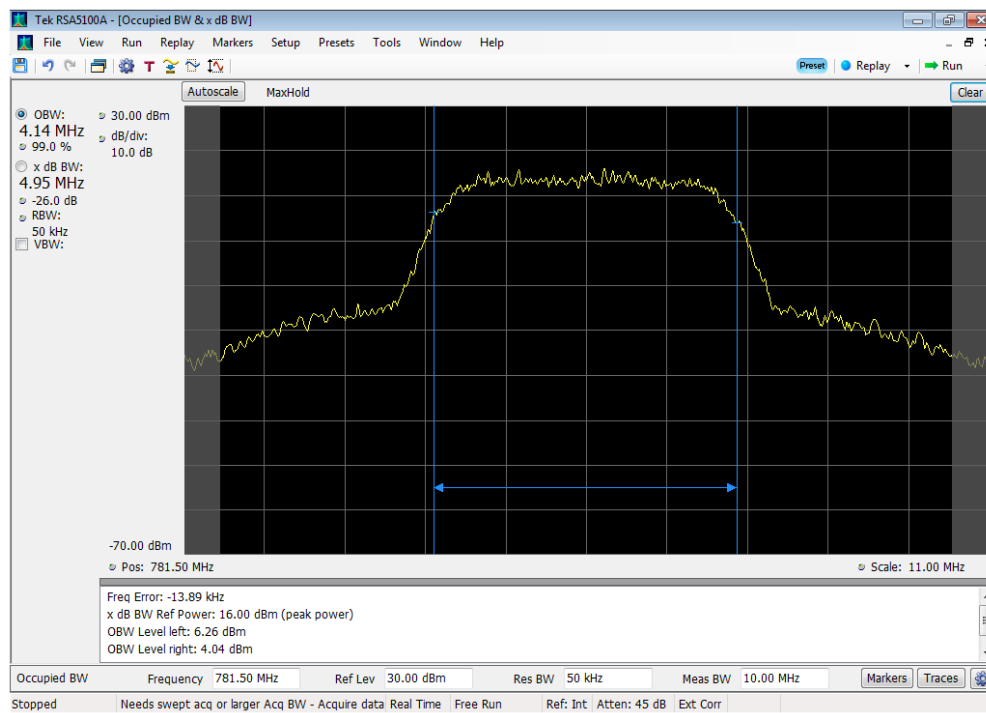
OCC BW_AWGN_B13_DL_Dedicated_D3 to S2_Pin + 3 dB



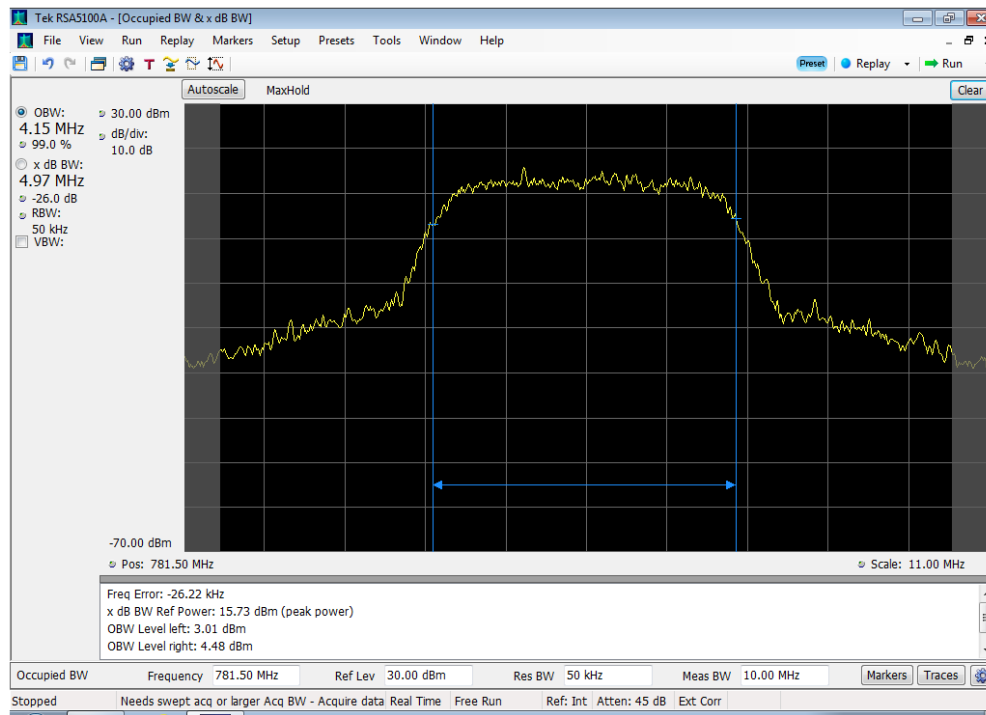
OCC BW_AWGN_B13_DL_Input Signal



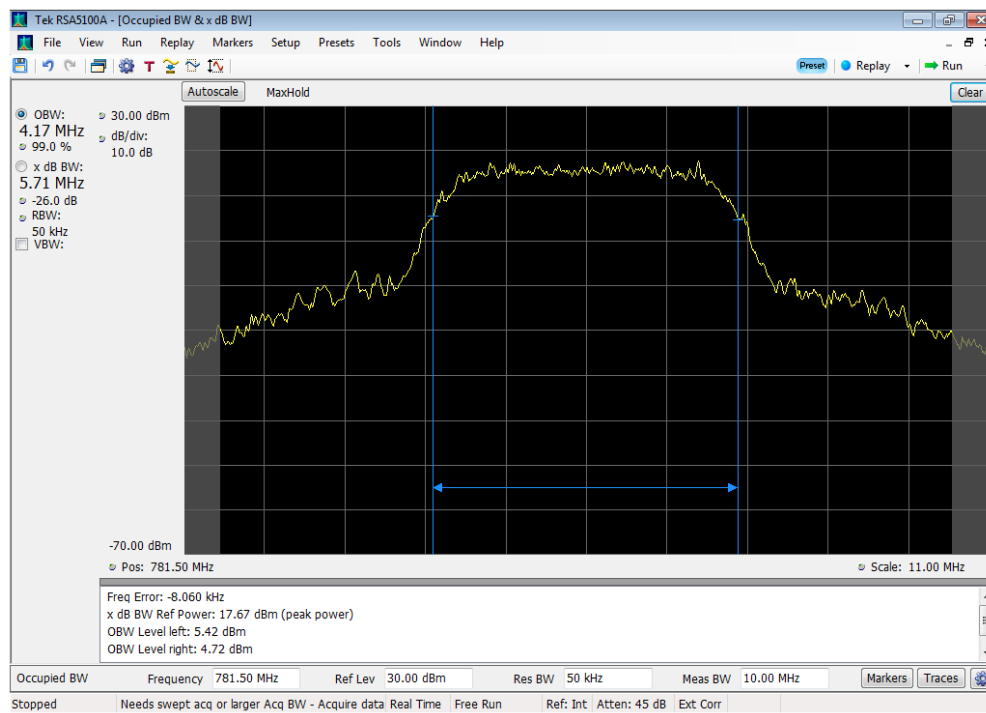
OCC BW_AWGN_B13_UL_Common_S2 to D2



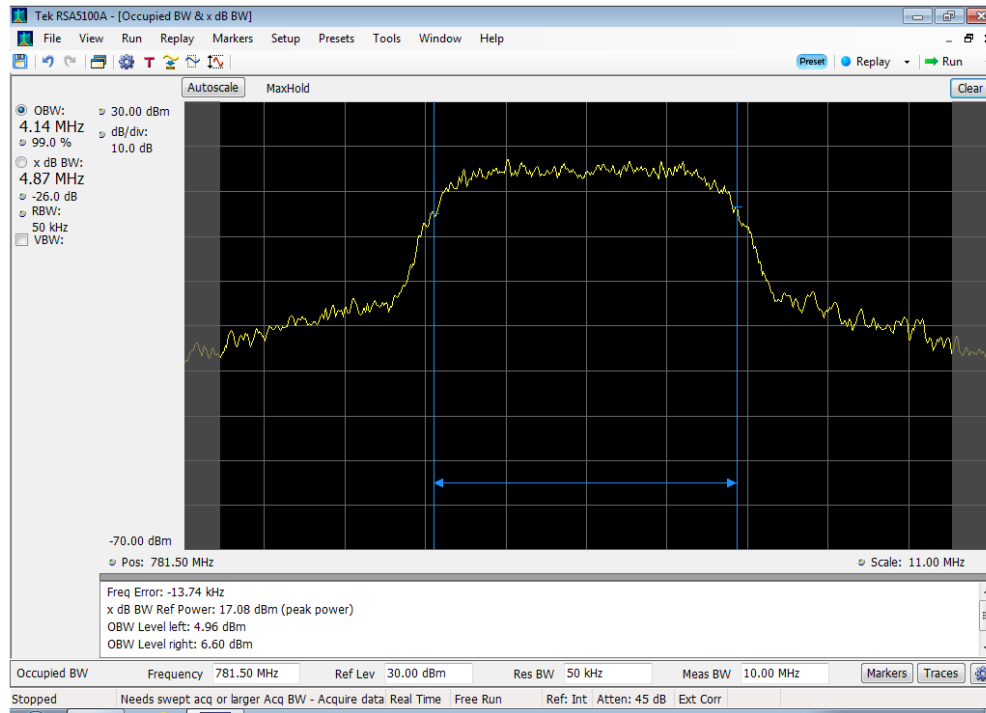
OCC BW_AWGN_B13_UL_Common_S2 to D2_Pin + 3 dB



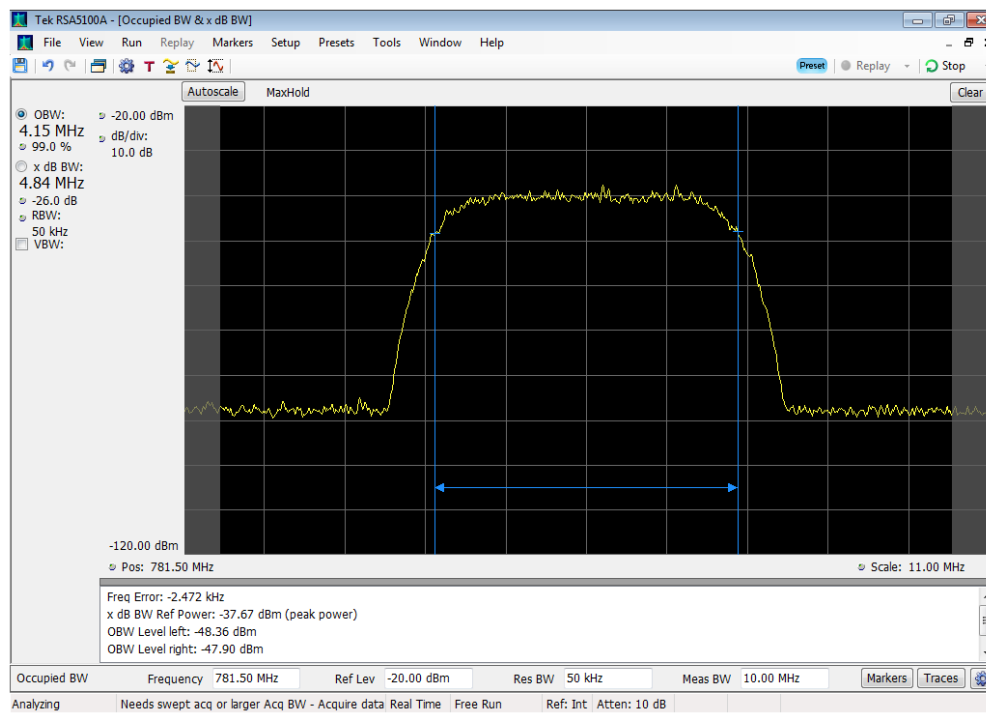
OCC BW_AWGN_B13_UL_Dedicated_S2 to D3



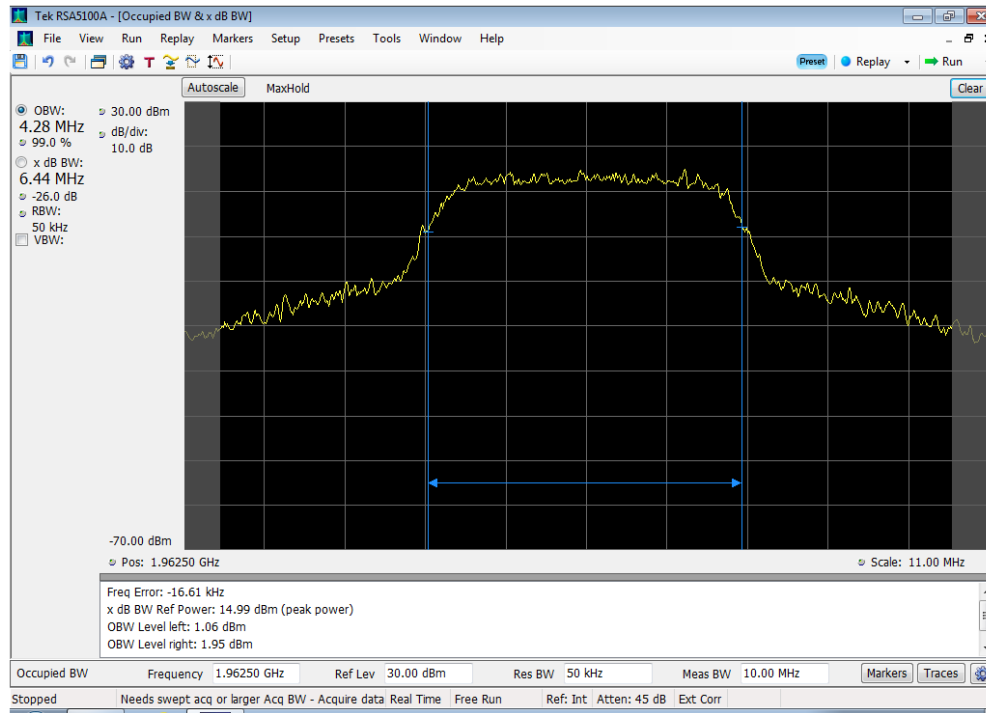
OCC BW_AWGN_B13_UL_Dedicated_S2 to D3_Pin + 3 dB



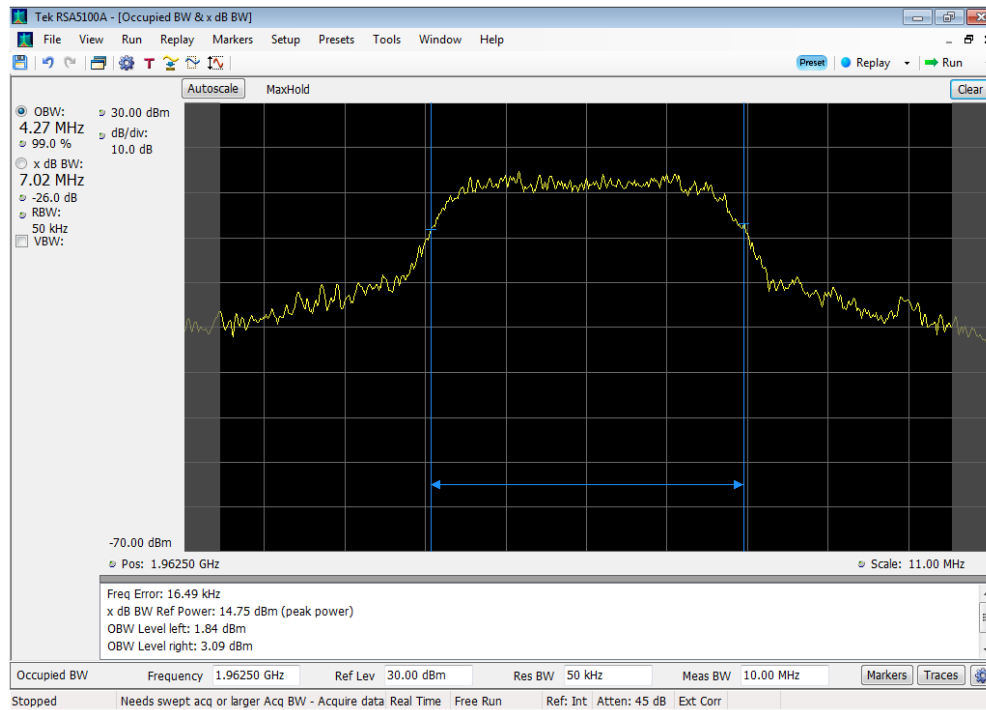
OCC BW_AWGN_B13_UL_Input Signal



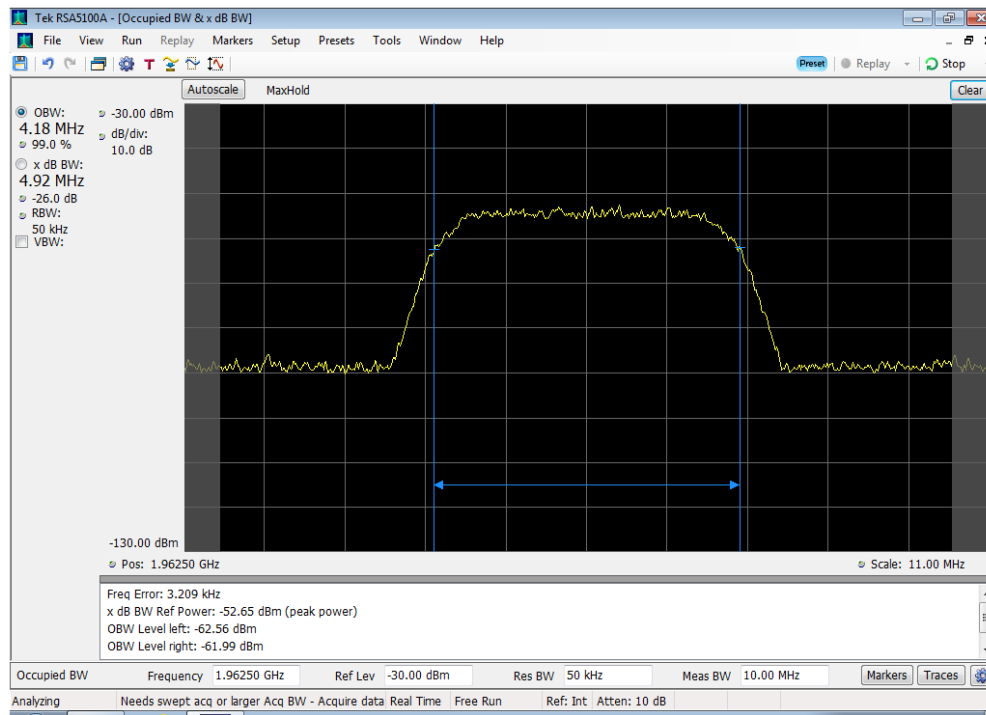
OCC BW_AWGN_B25_DL_Dedicated_D2 to S2



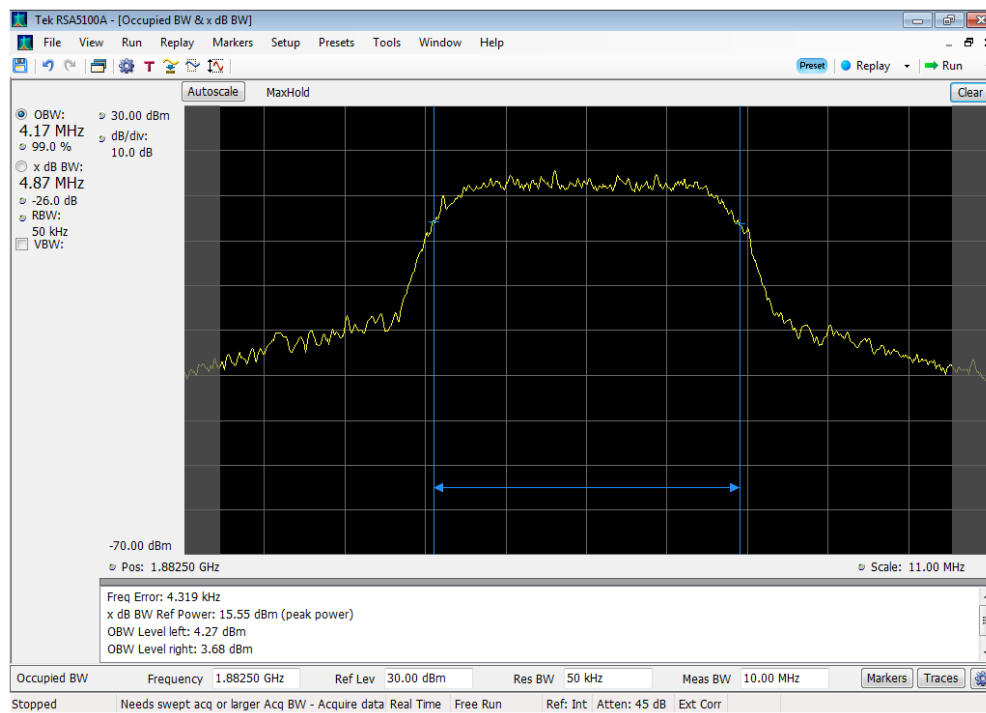
OCC BW_AWGN_B25_DL_Dedicated_D2 to S2_Pin + 3 dB



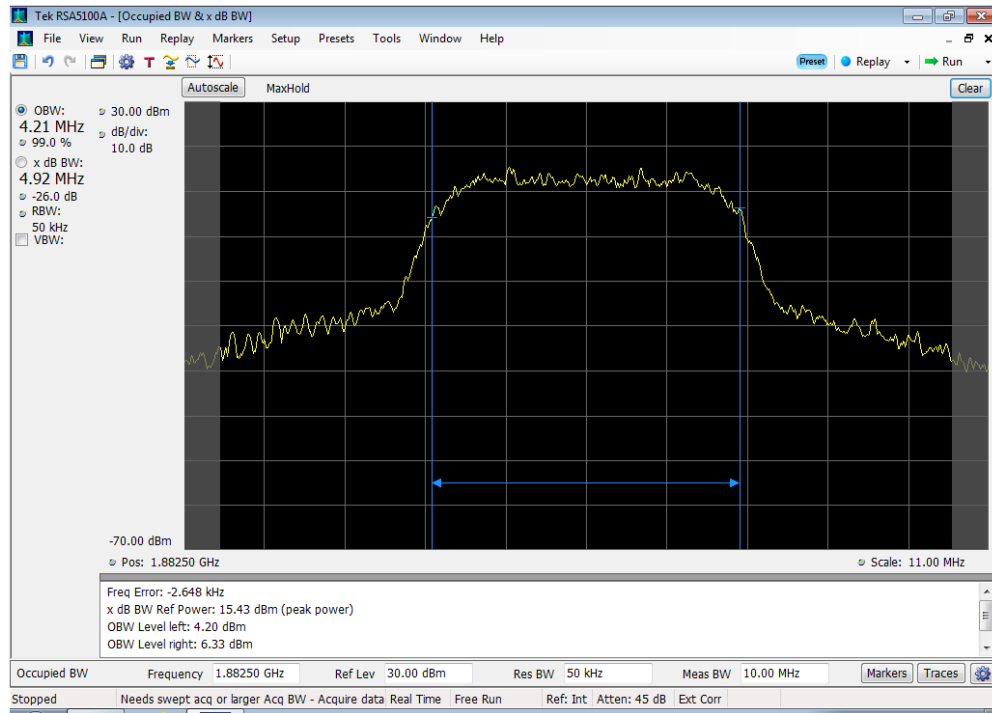
OCC BW_AWGN_B25_DL_Input Signal



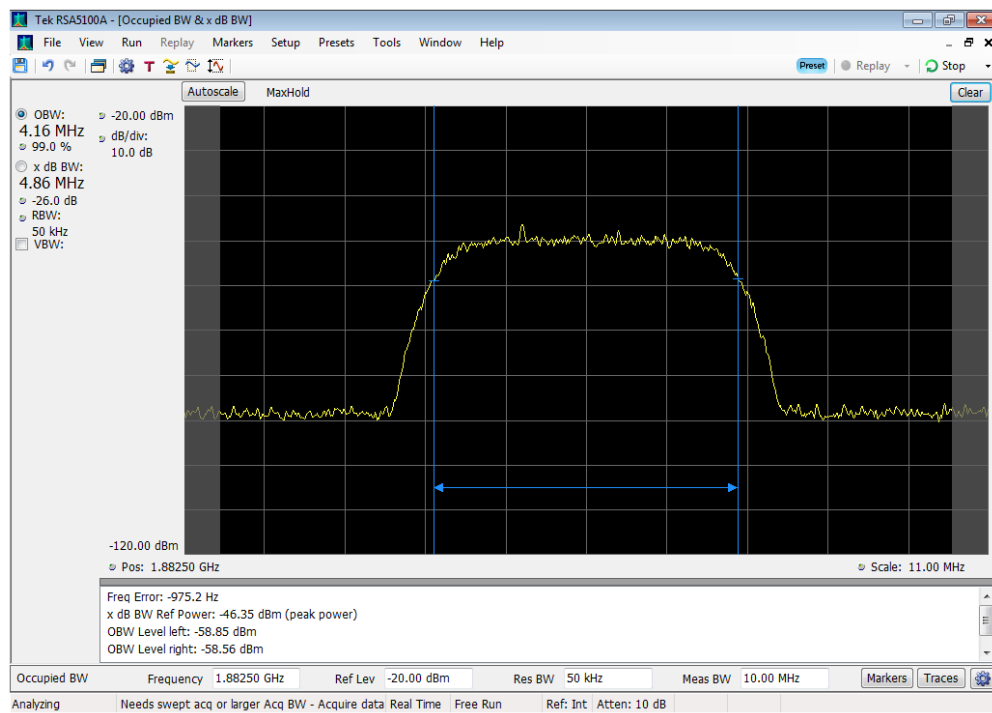
OCC BW_AWGN_B25_UL_Dedicated_S2 to D2



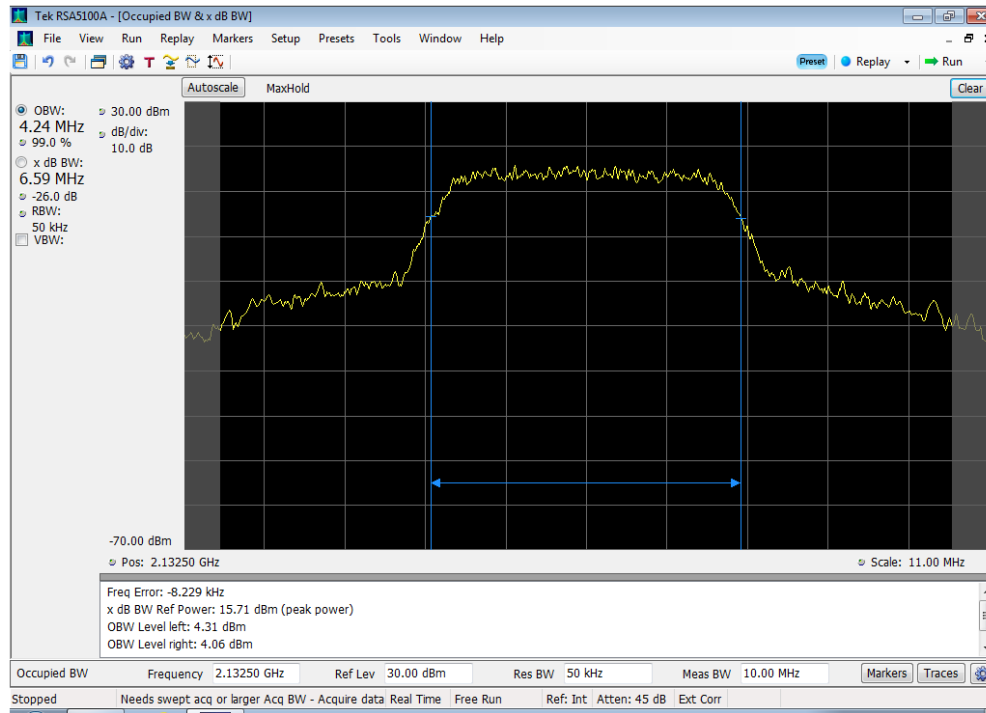
OCC BW_AWGN_B25_UL_Dedicated_S2 to D2_Pin + 3 dB



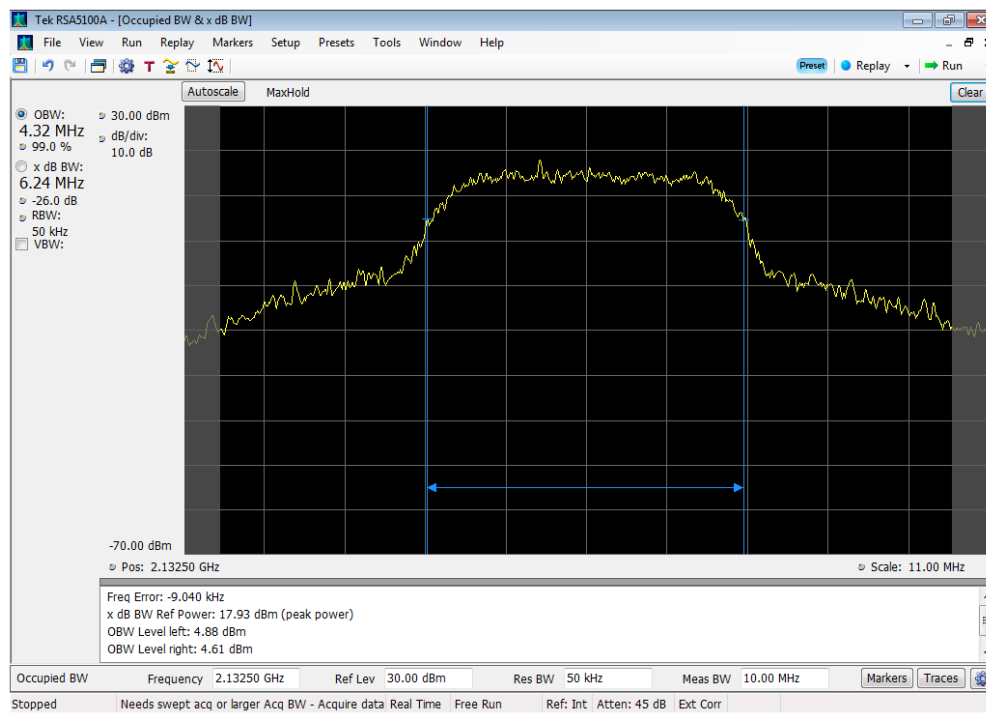
OCC BW_AWGN_B25_UL_Input Signal



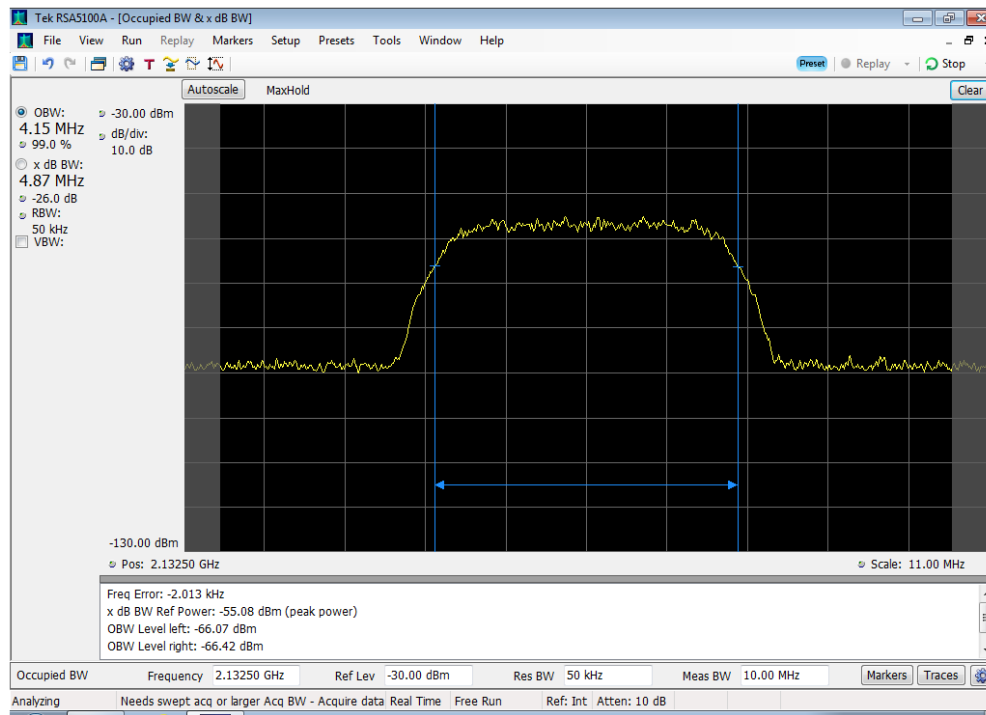
OCC BW_AWGN_B4_DL_Dedicated_D2 to S2



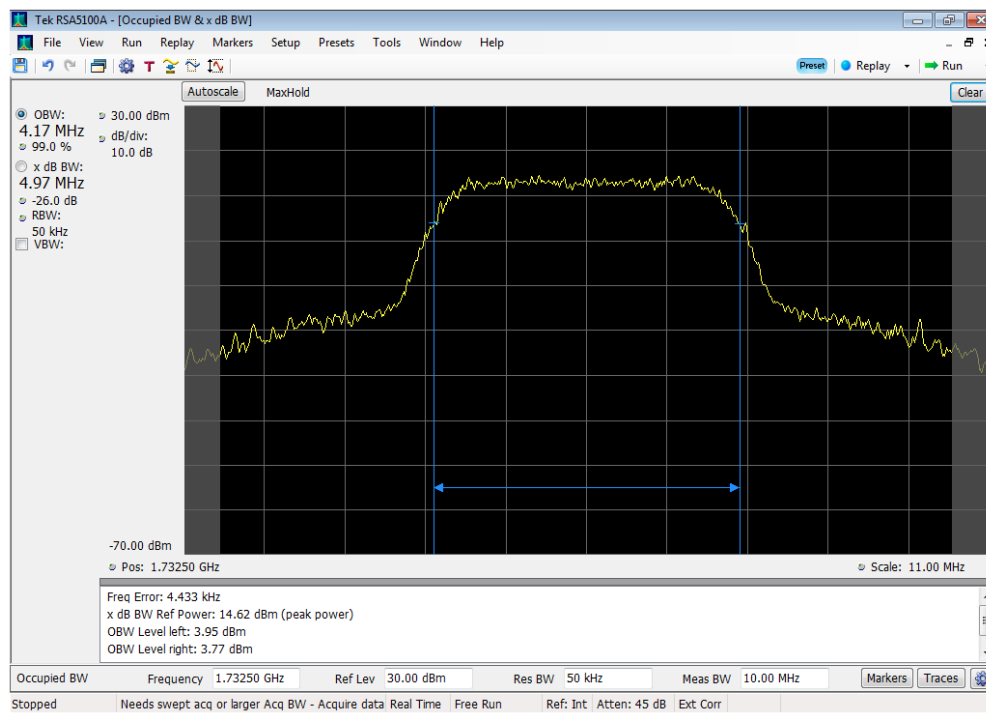
OCC BW_AWGN_B4_DL_Dedicated_D2 to S2_Pin + 3 dB



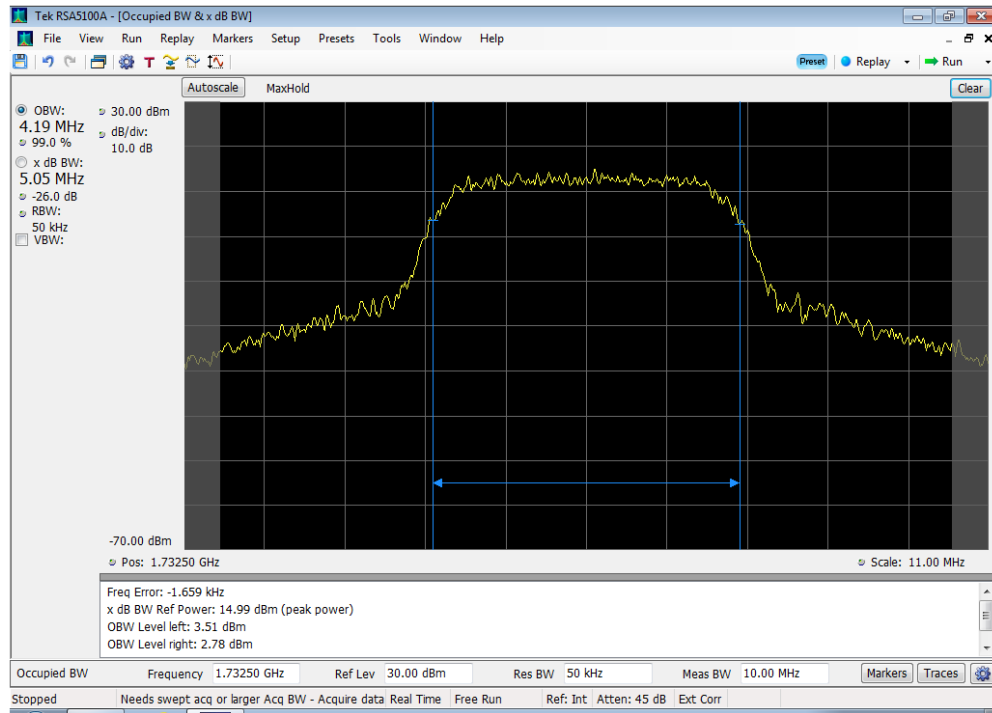
OCC BW_AWGN_B4_DL_Input Signal



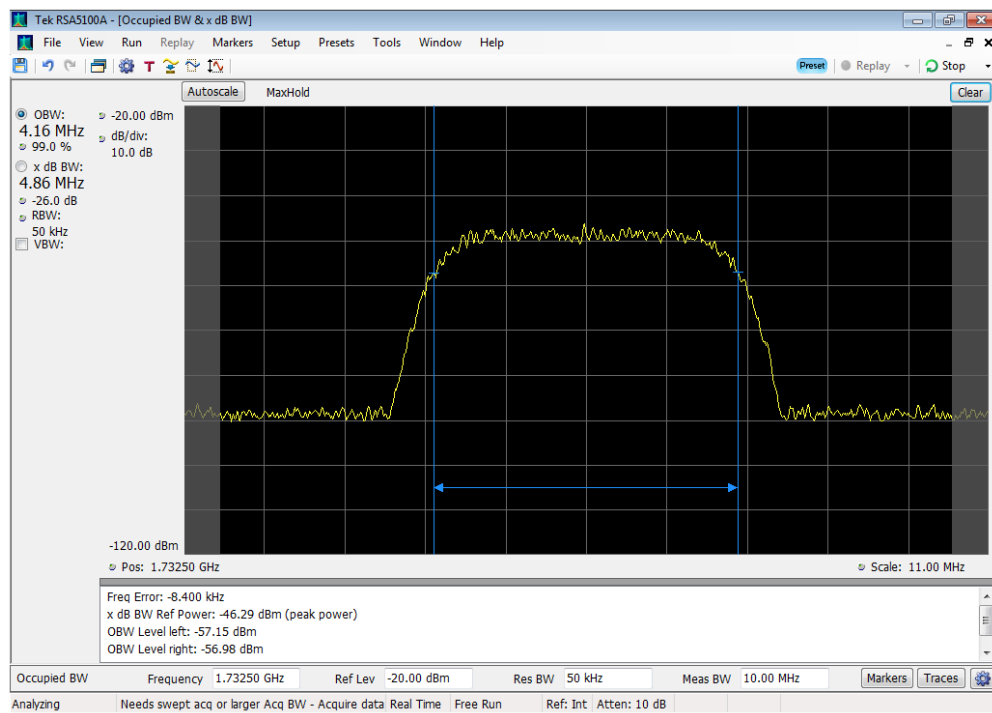
OCC BW_AWGN_B4_UL_Dedicated_S2 to D2



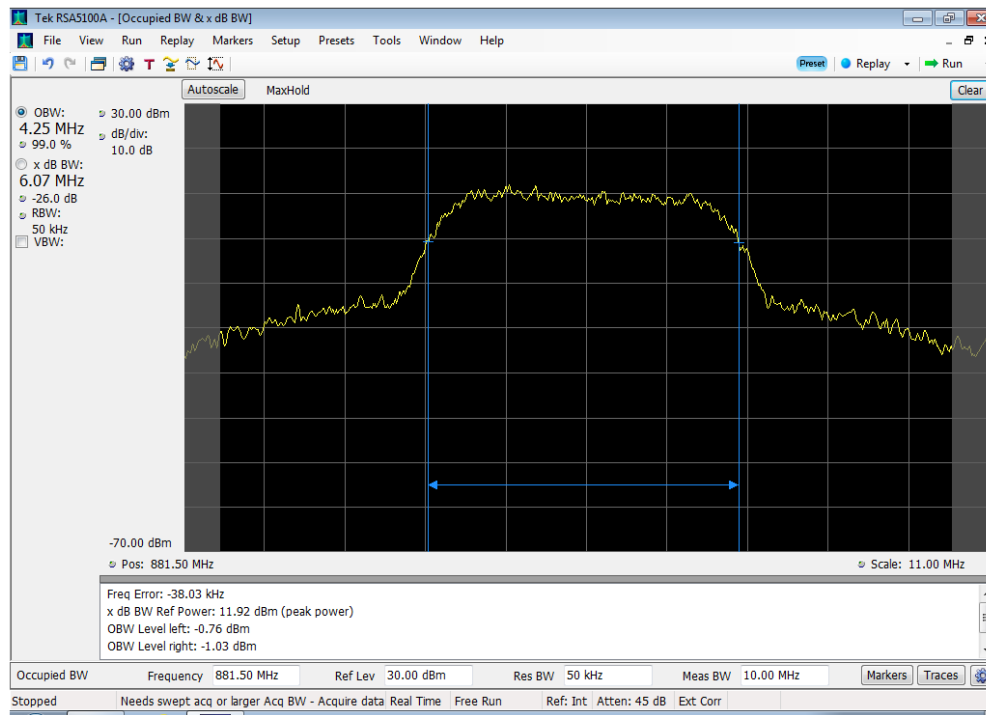
OCC BW_AWGN_B4_UL_Dedicated_S2 to D2_Pin + 3 dB



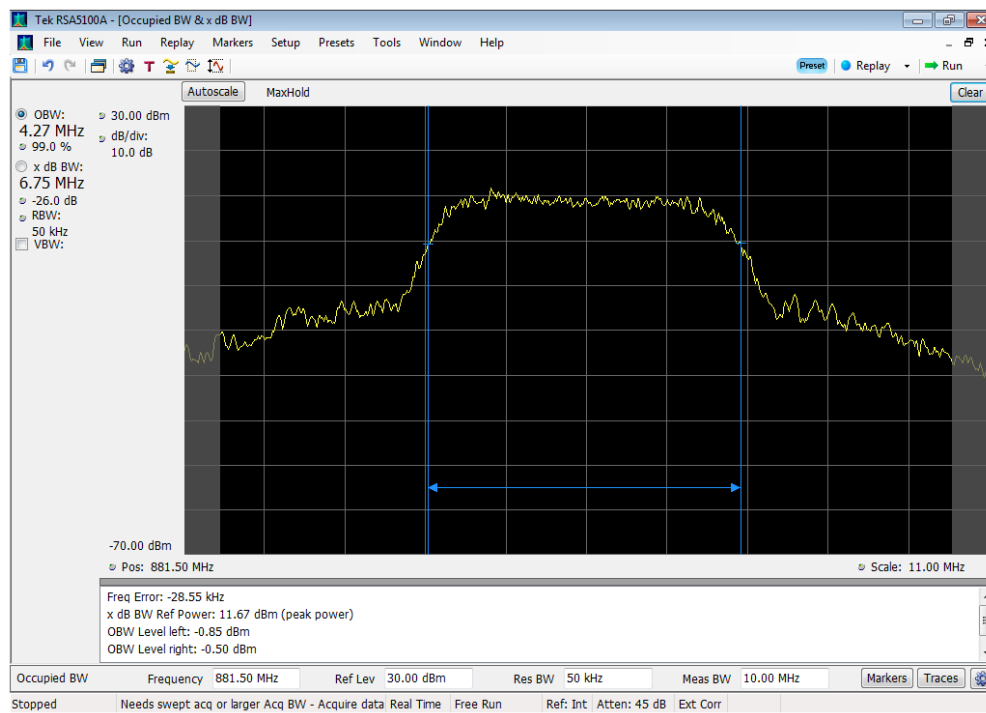
OCC BW_AWGN_B4_UL_Input Signal



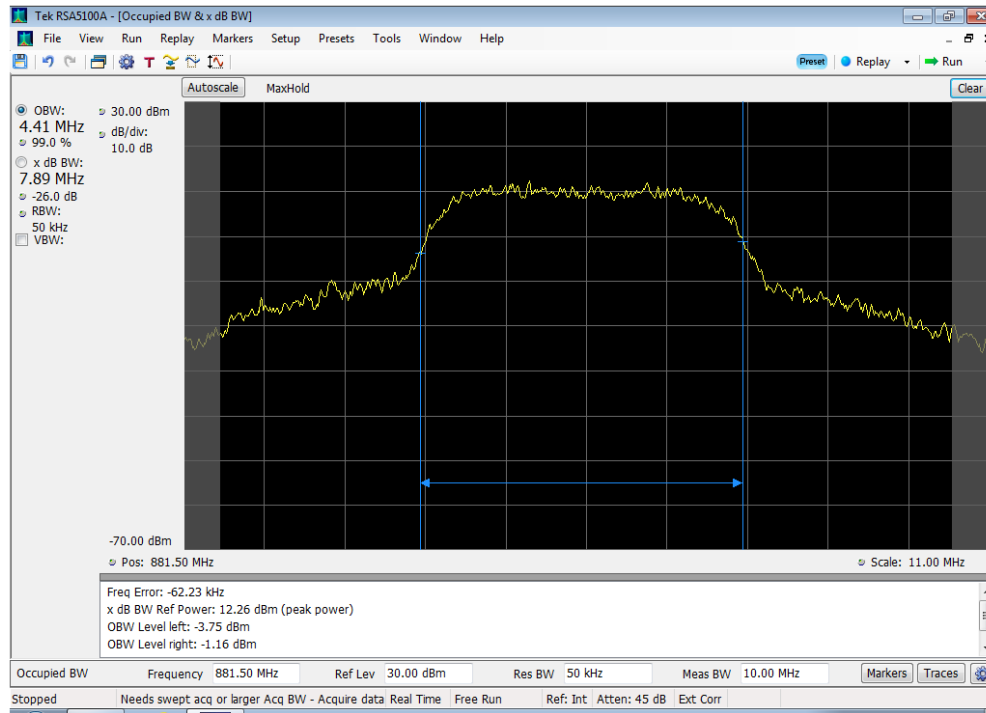
OCC BW_AWGN_B5_DL_Common_D2 to S2



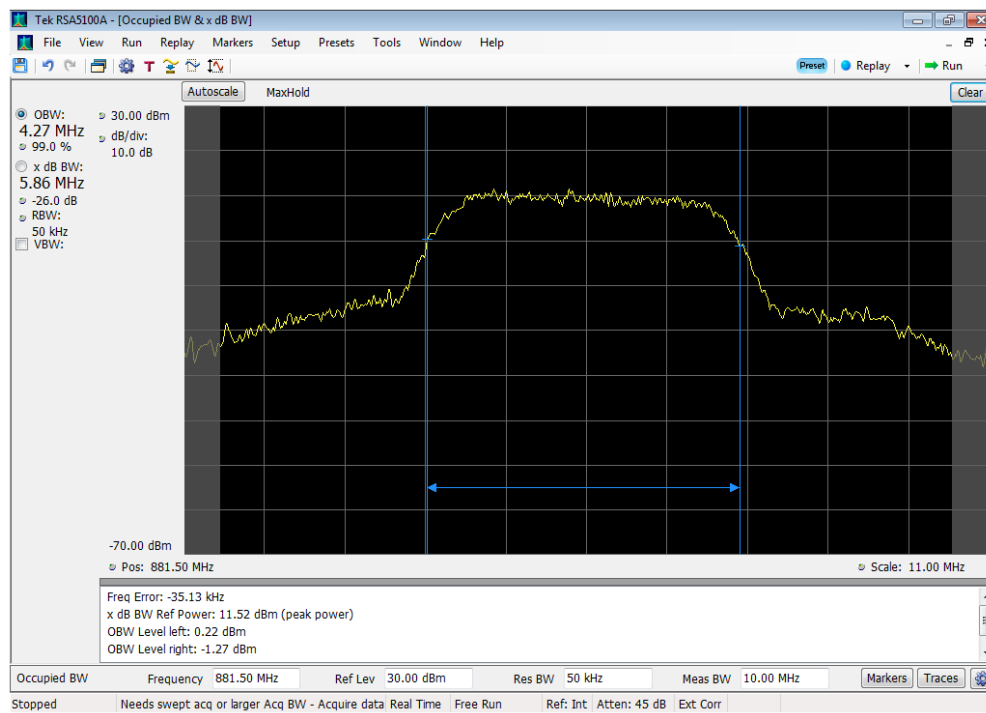
OCC BW_AWGN_B5_DL_Common_D2 to S2_Pin + 3 dB



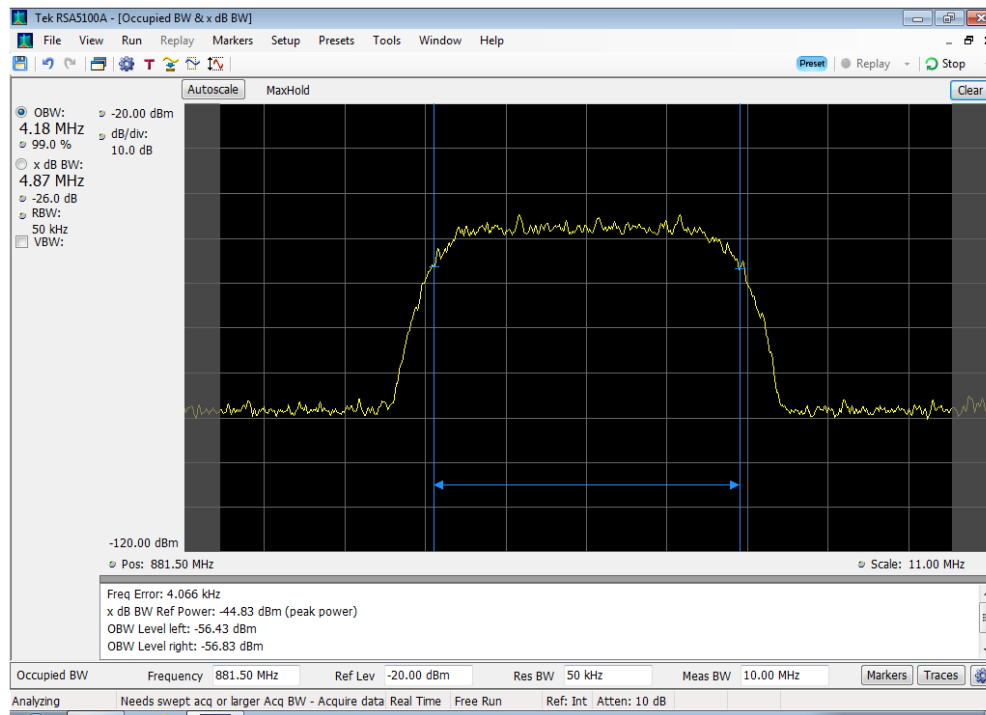
OCC BW_AWGN_B5_DL_Dedicated_D1 to S2



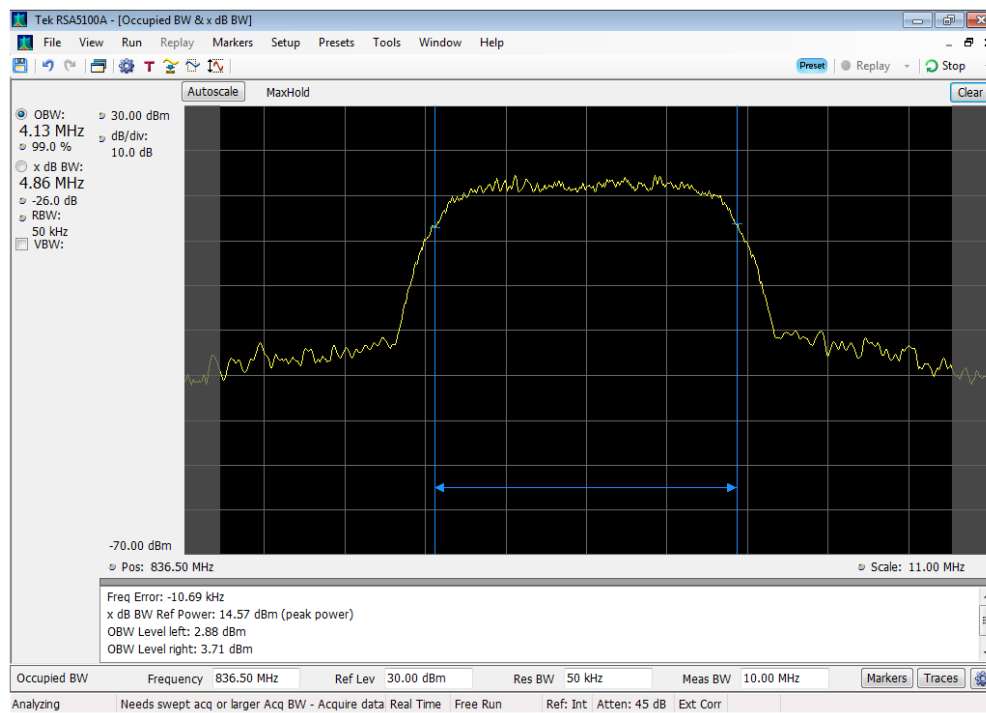
OCC BW_AWGN_B5_DL_Dedicated_D1 to S2_Pin + 3 dB



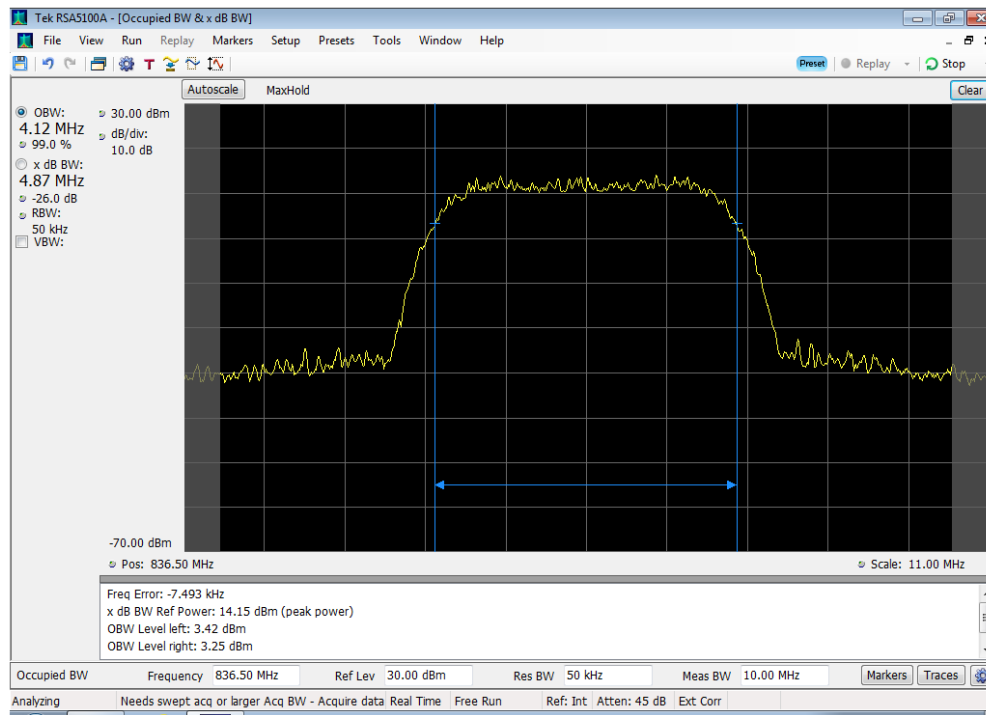
OCC BW_AWGN_B5_DL_Input Signal



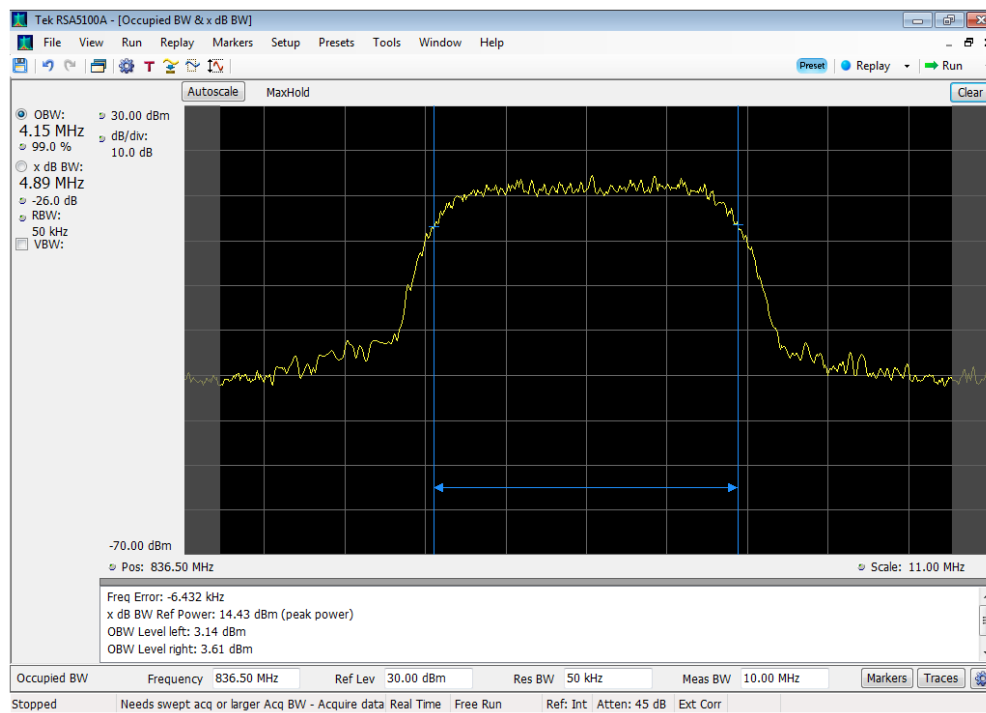
OCC BW_AWGN_B5_UL_Common_S2 to D2



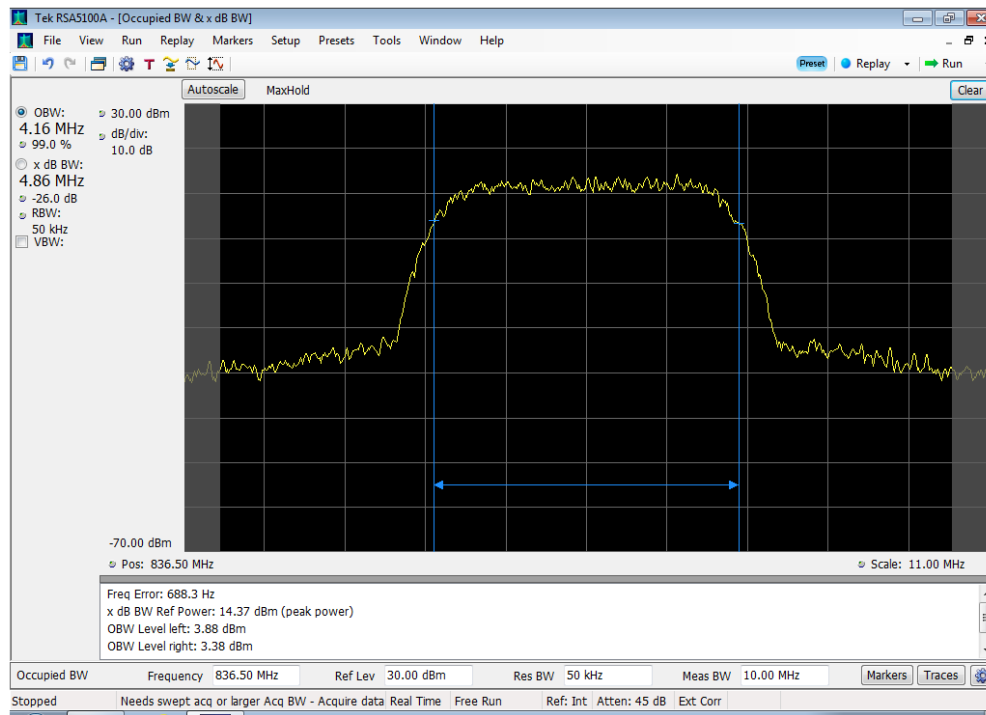
OCC BW_AWGN_B5_UL_Common_S2 to D2_Pin + 3 dB



OCC BW_AWGN_B5_UL_Dedicated_S2 to D1



OCC BW_AWGN_B5_UL_Dedicated_S2 to D1_Pin + 3 dB



OCC BW_AWGN_B5_UL_Input Signal

