ATTACHMENT F – BLOCK DIAGRAM(S)

Block Diagram

Main B/d

- D-Sub 15P
- DDC
- H.V
- Schmitt Trigger (74HCT14)
  - Hsync2
  - Vsync2
  - Hsyncm
  - Vsyncm
  - Hsync2
  - Vsync2
- (MICOM) (MYS212M)
- EEPROM 24LC08
- CON 5P
- To Audio B/d
- LCD Interface 20Pin Connector (12507WR-20P)
  - RIN
  - RCLK
- LVDS (LVDS83)
  - RGB
  - +3.3V
- ADC/PLL/Scaler (gmZAN1)
  - RGB
  - Control
  - SCL
  - SDA
  - ST5V
  - Audio Mute (Audio B/d)
  - 3.3VD 3.3V_PL 3.3V_AD
- Inverter
- CON 10P
  - 12V(Audio B/d)
- DC Jack
  - 12V
- ST5V
- Power Part DC/DC Converter (L4973)
  - ST5V
- Switch FDC6325L
- Switch FDC6325L
- Regulator BA033 — 3.3VD
- Regulator BA033 — 3.3V_AD
- Regulator BA033 — 3.3V_PL
- Regulator BA033 — +3.3V
- Module Power 3.3V

Audio B/d

- Headphone
- MIC In
- MIC Out
- Audio In
- Audio Amp (TDA7446L)
  - 12VA
  - Audio Mute
- Audio Processor (TDA7449)
  - SDA SCL
- CON 5P
  - 12V
  - GND
  - SCL
  - SDA
  - Audio Mute
  - GND
  - 12VA
  - 78M08
  - 8VA