

2009-08-26

Reference:

Attn: Reviewing Engineer

Federal Communications Commission
7435 Oakland Mills Road
Columbia, MD 21046

Declarations for using different system clocks on the cB-0925-01 module
FCC ID: PVH0925
IC: 5325A-0925

The cB-0925-01 module is available in two versions of the Bluetooth Host Processors with different system clocks. The processors are from the same processor family with same footprint etc. All components except the processor are identical (radio, oscillator etc).

- cB-0925-01-0-0x:
The processor (STM32F100RCT6) has less memory and is running on a lower system clock (20MHz) which reduce the performance and functionality. The oscillator feeding the processor is the same but the internal PLL multiplier is 2.0.
- cB-0925-01-1-0x:
The processor (STM32F101RCT6) has more memory and is running on a higher system clock (35MHz) which increase the performance (worst case) and functionality. The oscillator feeding the processor is the same but the internal PLL multiplier is 3.5.

The verification measurements are performed at the connectBlue test laboratory.

We (connectBlue) declare by this document that the differences of radiated emissions in the 1MHz-1GHz frequency range for the two system clock versions of the cB-0925-01 module is not measurable and only measurements on the worst case module version (cB-0925-01-1-0x with the highest system clock and performance) are required in an accredited test laboratory.

Regards



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