

FCC ID: PV6LF-DISHTX

Circuit Description

The 444MHz transistor oscillator drives the base of Q2 the final/buffer amplifier. The modulation provided by IR controller. The output of Q2 has the matching network consisting of C6 and C3 that limit the harmonic content and effect the proper coupling of the antenna to the output stage.

Antenna, Ground and Power Source

There is no external antenna, antenna build in on PCB. There is no external ground connection. The ground is only that of the printed circuit board. Electric current is supplied by a 12Volt ("A23") primary storage cell.