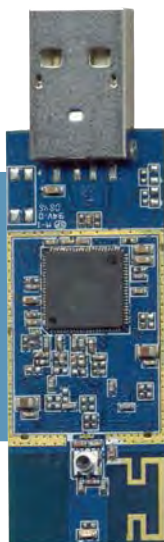


Note: Photo in this brochure may deviate a bit from the actual design used for this FCC application, see internal photographs exhibit. This brochure was added for informative purposes only.



# RT3572

802.11n 2T2R USB Single Chip



- CMOS single chip
- Blazing speed at 300 Mbps PHY rate
- Low power

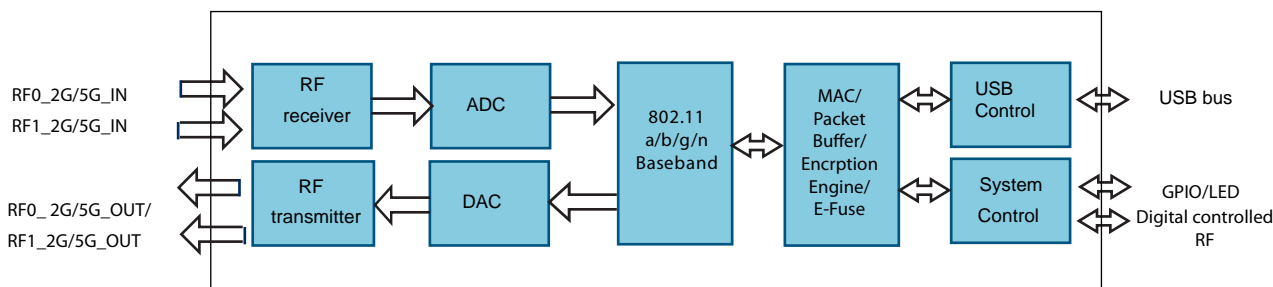
## Overview

The RT3572 single chip is a highly integrated MAC/BBP and 2.4/5GHz RF single chip with support for 300 Mbps PHY rate. It fully complies with IEEE 802.11n draft 4.0 and IEEE 802.11 a/b/g, features rich wireless connectivity at high standards, and delivers reliable, cost-effective throughput from an extended distance. Optimized RF architecture and baseband algorithms provide superb performance with low power consumption. An intelligent MAC design deploys a highly efficient USB engine and hardware data processing accelerators without overloading the host processor. The RT3572 is designed to support basic features related to security, quality of service and international regulations. It gives end users the greatest performance at anytime, and in any circumstance.

## Features

- USB 2.0
- Built in e-fuse memory
- 2T2R with 300Mbps PHY rate
- 20MHz/40MHz bandwidth support
- Legacy and High Throughput modes
- Reverse Data Grant (RDG)
- Maximum Likelihood Decoding (MLD)
- Maximal Ratio Combining (MRC)
- Security: WEP / TKIP / AES / WPA / WPA2 / WAPI
- WPS - PIN, PBC
- QoS - WMM, WMM-PS
- International Regulation - 802.11 d+h
- Multiple BSSID support
- Cisco CCX5 support
- Bluetooth Co-existence
- MIMO power saving support
- Supported Operating Systems: Windows XP 32/ 64, 2000, Vista 32/ 64, Linux, and Macintosh
- Package: 10 x 10 mm 88-pin QFN

## RT3572 Block Diagram



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