MARS 2.4GHz

Description of transmitter and receiver architecture

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1 Overview

The MARS system is a frequency hopping system.

2 Baseband

The baseband circuit consists primarily of a DECT baseband processor. A 9.216 MHz crystal drives it.

The baseband processor handles all audio, signal and data processing needing in a handset. The baseband processor includes CODEC and ADPCM coder/decoder.

The baseband processor also include a Burst Mode Controller that performs the generation and decoding of the frames used. The BMC also generates the control signals for the radio part. The bit rate is 1.024 Mbit/s.

The baseband processor has a gaussian filter to perform the gaussian filtering of the transmitted data. The output signal (TRADAT) is an analog signal.

The baseband processor has a comparator to transform the analog RECDAT signal to a digital signal. The data and clock recovery circuit extracts the timing information from the received signal.

3 Transmitter

The MARS system uses the open loop modulation scheme. In the guard band between two slots, (128 bittimes) the synthesizer is programmed with the new data and the PLL obtains phase lock on the desired frequency. Just before the PA is powered up the synthesizer is powered down and the phase detector output is tri-stated. Thus, the VCO runs freely during the transmission with the frequency determined by the voltage on the loop capacitors.

It is now possible to modulate the VCO with an analog signal (TRADAT) at its modulation input.

The VCO signal is doubled in the RF chip. After the doubler, a driver amplifier is used to obtain the correct power level for the PA. The doubler and driver amplifier is turned on in the guard band during the lock-in time. The PA is only turned on when a burst is transmitted. The power up and power down of the PA is slowed down to reduce the generated switching spectrum.

There is a TX-RX switch to switch the antenna between the PA and the LNA. Between the TX-RX a BPF (ceramic) is used to perform some filtering.

In the handset an internal inverted F antenna is used. This antenna has a gain of omni-directional antenna pattern.

4 Receiver

The front-end consists of the antennas, bandpass filter and TX-RX switch as described in the transmitter section. There is also a single transistor LNA before the second bandpass filter (ceramic).

The mixer is internal in the RF chip. The LO signal comes from the doubler used in the transmitter.
The VCO is band-switched, so a different mode of the VCO is used for RX and TX. The PLL and doubler are common for the TX and RX path. This is possible as the transceiver only operates in either TX or RX mode.

The down-converted signal has an frequency of 110.592 MHz. The IF signal is filtered in a SAW filter with a approx. 1.2 MHz bandwidth. The SAW filter gives most of the channel selectivity. After the filter a limiter and a discriminator is used. The discriminator has a LC tank centered at the IF.
The demodulated signal is lowpass filtered with RC filter to obtain a better channel selectivity and noise reduction. The lowpass filter has a cut off frequency of approx. 500 kHz.

A sample and hold circuit is used to extract the DC offset of the received data to improve the comparator performance. The received data is sampled in the preamble where the alternating sequence ensures a correct average value.

5 Frequencies

The crystal used has a frequency of 9.216 MHz. The microcontroller runs on this frequency or a fraction of this.

During transmission is the VCO half the transmitted frequency.

The IF frequency in the receiver is 110.592 MHz. The MARS system uses the lower LO signal. The same VCO is signal is doubled and used as LO signal. The frequencies in the system are as follows.

The channel spacing is 1.024 MHz

All frequencies are given in MHz

Channel	TX VCO	TX freq.	RX VCO	RX LO	IF

	freq.		freq.	freq.	
0	1200,640	2401.280	1145,344	2290,688	110.592
40	1221,120	2442.240	1165,824	2331,648	110.592
78	1240,576	2481,152	1185,280	2370,056	110.592