

# LITE GROUP LTR-16101B (16X/10X/40X) Block Diagram

The block diagram illustrates the architecture of the LTR-16101B optical drive. The system is connected to a **Host** via an **ATAPI Interface Controller**. This controller is part of a **Combo IC** which also includes an **Encoder** and **Decoder**. The **Combo IC** is linked to **DRAM** and a **Servo ASP/DSP 33.86MHZ** block. The **Servo ASP/DSP** is connected to a **Micro-Controller** (20.0MHz) which contains **Flash 256Kbytes** and is also connected to an external **Flash 256Kbytes** memory. The **Micro-Controller** manages the **Write Strategy** and is connected to the **Encoder/Decoder** and **ATAPI Interface Controller**. The **Micro-Controller** is also connected to an **AFEP** (Automatic Focus Error Processor) and an **OPC IC** (Optical Pick-off Control IC). The **AFEP** and **OPC IC** are connected to the **Stepping Spindle Driver** and the **4-Channel Driver**. The **Stepping Spindle Driver** is connected to the **PWM Spindle Driver**, which in turn drives the **Spindle Motor**. The **Spindle Motor** is connected to the **Optical Head**, which is mounted on the **Optical Disc**. The **Optical Head** is connected to the **AFEP** and **OPC IC**. The **Optical Head** also includes a **PD** (Photodiode) and **LD** (Laser Diode).