

To: <jmartinez@elliottlabs.com> Cc: "Raymond Laforge" <RLAFORGE@fcc.gov>
Subject: Re: Part 24 question.

If the IF card / module is designed and intended to be used with the final amplifier, then it is to be tested under the Part 24 limits.

In other words, because the IF card is part of the system architecture (not a stand alone device) it is to be tested under the Part 24 / PCS limits.

>>> Juan Martinez <jmartinez@elliottlabs.com> 10/05/01 07:02PM >>>

Mr. Coperich,

Attach please find what you requested it is label "IF Card Block Diagram". I have also attach a file called "Amplifier Block Diagram" this will show you how the IF card and the PA interconnect between each other. Thanks in advance for your assistance.

Regards

Juan Martinez

Elliott Laboratories

At 08:19 AM 10/2/2001 -0400, you wrote:

>Please send me a block diagram so I can be sure of what you are describing.

>

> >>> Juan Martinez <jmartinez@elliottlabs.com> 10/01/01 07:51PM >>>

>Hello Mr. Coperich,

>

>I have recently tested a Radio modem that provides an IF of 122.28 MHz and

>an RF PCS operation to an amplifier, which operated under FCC 24E

>section. During per-liminary testing the IF's 122.28 MHz was producing

>harmonics over the FCC 15 class A limits. My question is, since the IF is

>used to operate the PCS amplifier does the harmonic content of the IF need

>to be tested to FCC 15 unintentional or the FCC 24E?

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>Thank you in advance for your time.

>

>Regards,

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>

>Juan Martinez

>Elliott Laboratories