

5 Block Diagram

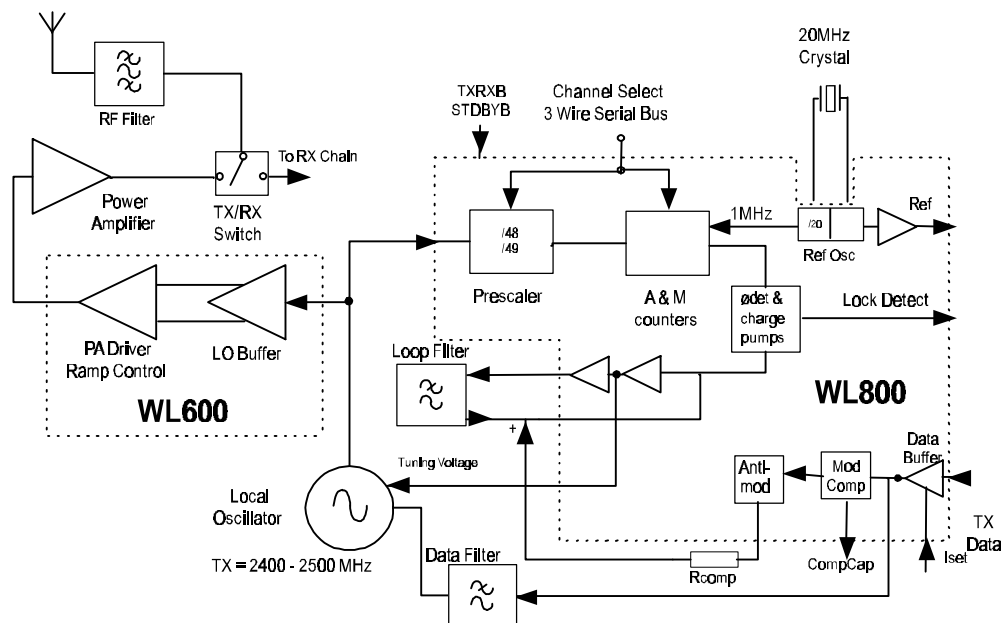


Fig1 - Transmitter circuitry

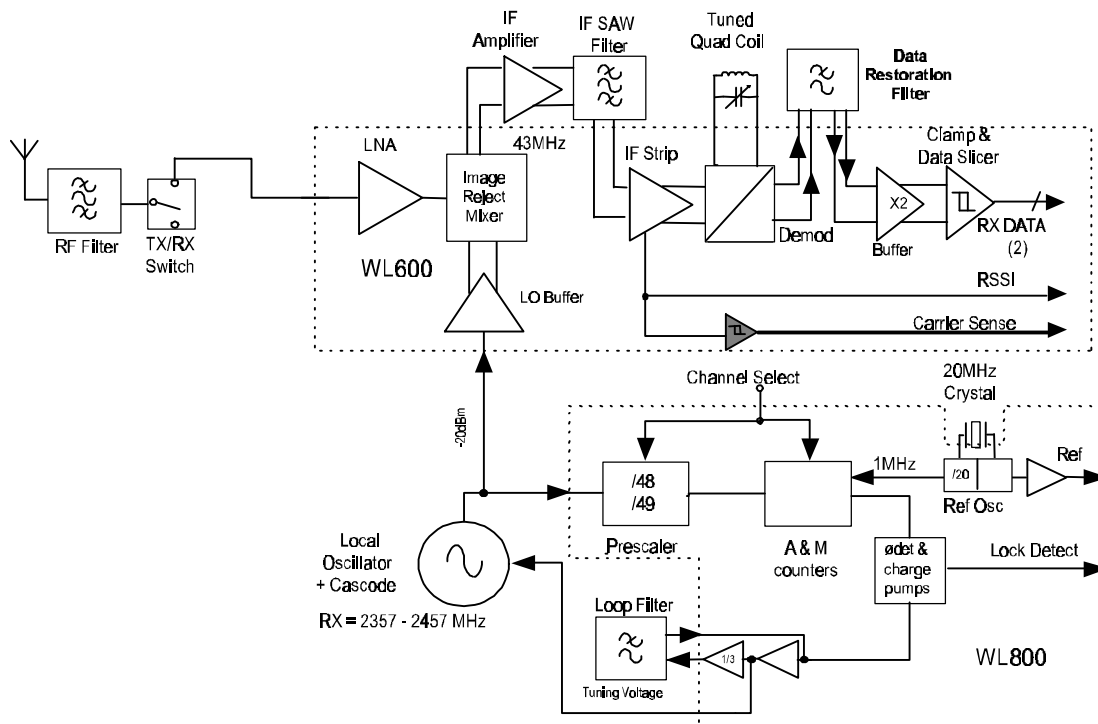


Fig 2 - Receiver circuitry