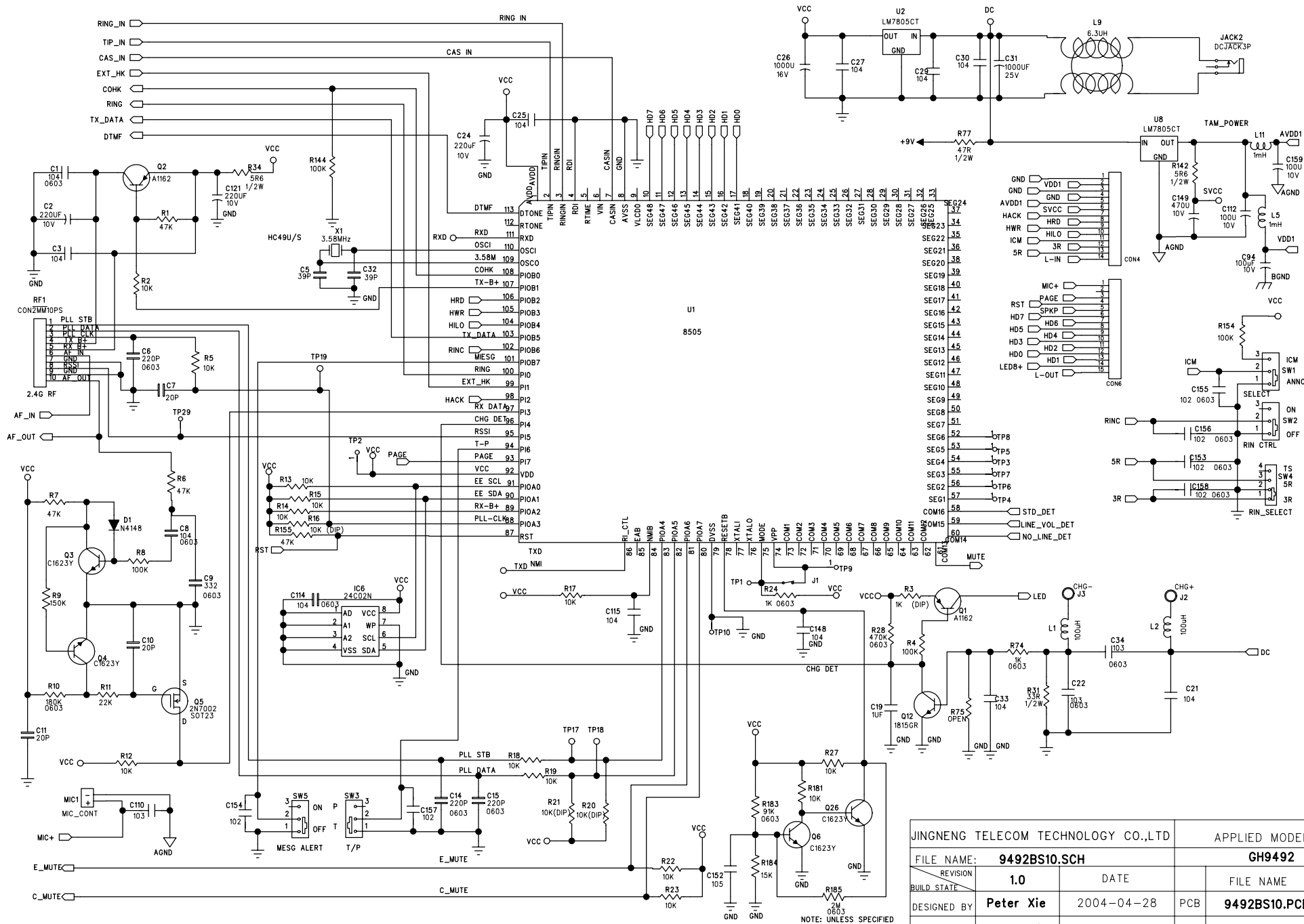
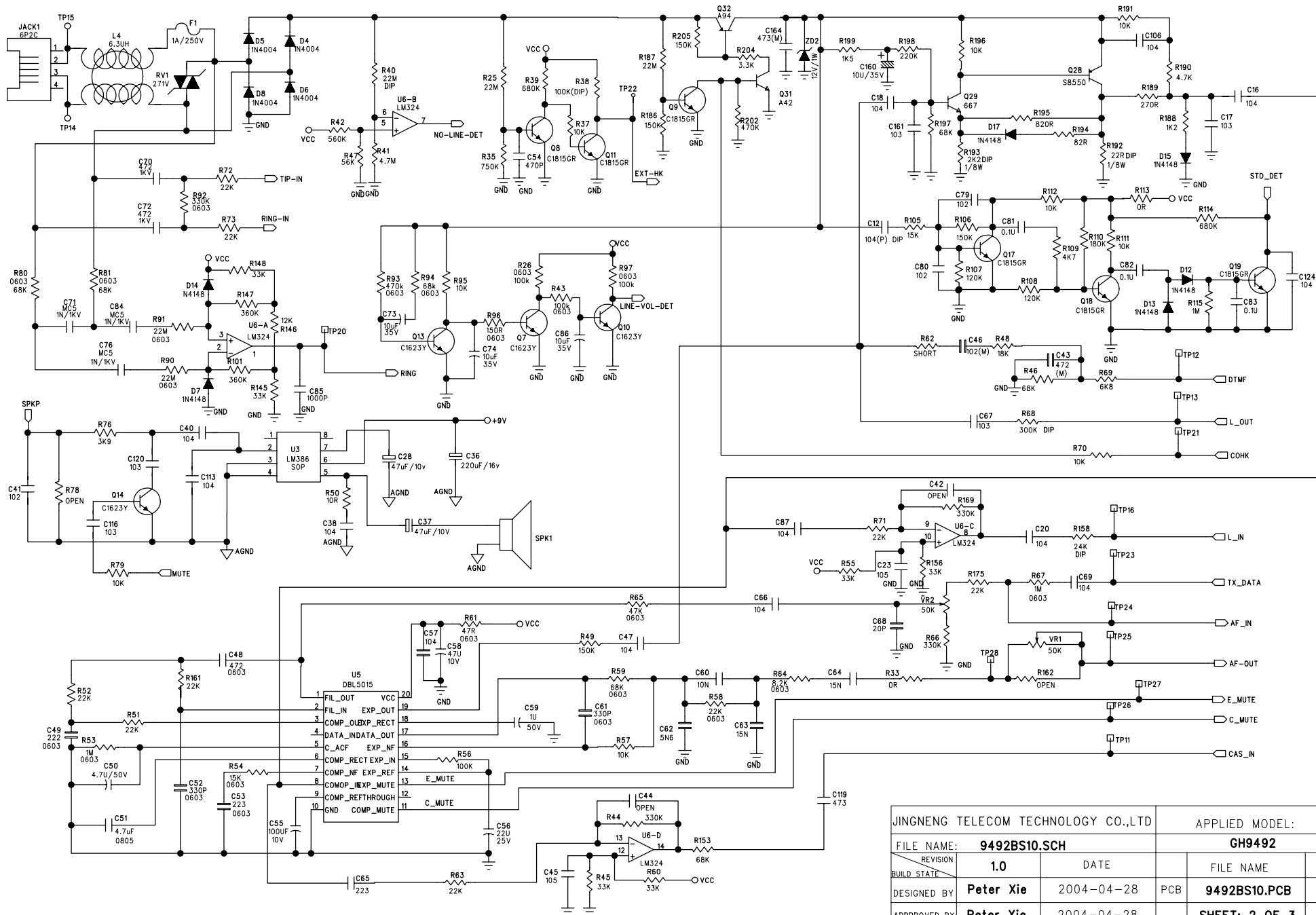
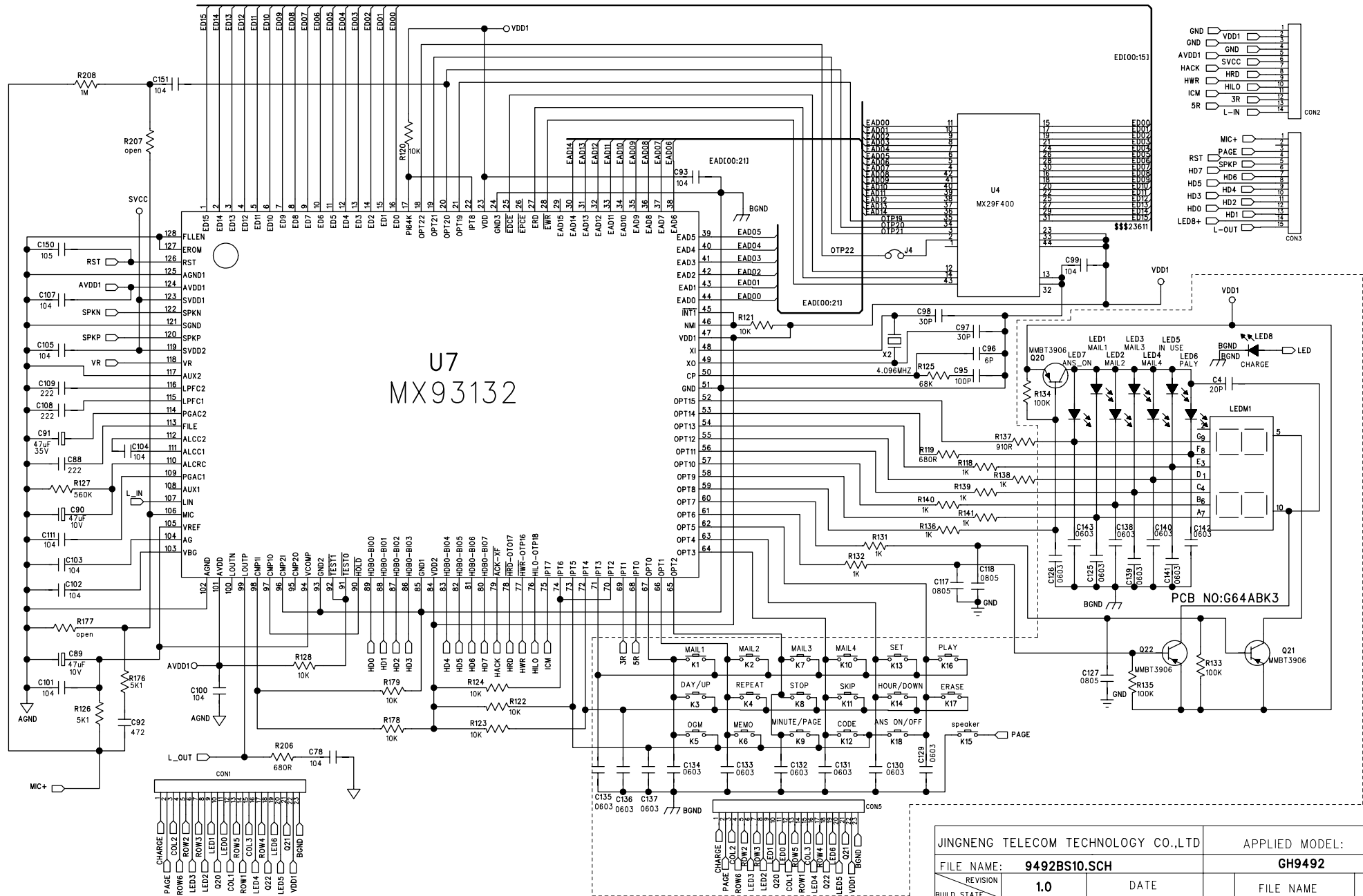




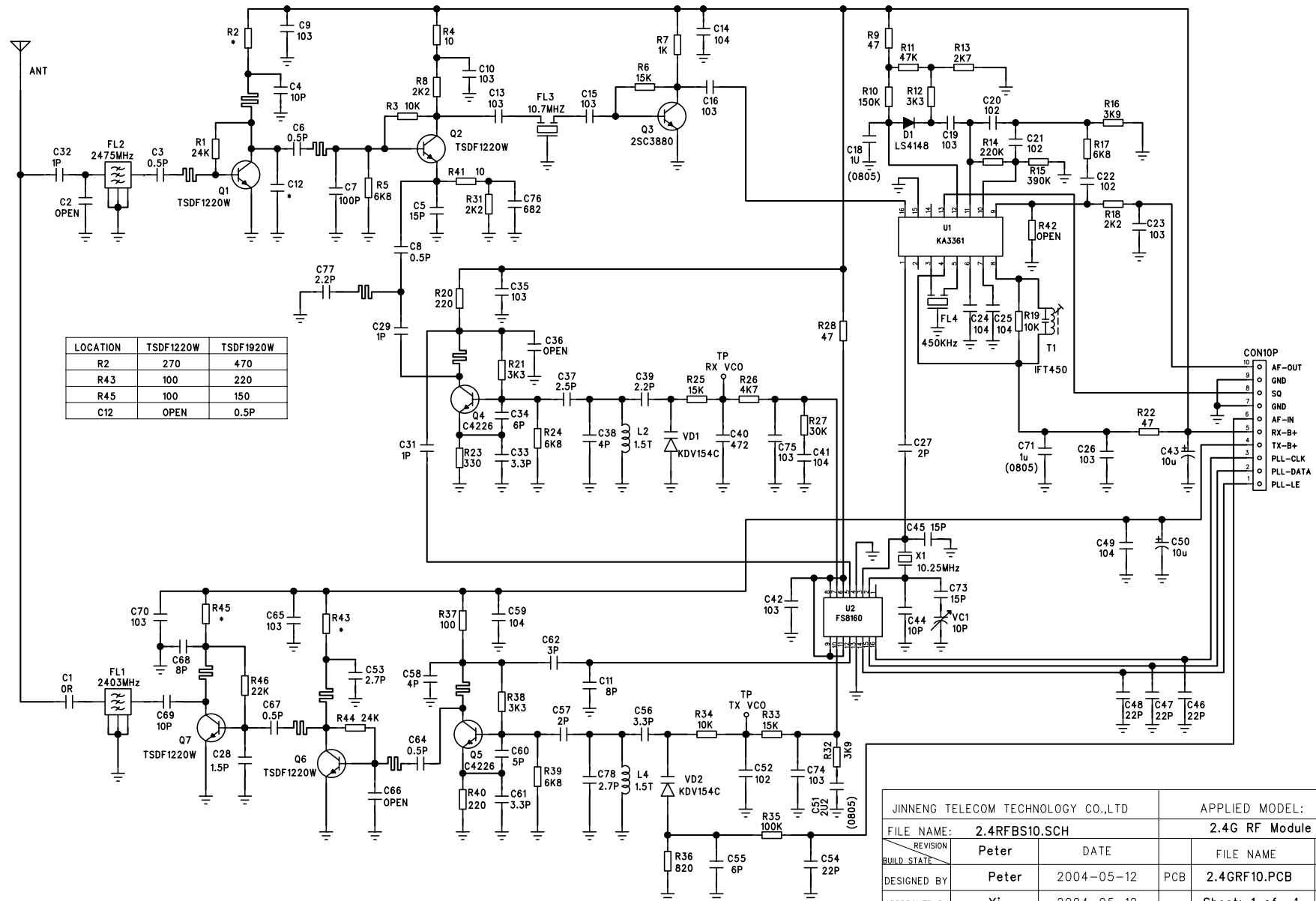
| JINGNENG TELECOM TECHNOLOGY CO.,LTD |             |            |     | APPLIED MODEL: |     |
|-------------------------------------|-------------|------------|-----|----------------|-----|
| FILE NAME: 9492BS10.SCH             |             |            |     | GH9492         |     |
| REVISION                            | 1.0         | DATE       |     | FILE NAME      | REV |
| BUILD STATE                         | DESIGNED BY | 2004-04-28 | PCB | 9492BS10.PCB   | 1.0 |
| APPROVED BY                         | Peter Xie   | 2004-04-28 |     | SHEET: 1 OF 3  |     |

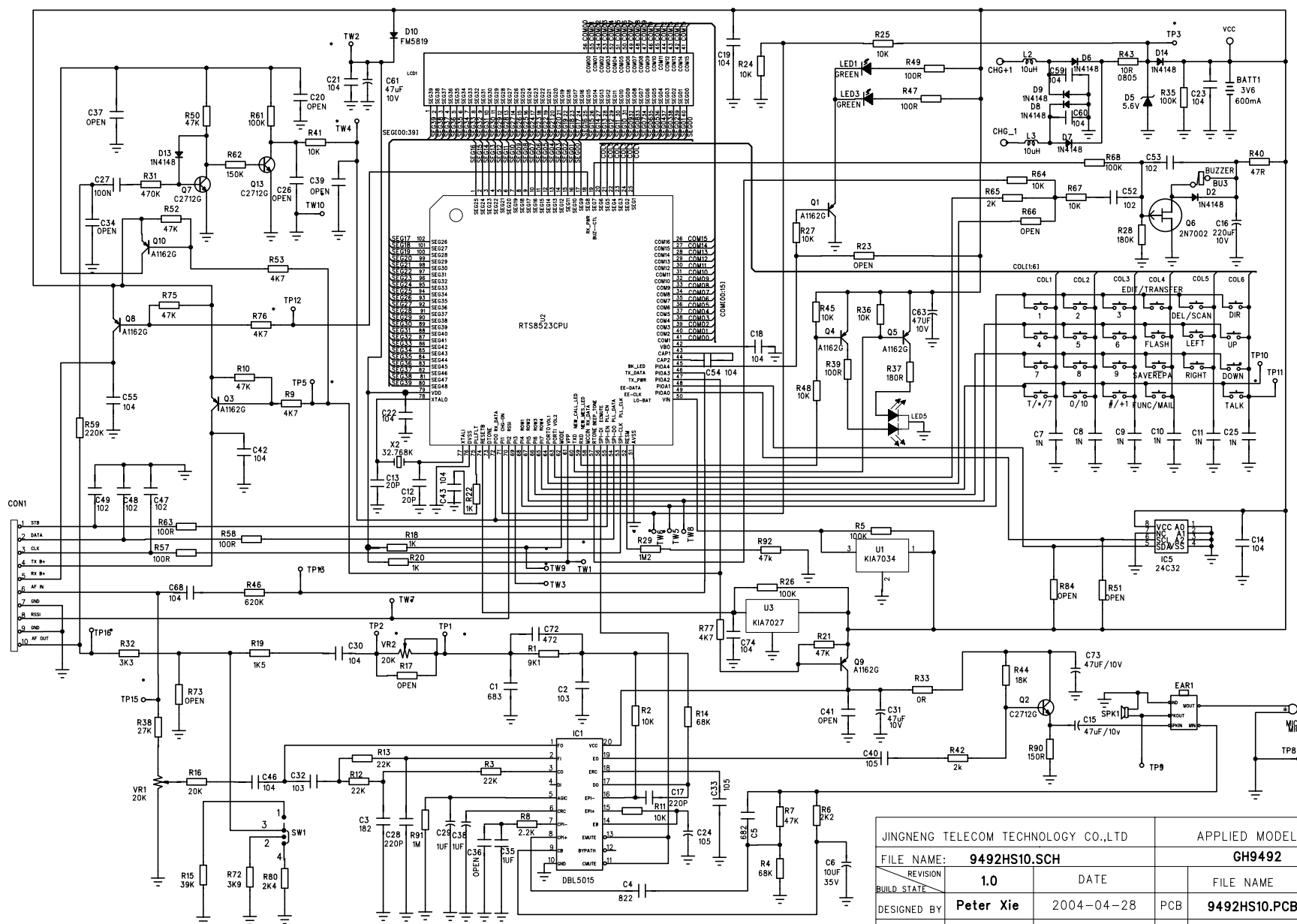


| JINGNENG TELECOM TECHNOLOGY CO.,LTD |           |            |     | APPLIED MODEL: |     |
|-------------------------------------|-----------|------------|-----|----------------|-----|
| FILE NAME: 9492BS10.SCH             |           |            |     | GH9492         |     |
| REVISION                            | 1.0       | DATE       |     | FILE NAME      | REV |
| BUILD STATE                         |           |            |     |                |     |
| DESIGNED BY                         | Peter Xie | 2004-04-28 | PCB | 9492BS10.PCB   | 1.0 |
| APPROVED BY                         | Peter Xie | 2004-04-28 |     | SHEET: 2 OF 3  |     |



| JINGNENG TELECOM TECHNOLOGY CO.,LTD |              |  |  | APPLIED MODEL: |              |
|-------------------------------------|--------------|--|--|----------------|--------------|
| FILE NAME:                          | 9492BS10.SCH |  |  | GH9492         |              |
| REVISION                            | 1.0          |  |  | DATE           | 2004-04-28   |
| BUILD STATE                         | DESIGNED BY  |  |  | PCB            | 9492BS10.PCB |
| DESIGNED BY                         | Peter Xie    |  |  | FILE NAME      | REV          |
| APPROVED BY                         | Peter Xie    |  |  | SHEET: 3 OF 3  | 1.0          |





| JINGNENG TELECOM TECHNOLOGY CO.,LTD |           |            |     | APPLIED MODEL: |     |
|-------------------------------------|-----------|------------|-----|----------------|-----|
| FILE NAME: 9492HS10.SCH             |           |            |     | GH9492         |     |
| REVISION                            | 1.0       | DATE       |     | FILE NAME      | REV |
| BUILD STATE                         |           |            |     | 9492HS10.PCB   | 1.0 |
| DESIGNED BY                         | Peter Xie | 2004-04-28 | PCB | SHEET: 1 OF 1  |     |
| APPROVED BY                         | Peter Xie | 2004-04-28 |     |                |     |

