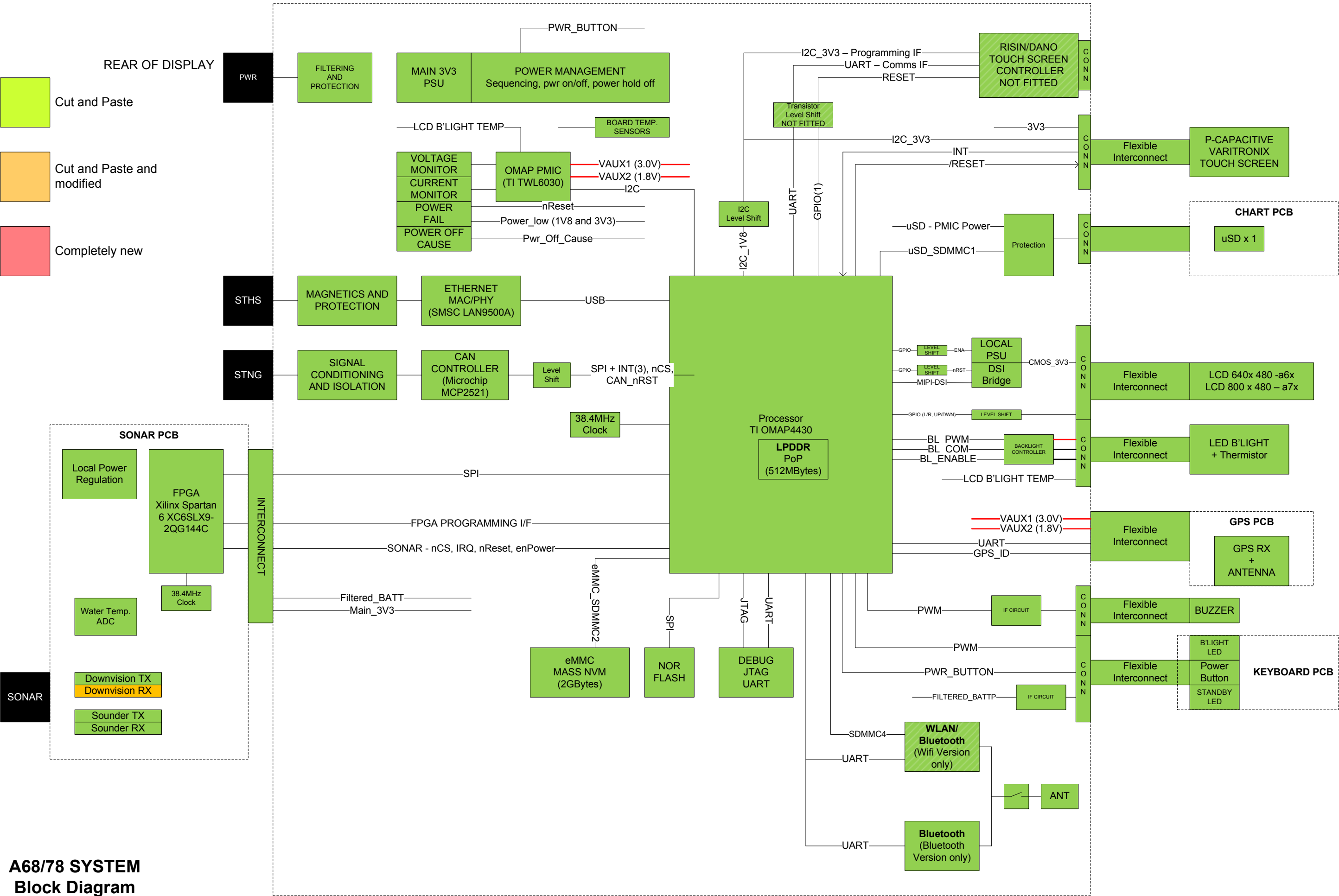


Cut and Paste

Cut and Paste and modified

Completely new



A68/78 SYSTEM
Block Diagram
Version 1
 (23rd Oct 2012)

Change History

Version 2 8.3.2012

Re-drawn to show Sonar Circuitry
on its on board

Version 3 13.3.2012

WLAN/Bluetooth Module added

Version 4 26.3.2012

Decision to use DSI interface and
DSI bridge for LCD. Removed
CMOS interface blocks

Version 5 29.3.2012

LVDS interface to FPGA removed

NOR Flash added

eMMC moved from uSD board to CPU board