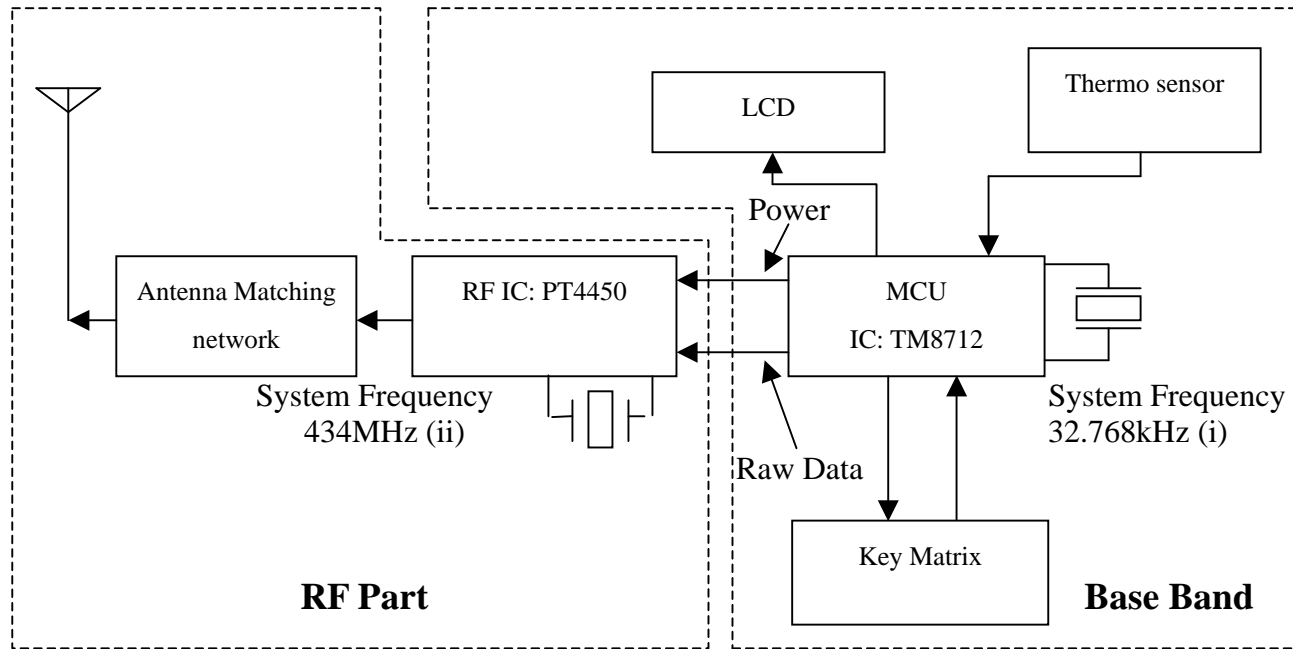


Block Diagram of KAJ260 (Tx)



- (i) The 32.768kHz is the MCU clock frequency for normal operation.
- (ii) The 434MHz is the output of the RF circuit
- (iii) The matching network is formed by L2, L3, L4 & C2, C7, C8