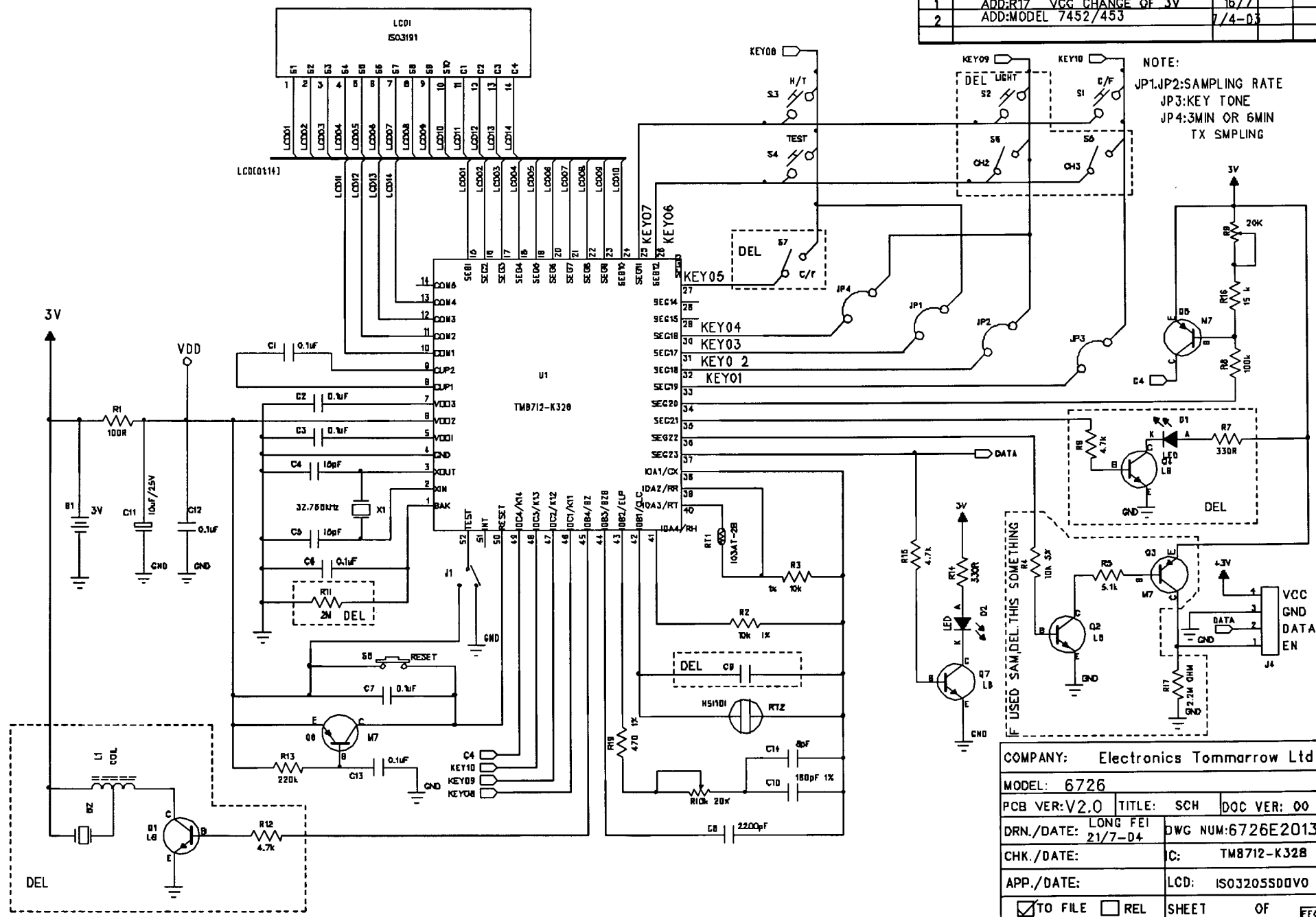
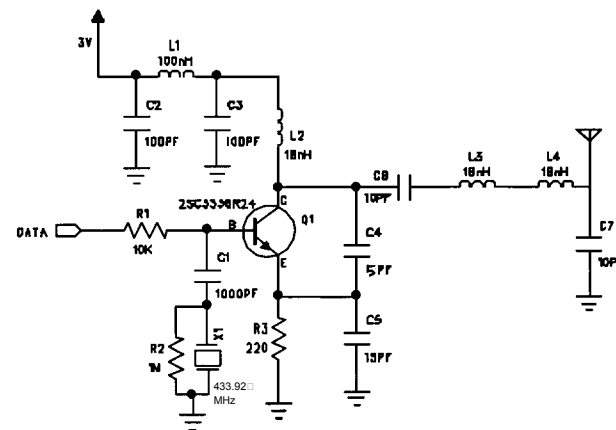


REVISION		DATE:	CHK:	APP:
1	ADD:R17 VGG CHANGE OF 3V	16/7		
2	ADD:MODEL 7452/453	7/4-03		

NOTE:
JP1:JP2:SAMPLING RATE
JP3:KEY TONE
JP4:3MIN OR 6MIN
TX SMPLING



NO.	REVISIONS:	DATE:	CHK:	APP:



COMPANY: Electronics Tomorrow Ltd.			
MODEL: SAW resonator			
PCB VERT2.D		TITLE: S C H	DOC VER: 00
DRN./DATE: 20 28/3		PCB DWG NO:	
CHK./DATE:		PCB P/N:	
APP./DATE:		TOTALING DWG NO:	
☒ TO FILE ☐		SHEET OF:	