

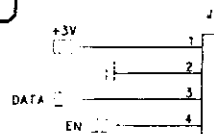
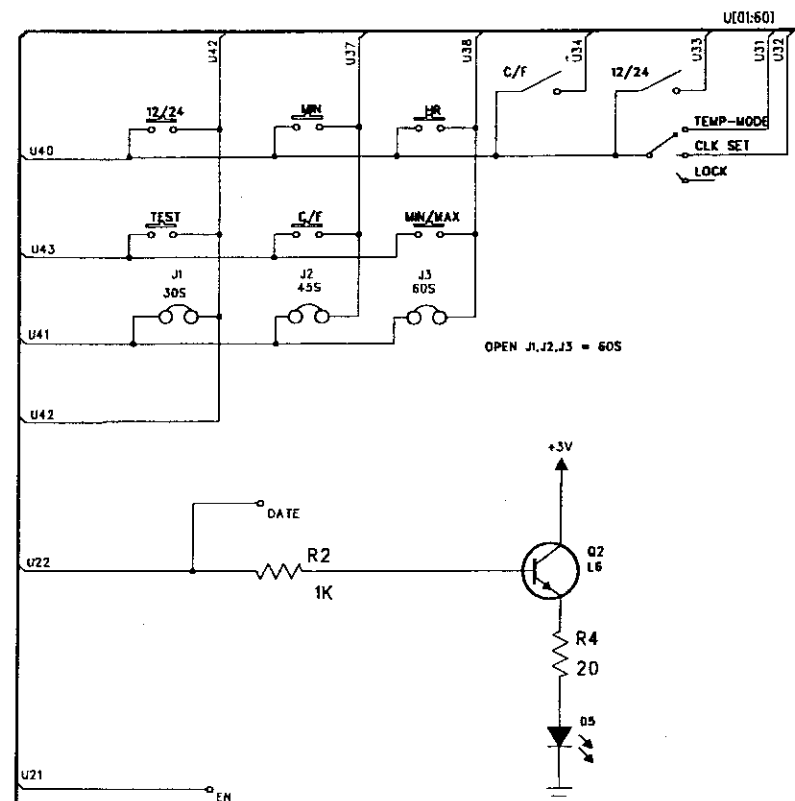
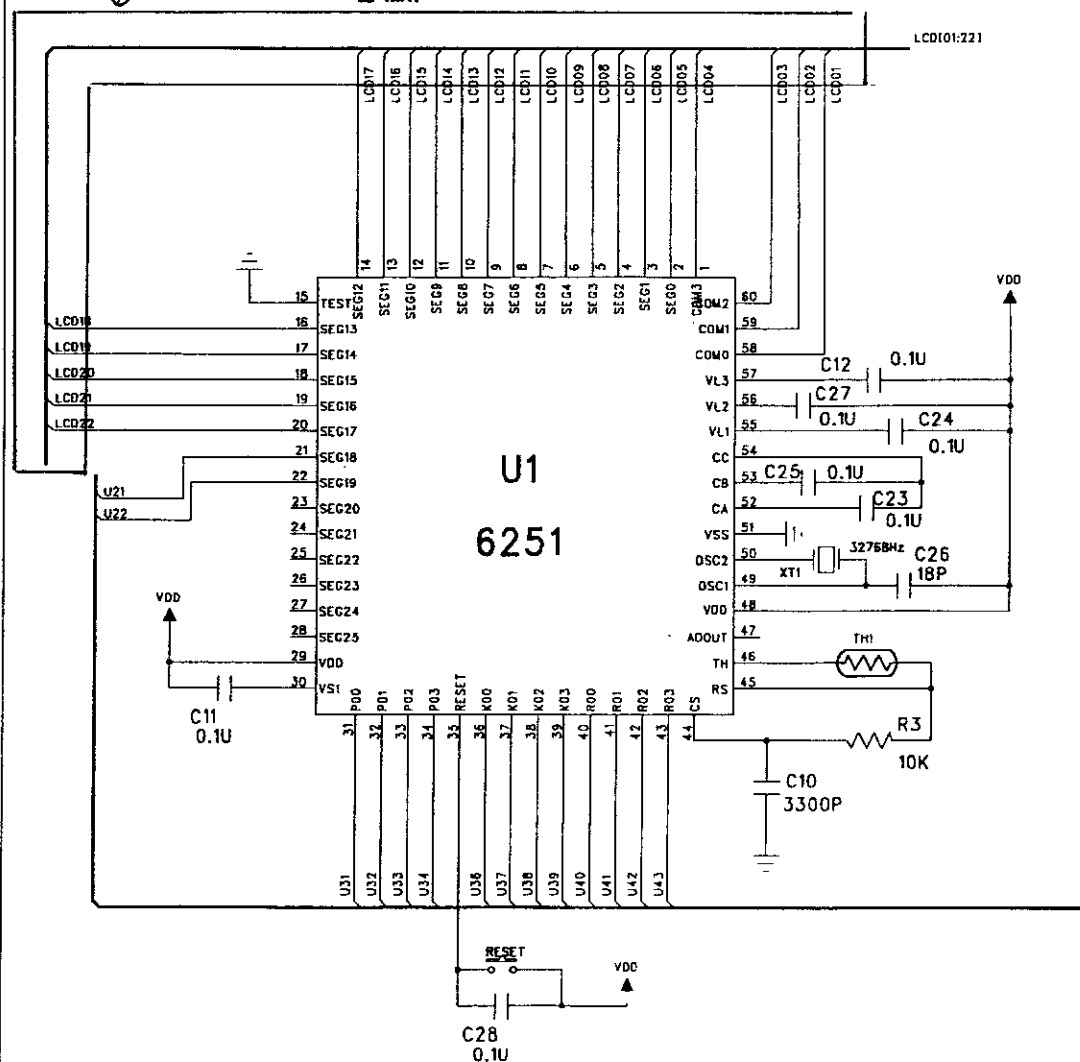
for model = 6720

☒ Engg. ☐ Project ☒ PMC ☐ Prod
☒ CD ☐ System ☐ Admin. ☐ EDP
☐ MKT ☐

受控复制文件
Controlled copy

CDC 02 08753
日期: 31 JUL 2002

NO.	REVISIONS	DATE:	CHK:	APP:



COMPANY: Electronics Tomorrow Ltd.

MODEL: 6720/

PCB VER: V2.0	TITLE: SCH	DOC VER: 00
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DRN./DATE: LAQ 7/02 DWG NUM: 6720E20131

CHK./DATE: 164/7/72 IC: 6251D07E

APP./DATE: LCD: 15031815

☒ TO FILE ☐ REL ☐ SHEET OF:

EE008

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