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FCC ID PDVRFLAN



RF-LAN Module

Description of Operation v1.00

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Operational Overview

The purpose of the RF-LAN Module is to provide communication over the air to another similar unit or an RF-LAN Hub. The RF-LAN Module will be installed into different types of host electronic measurement, control, and data acquisition equipment, such as electric meters. After installation at the factory into a host device the Module is fully enclosed and not accessible externally.

The module has to be installed by a trained professional only and only at the factory.

The following describes the general operation of the RF-LAN Module:

- The Module operates in ISM band (902 – 928 MHz) using the Frequency Hopping method.
- The Module can be installed in devices that provide a 5VDC power supply.
- Communication with the host device is accomplished through a 9600 baud serial interface.
- The Module adjusts output RF power according to reception of an acknowledgment signal.
- The Module can communicate with a similar Module located in other devices, such as a Gateway, and with low power RF transceivers located nearby inside devices like Water or Gas Meters.

Hardware Description

The main building blocks of the RF-LAN Module are the microcontroller, the RF transceiver, and the 1-Watt RF power amplifier, as shown in the following Figure 1: Block Diagram of RF-LAN Module.

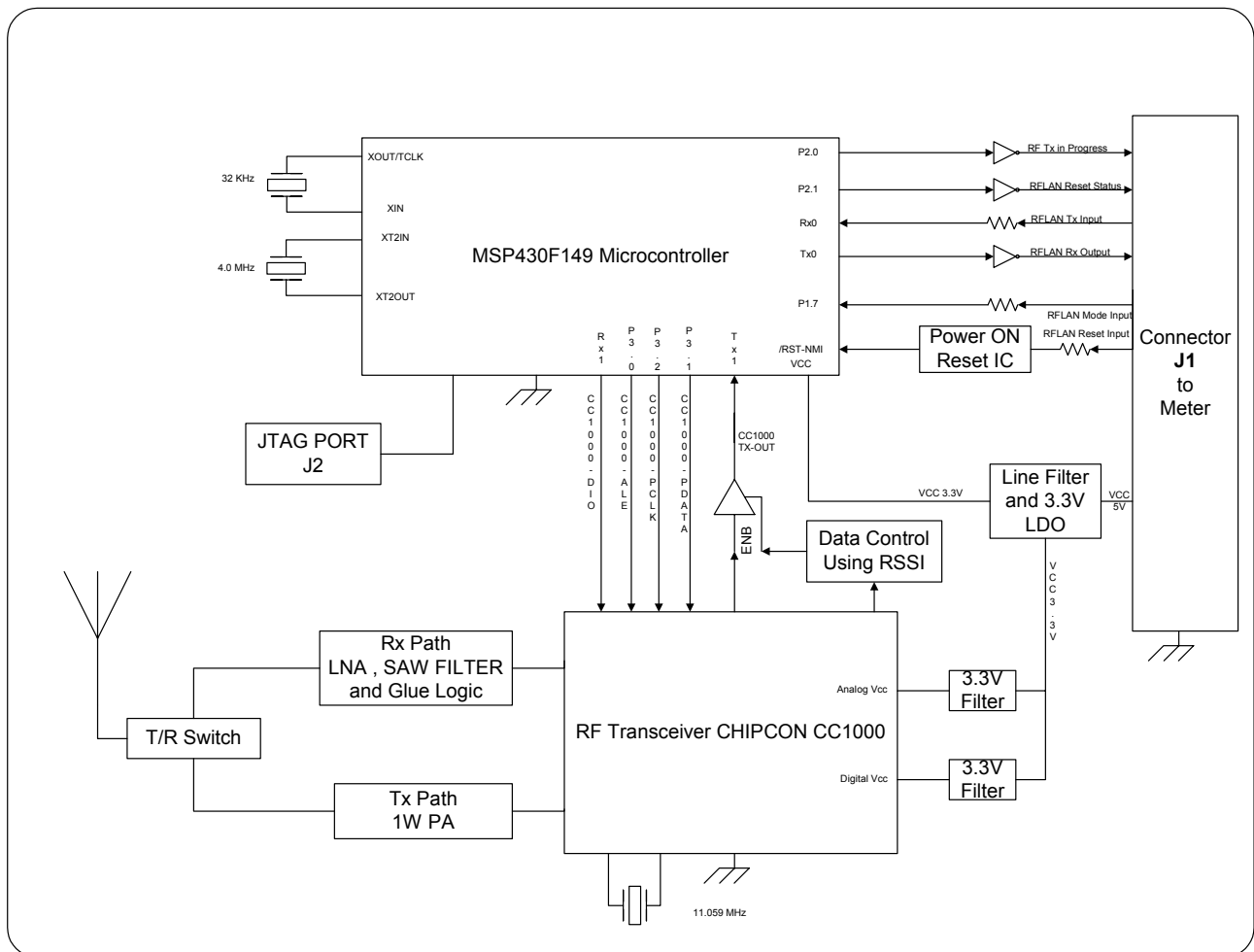


Figure 1: Block Diagram of RF-LAN Module

The MSP430 controller is the heart of the circuit, which interfaces with:

- The host device via the J1 connector.
- The CC1000 chip to send and receive data over the air.

The firmware program resides in the internal flash of MSP430. MSP430 has two crystals, Y1 is 32.768 KHz and Y2 is 4.0MHz. Y1 is basically used to work with a JTAG emulator for debug purposes, Y2 is used for normal operation. When the RF LAN Module is connected to a host device, the host device controls the way the RF LAN Module will work through the serial port, J1.5 (Reset), and J1.6 (Command/Data).

MSP430 controls all of the functions on the RF-LAN Module, it talks to the host device via Serial port 0. The mode of RF-LAN is set by the signal level at J1.6. When the signal level is high it is in command mode, when the signal level is low it is in data mode. MSP430 appropriately handles the data by polling the J1.6.

MSP430 talks to the CC1000 via port bits and sends/receives RF data using serial port 1. MSP430 initializes CC1000 by toggling port bits (simulated synchronous serial bus), P3.2 (PCLK), P3.1 (PDATA IO), and P3.0 (PALE), as provided by the CC1000 data sheets.

The CC1000 is a single chip RF transceiver, used for wireless application in the ISM band 300-1000MHz frequency ranges. The analog and digital supply to CC1000 is well filtered by using a PI filter to suppress noise. An 11.059 MHz crystal is connected to CC1000 as a reference for the data rate and for other internal functions.

On the receive path, the RF input signal from the antenna goes through a saw filter, an LNA, and a 50-ohm impedance matching network, before feeding the RF input of CC1000. Great care is taken to design the traces on these paths to match for 50-ohm traces in order to reduce loss.

On the transmit path, the RF2131 IC is connected to the RF output of CC1000 to provide a 1-Watt RF power output. The MSP430 controller does the on-off control of the PA.

The RF transmit and receive paths are connected to the antenna via a T/R switch. MSP430 controls which signal to pass by controlling the T/R switch, using port bits.

When CC1000 is programmed in receive mode, the data to the MSP430 is allowed only when RSSI (Receiver Signal Strength Indicate) is better than -105 DBm, using RSSI signal and glue logic.

MAX825SEUK (U5) provides the power on reset to the RF-LAN Module.

Software Description

At power-on, MSP430 initializes all port pins and initializes the CC1000. MSP430 sets the mode of operations by looking at J1 connector mode input (J1.6). The host device initializes all the RF-LAN registers at power-on in command mode.

When J1.6 is in data mode, MSP430 initializes the CC1000 in Rx mode. When a valid data packet is received, MSP430 checks for the valid CRC. If valid, it checks to see if it is addressed to this RF-LAN Module. If addressed correctly, the data packet is sent to the host device.

In data mode, data received on the serial port is buffered and packetized with the header, initialized with the CC1000 in the transmit mode, setting the T/R switch to the transmit path, and sends the data to the CC1000 to transmit the data over the air. Once the data is sent, the CC1000 is switched to the receive mode, waiting for the acknowledge packet for the data just sent. The frequency-hopping algorithm is implemented to send/receive data on various frequency channels. The frequency-hopping tables are residing in the flash of MSP430.

Antenna Radiation Pattern, FCC ID PDVRFLAN



SIGNAL ANTENNA SYSTEMS INC



For use with C01 turn table and Panther LI controller

TEST PARAMETERS

TX POLARIZATION

Vertical

ROTATION

Elevation 2

ANTENNA DESCRIPTION

MTR 2

DATA: GAINABSOLUTE

Gain

AVG

FREQUENCY BAND

M T C

FILE NAME

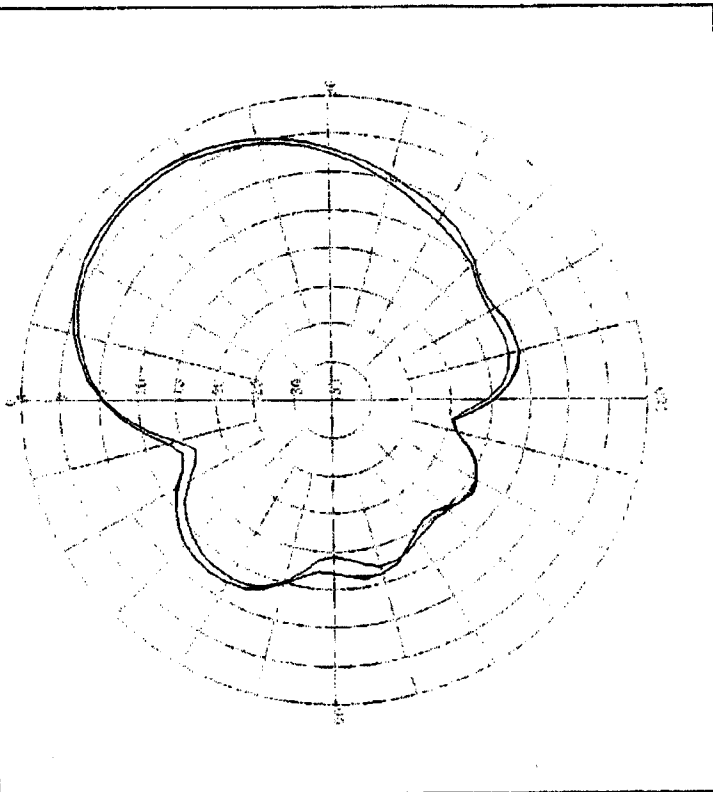
C:\MTC EL2 Mtr 21

Test Parameter Setup

Calibrate Dipole

Recall Data

START TEST



Chamber

SAS1

DATE

5/20/2002

TIME

3:25 PM

BEAM PEAK

Freq(MHz)	Degree	dB(Ave)	dB(Gain)
902.00	45.000	-28.50	2.4730
915.00	50.000	-28.51	2.4730
928.00	50.000	-29.59	1.8021

Degrees per Data Point

5

CARTESIAN ☒ POLAR

Pattern Presentation

Scale Minimum

-35

Scale Maximum

5

Rescale

Over 2

Ant
LMS
for h-w

Handwritten signature and notes.

Antenna Radiation Pattern, FCC ID PDVRFLAN

SIGNAL ANTENNA SYSTEMS INC

For use with COL terra table and Panther LI controller

TEST PARAMETERS

POLARIZATION

Horizontal

STATION

Elevation 1

ANTENNA DESCRIPTION

DATA: GAIN/ABSOLUTE

Gain

Avg

FREQUENCY BAND

MTC

LE NAME

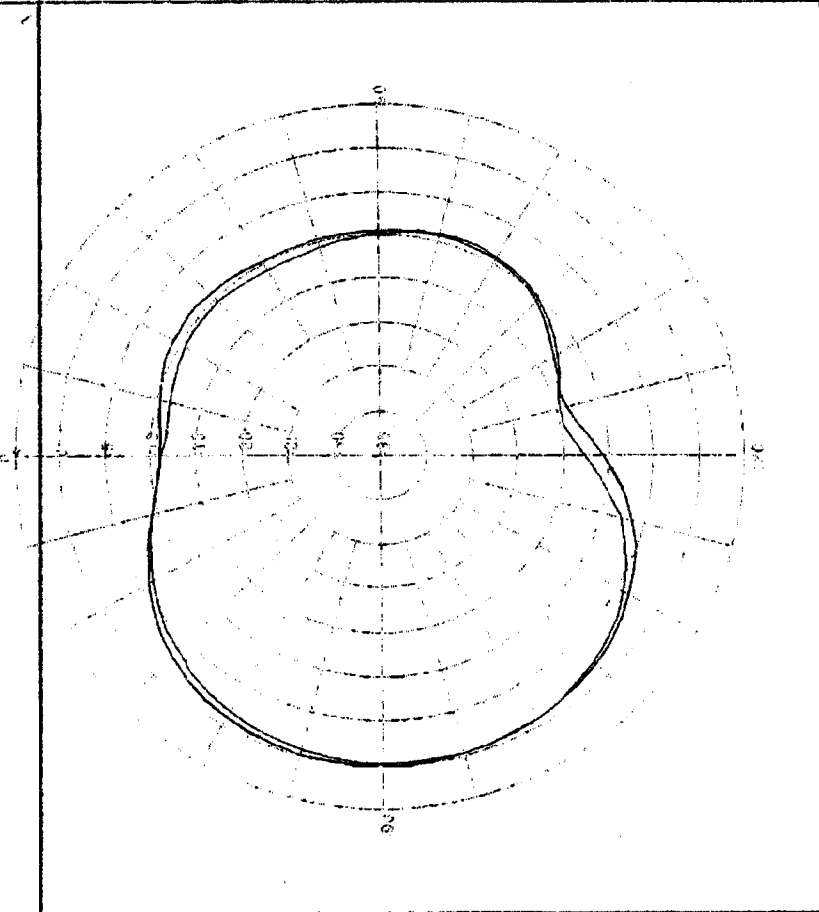
WTC EL1 Mtr 21

Test Parameter Setup

Calibrate Dipole

Recall Date

START TEST



Degrees per Data Point

CARTESIAN ☒ POLAR ☐

Scale Minimum dB

Scale Maximum dB

Pattern Presentation

Rescale

BEAM PEAK

Freq(MHz)	Degree	dB(Abs)	dB(Gain)
902.00	270.00	-30.85	0.1185
916.00	270.00	-30.85	0.1273
928.00	265.00	-31.55	-0.164

Chamber

SAS1

DATE

5/20/2002

TIME

4:02 PM

Quail 2

Ant

EL1

Pattern