

Hitag Reader Chip

HTRC110

4 BLOCK DIAGRAM

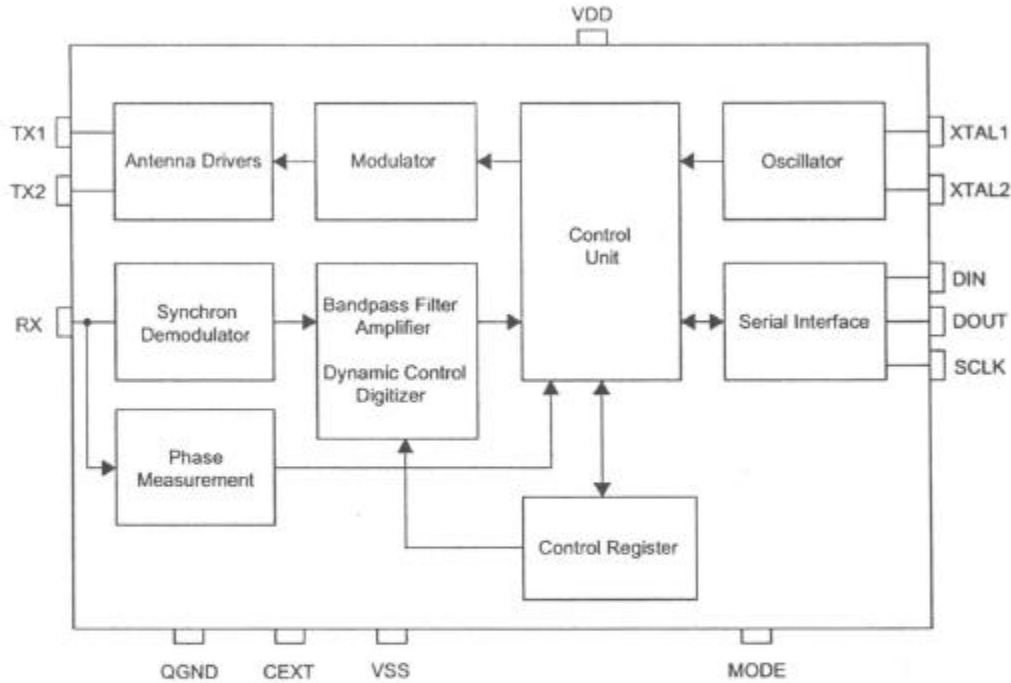


Fig.1 Block diagram Hitag Reader Chip HTRC110

5 KEY DATA

Supply VDD	5 V $\pm 10\%$
Clock/Osc. frequency	4,8,12,16 MHz programmable (antenna carrier frequency 125 kHz)
Antenna driver current	200 mA _p continuous
Serial interface	CMOS compatible
Package	SO14
Operation temperature range	-40°C to +85°C