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PROPRIETARY NOTICE

NOTICE

REVISIONS

ZONE REV DESCRIPTION DATE APPROVED

A

INITIAL RELEASE INCLUDING REV XA THRU XE.

02MAR00

R.BACULA

B

TABLES: TABLE I: ADD COND NO. 610 THRU 613. SH IDX REV. PL AFFECTED.

25SEP00

L.KEEPER
M.BROWN
J.OLSON

C

NOTES: ADD NOTE 13. SH IDX REV. PL AFFECTED.

25SEP00

M.SMITH
M.BROWN
R.RICHARDS

D

NOTES: NOTE 13 WAS ITEM 116 MAY BE SUBSTITUTED --UNAVAILABLE.

25SEP00

M.SMITH
M.BROWN
R.RICHARDS

E

SH IDX REV. PL AFFECTED.

25SEP00

M.SMITH
M.BROWN
R.RICHARDS

F

NOTES: NOTE 4 WAS MAXIMUM---SIDE A: .060. ADD NOTE 16. SH IDX REV. PL NOT AFFECTED.

13NOV00

L.KEEPER
R.RICHARDS
J.OLSON

G

SH IDX REV. PL AFFECTED.

25SEP00

M.SMITH
M.BROWN
R.RICHARDS

TABLE I

BUSWIRE DATA

CONDUCTOR		PWB SIDE	FROM CIRCUIT POINT	TO CIRCUIT POINT
COND. NO.	ITEM NO.			
601	703	A	C450 TOP	C450 TOP PROBE PAD
602	703	A	C450 BOTTOM	C450 BOTTOM PROBE PAD
603	703	A	C451 TOP	U451 TOP PROBE PAD
604	703	A	C451 BOTTOM	U451 BOTTOM BOTTOM PROBE PAD
605	703	A	C452 TOP	C452 TOP PROBE PAD
606	703	A	C452 BOTTOM	C452 BOTTOM PROBE PAD
607	703	B	U43 PIN 10	U43 PIN 4
608	703	B	U22 PIN 28	J3 PIN 44
609	703	A	U20 PIN 128 VIA	J2 PIN 75
610	703	B	U2 PIN 6	U36 PIN 10
611	703	B	U2 PIN 14	U36 PIN 9
612	703	B	U43 PIN 2	U36 PIN 26
613	703	B	U58 PIN 17	U36 PIN 11
614	703	A	R1001 PIN 1	C405 PIN 1

NOTES:

1. ASSEMBLE PER ITEM 701.

CAUTION

ELECTROSTATIC SENSITIVE EQUIPMENT
SPECIAL HANDLING REQUIRED

THIS ASSEMBLY INCLUDES COMPONENTS WHICH ARE SUBJECT TO DAMAGE BY ELECTROSTATIC CHARGES; THEREFORE, ALL COMPONENTS SHALL BE HANDLED IN ACCORDANCE WITH GUIDELINES FOR ELECTROSTATIC DISCHARGE CONTROL.

3. COMPONENT LEAD PROTRUSION SHALL BE .060 MAXIMUM, BARE COPPER PERMISSIBLE ON COMPONENT LEADS AT TRIMMED SURFACES.

4. MAXIMUM COMPONENT HEIGHT:
SIDE B: .330, EXCLUDING ITEM 2 AND P1.
AREA A .210 CONDUCTIVE CASE AND .260 NONCONDUCTIVE CASE.
SIDE A: .080

5. SQUARE PADS ON PRINTED WIRING BOARD INDICATE PIN 1.

6. CONFORMAL COAT CCA PER ITEM 702, EXCEPT J1, J2 AND J3 SIDE B, ITEMS 5 AND 6 BOTH SIDES AND AREAS NOTED. LEADS AND PADS OF P1 SHALL BE COATED ON SIDE A.

7. SOME COMPONENTS SHOWN OVERSIZE TO PROVIDE SPACE FOR DESIGNATORS.

8. SHADED COMPONENTS ARE NOT INSTALLED.

9. RADIAL CRACKS PERMISSIBLE ON ITEM 5, IF FREE OF POTENTIALLY LOOSE MATERIAL.

10. J1, J2, J3 AND P1 SHALL BE FLUSH TO .010 TO SIDE B OF ITEM 1.

11. INSTALL BUS WIRES PER TABLE I, LEFT OR RIGHT, TOP OR BOTTOM AS VIEWED FROM INDICATED SIDE.

12. INSTALL CAPS C450, C451 AND C452 APPROXIMATELY 1/8 IN TO THE LEFT OF PADS PROVIDED FOR THESE DEVICES USING ITEM 704.

13. ITEMS 116 OR 117 MAY BE SUBSTITUTED FOR ITEM 100, IF ITEM 100 IS UNAVAILABLE.

14. INSTALL COMPONENTS PER TABLE II, LEFT OR RIGHT, TOP OR BOTTOM, AS VIEWED FROM SIDE B.

15. INSTALL RESISTOR R1001, TO LOWER PAD FORMERLY USED FOR R585 PIN 1. ORIENT R1001 PIN 2 TO BE TO THE LEFT OF PIN 1 TOWARDS C405 USING ITEM 704.

16. REMOVE BREAKAWAY TAB.

TABLE II

COMPONENT INSTALLATION

COMPONENT		TO CIRCUIT POINT	PWB SIDE	SLVG ITEM NO.
REF DES	PIN			
R1001	1	NOT ATTACHED	A	
	2	R585 PIN 1 BOTTOM PAD		

CONTRACT

TCASII

DRAWN BY

V.SZWEZ

DATE

28FEB00

CHECKED BY

E.THIESSEN

DATE

28FEB00

APVD FOR MFG

J.DEBERNARDIS

DATE

28FEB00

APVD FOR GE

DATE

APVD FOR ENGRG

DATE

APVD FOR ENGRG

DATE

OPERATIONAL NOTE

SIMILAR ASSEMBLY

Honeywell

COMMERCIAL FLIGHT
SYSTEMS GROUP
PHOENIX, ARIZONA

TITLE

CIRCUIT CARD ASSEMBLY-
PROCESSOR I/O A2A1

SIZE

CAGE CODE

E 55939

DRAWING NO.

4066017-905

REV

G

SCALE

NONE

UNIT

WT

SHEET

1

OF

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FOR HONEYWELL REFERENCE

MFG REF NO.

DPA NO. A-19810

PRODUCT LINE NO. 3841

AW/PS ANALYSIS EB 4054939

REF DWG

REF SPEC

APPLICATION

INTERPRET DIMENSIONS AND TOLERANCES PER ANSI Y14.5M-1982

MATL-PROCESS-FINISH

MATL

PROCESS

FINISH

MILITARY DESIGNATION

PROCESS, FINISH, SEE 00.40.

ITEM CODE (PER EB699384)

THIRD ANGLE PROJECTION

DWG CLASS

FIRST USED ON

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES; TOLERANCES:

2 PL DEC ±

3 PL DEC ±

5231-040 (ANSI Y14.1-1980) HONEYWELL INC.
(REV 3-87)

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REV STATUS OF PARTS LIST SHALL BE SAME AS
BASIC DRAWING NUMBER
SEE SEPARATE PARTS LIST

REV

1

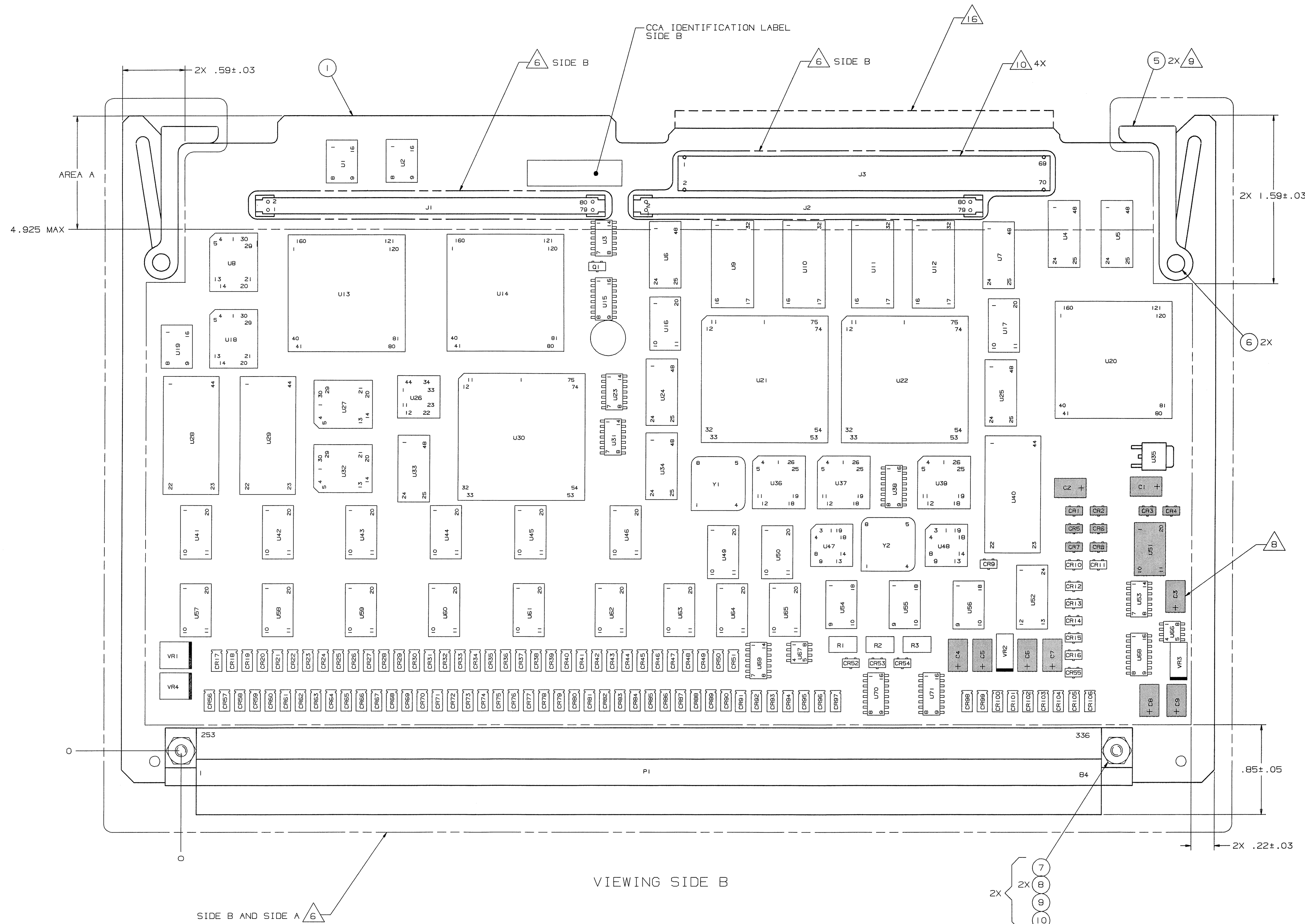
OF

28

COMPUTER GENERATED

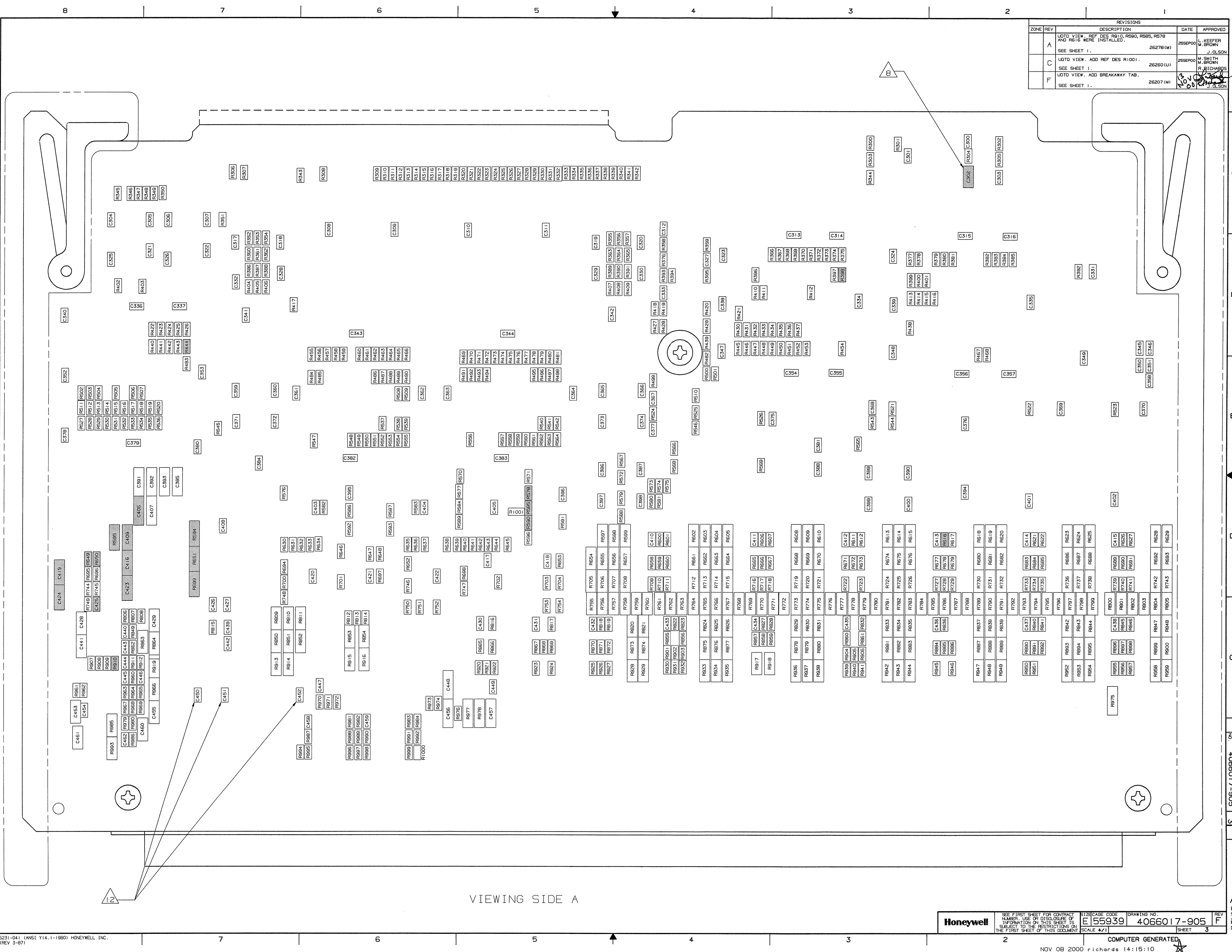
DEC 07 2000 richards 10:59:10

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	F	U2TD VIEW, ADD NOTE 16 CALLOUT, ADD BREAKAWAY TAB. 26207 (M)	13 NOV 00	<i>[Signature]</i> J. OLSON
		SEE SHEET 1.		

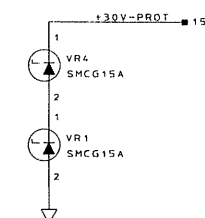
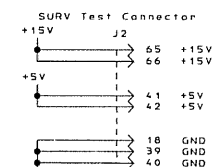
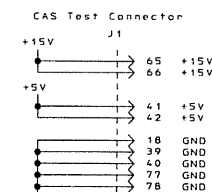
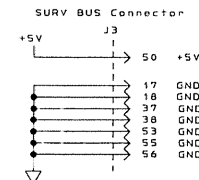
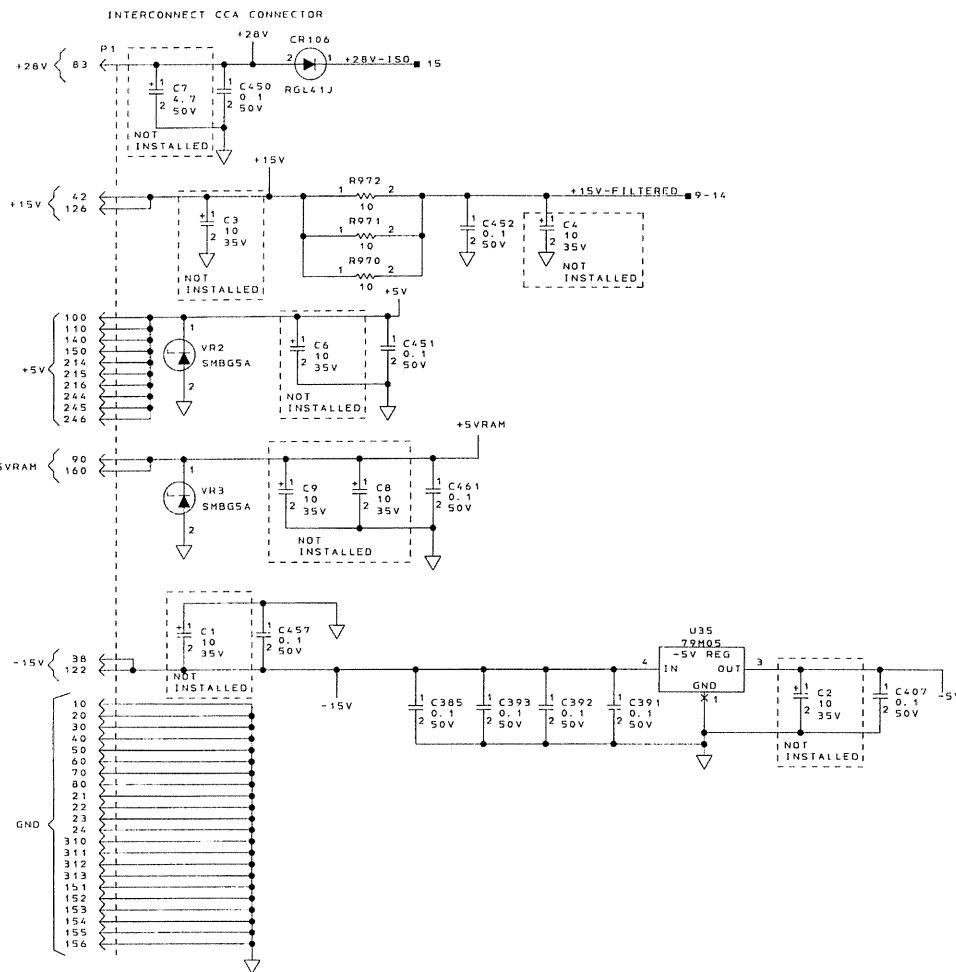


Honeywell		SEE FIRST SHEET FOR CONTRACT NUMBER, USE OR DISCLOSE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.		SIZE/CAGE CODE	E 55939	DRAWING NO.	4066017-905	REV	F
				SCALE	3/1	SHEET		2	

ZONE		REV		DESCRIPTION		DATE	APPROVED
A	1	A	1	UDTD VIEW. REF. DES R610, R590, R585, R578 AND R616 WERE INSTALLED.		26SEP00	L. KEEFER M. BROWN
				SEE SHEET 1.			J. OLSON
C	1	C	1	UDTD VIEW. ADD REF DES R1001.		26SEP00	M. SMITH M. BROWN
				SEE SHEET 1.			R. RICHARDS
F	1	F	1	UDTD VIEW. ADD BREAKAWAY TAB.		26SEP00	J. OLSON
				SEE SHEET 1.			



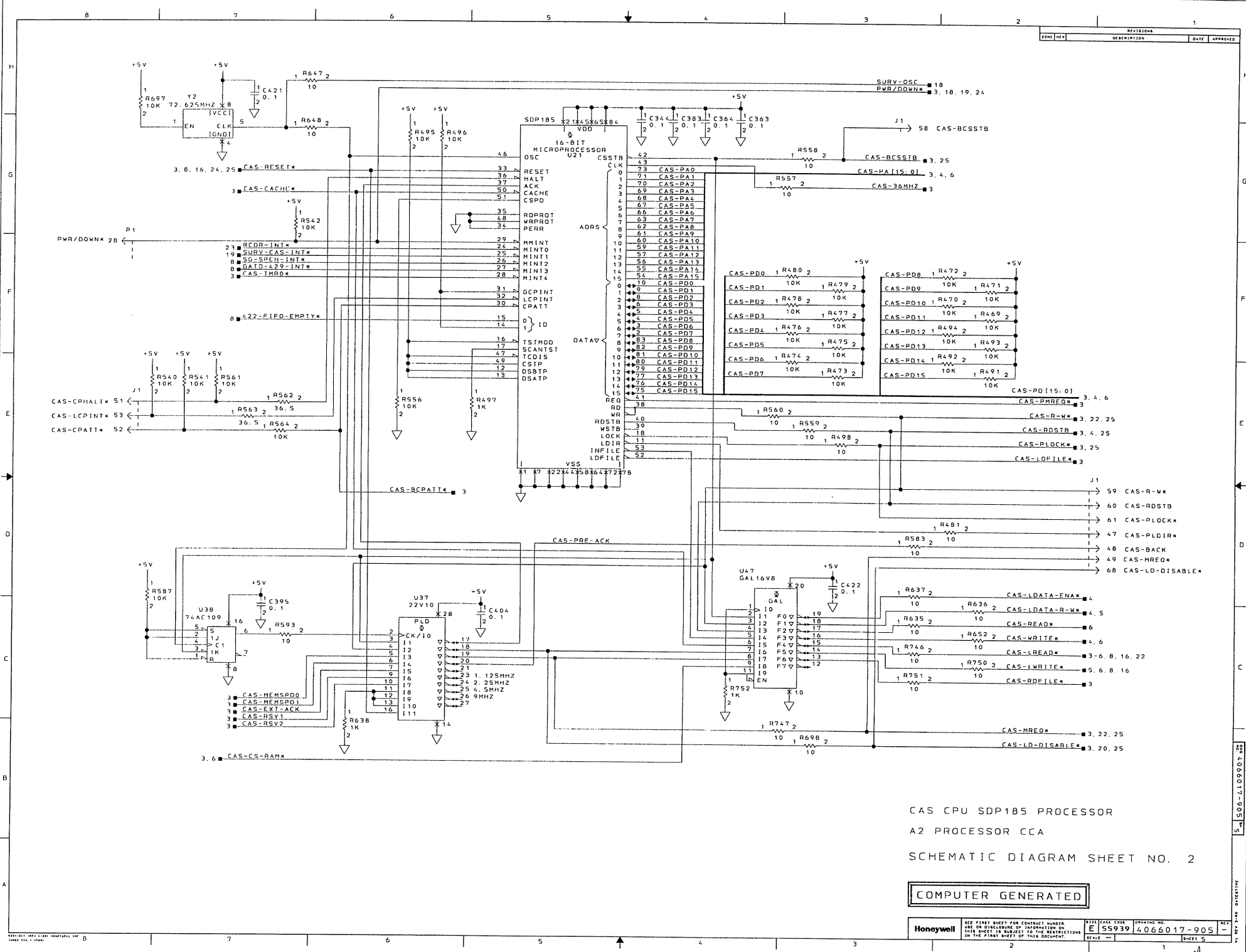
VIEWING SIDE A



- NOTES:**
1. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 2. ALL RESISTANCE VALUES ARE IN OHMS.
 3. ALL INDUCTANCE VALUES ARE IN MICROHENRIES.
 4. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN. FOR COMPLETE DESIGNATIONS PREFIX PART DESIGNATION WITH SUBASSEMBLY DESIGNATION.
 5. INTERPRET DRAWING PER ANSI Y32.2-1975 AND ANSI/IEEE STD 91-1986. LOGIC SYMBOLS CONFORM TO ANSI/IEEE STD 91-1984.

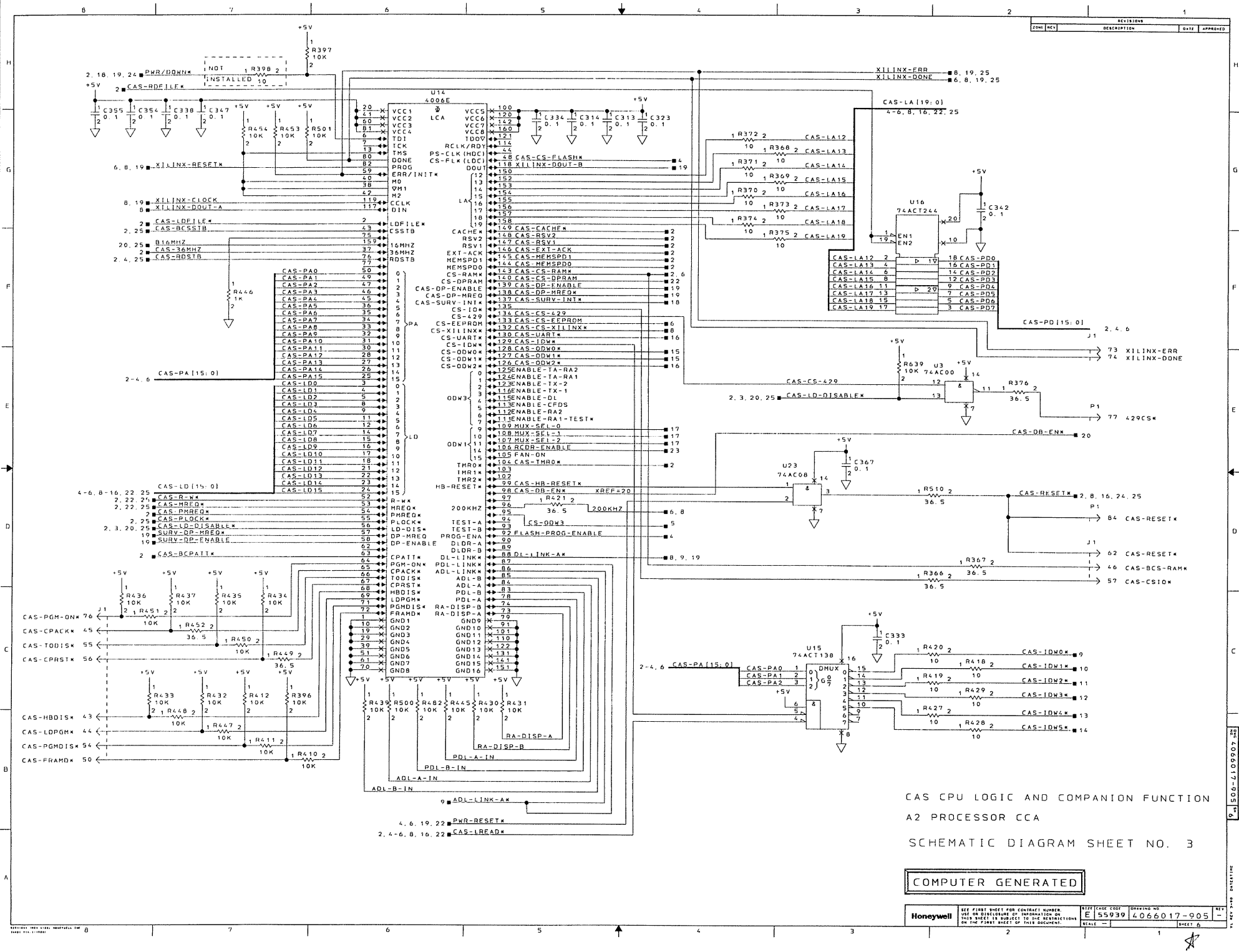
POWER SUPPLY INPUTS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO.1 OF 25

COMPUTER GENERATED



CAS CPU SDP185 PROCESSOR
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 2

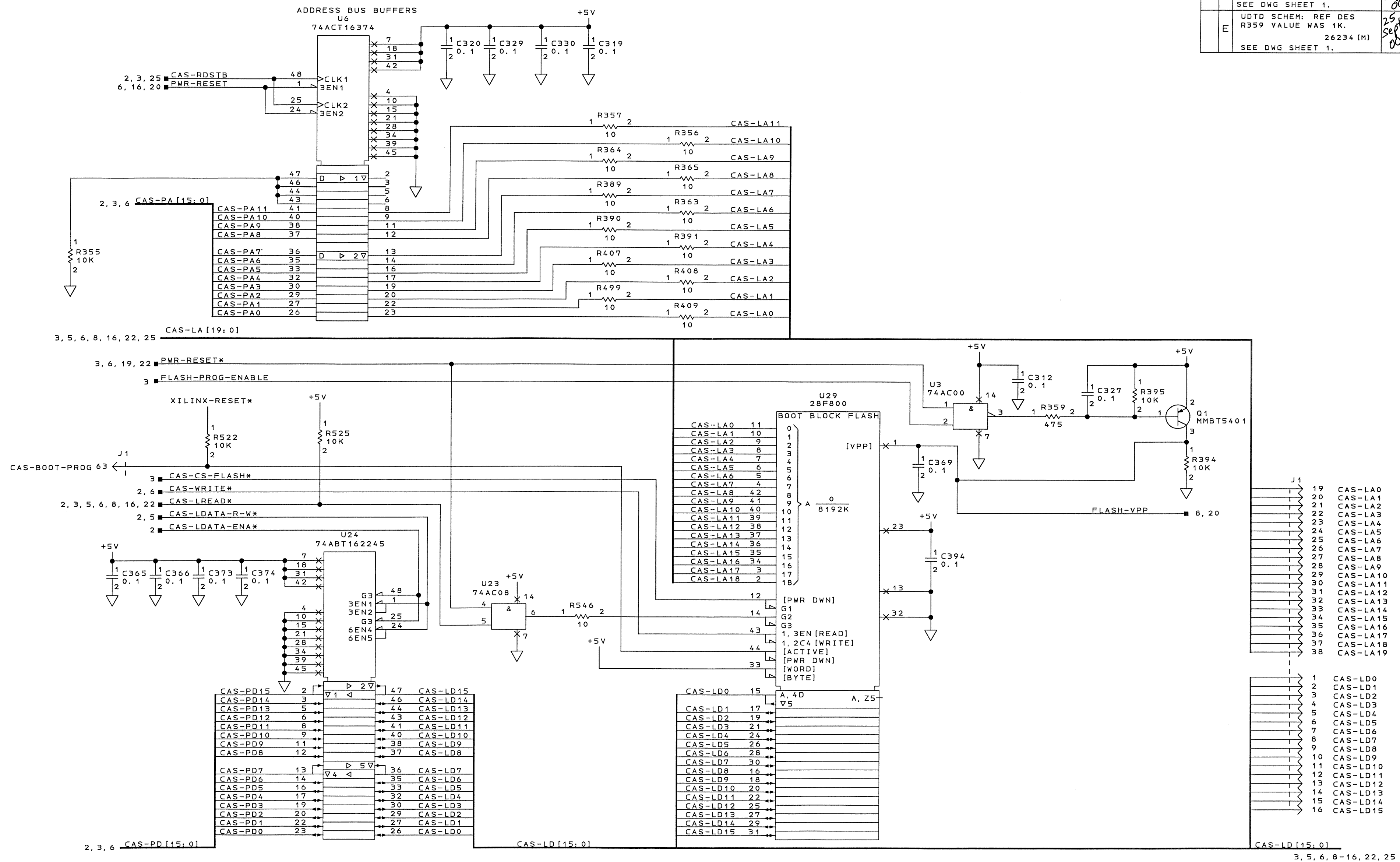
COMPUTER GENERATED



CAS CPU LOGIC AND COMPANION FUNCTION
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 3

COMPUTER GENERATED

REVISIONS			
ZONE	REV	DESCRIPTION	DATE APPROVED
	D	PARTS LIST DATA BASE CHANGE ONLY. 78319 (U) SEE DWG SHEET 1.	25 Sept 00 [Signature] R. RICHARDS
	E	UTD SCHEM: REF DES R359 VALUE WAS 1K. 26234 (M) SEE DWG SHEET 1.	25 Sept 00 [Signature] R. RICHARDS

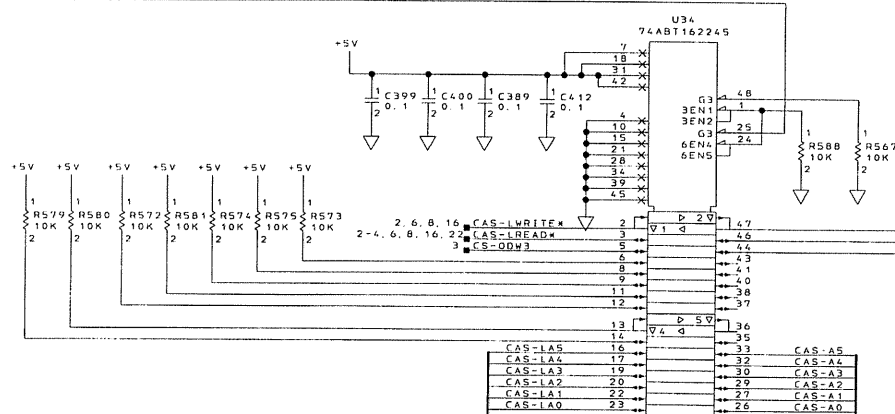


CAS CPU FLASH EPROM MEMORY/POWER RESET	05
A2 PROCESSOR CCA	7
SCHEMATIC DIAGRAM SHEET NO. 4	

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE	CAGE CODE	DRAWING NO.	REV
		E	55939	4066017-905	E
		SCALE —	SHEET 7		

20 ■ C-S-ADD-EN■



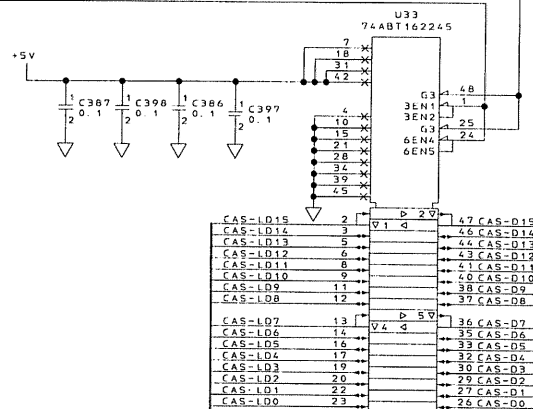
3, 4, 6, 8, 16, 22, 25 C-S-LA[12:0]

CAS-A[0:13]

P1
248 CAS/LWRITE
249 CAS/LREAD
78 ODW3■

20 ■ C-S-DATA-FN■

2, 4 ■ C-S-LDATA-R-W■



3, 4, 6, 8-16, 22, 25 CAS-LD15:01

CAS-D[0:15]

75 CAS-A0
74 CAS-A1
73 CAS-A2
72 CAS-A3
71 CAS-A4
69 CAS-A5
68 CAS-A6
67 CAS-A7
65 CAS-A8
64 CAS-A9
63 CAS-A10
62 CAS-A11
61 CAS-A12

59 CAS-D0
58 CAS-D1
57 CAS-D2
56 CAS-D3
55 CAS-D4
54 CAS-D5
53 CAS-D6
52 CAS-D7
51 CAS-D8
49 CAS-D9
48 CAS-D10
47 CAS-D11
46 CAS-D12
45 CAS-D13
44 CAS-D14
43 CAS-D15

CAS CPU SURV BUS INTERFACE

A2 PROCESSOR CCA

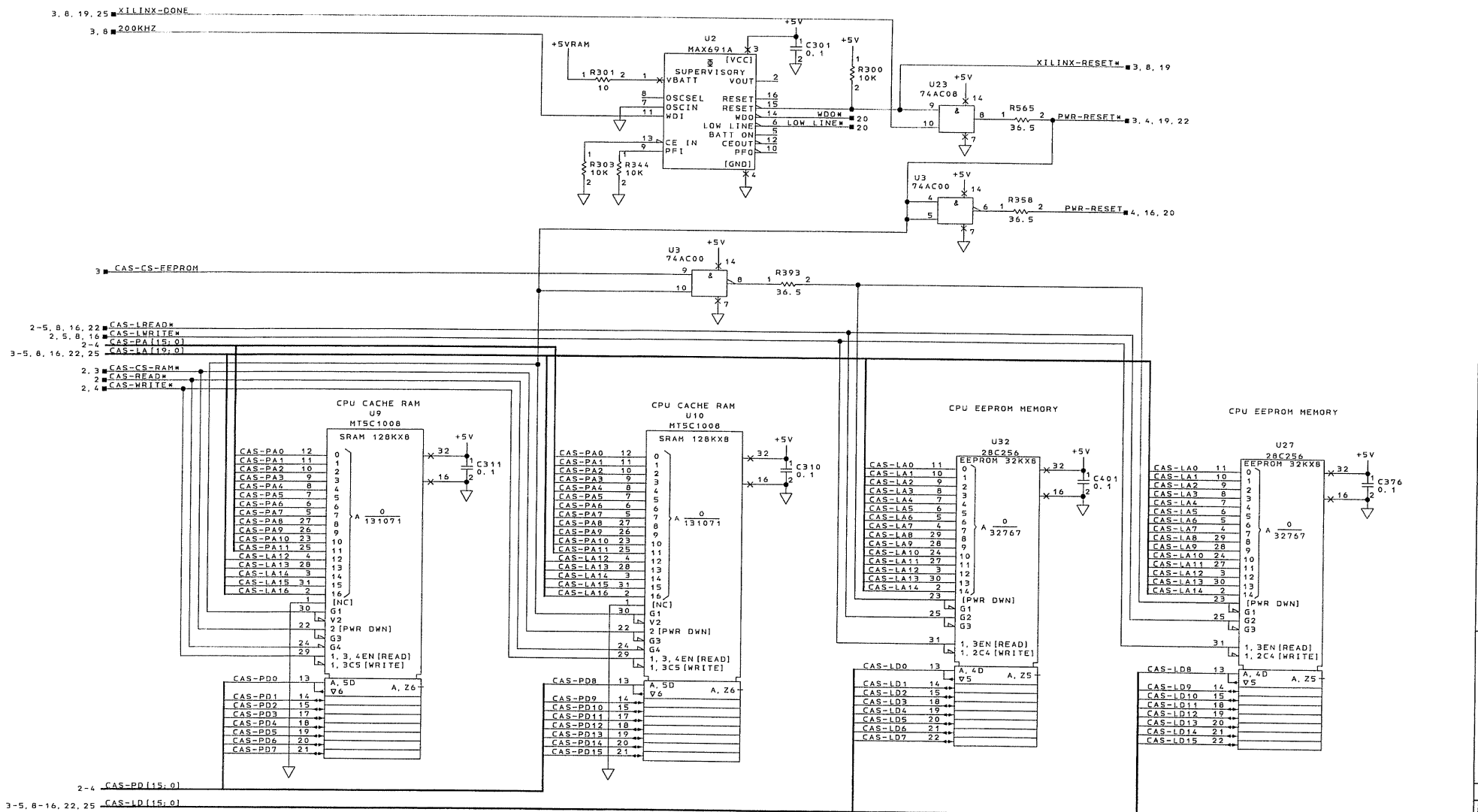
SCHEMATIC DIAGRAM SHEET NO. 5

COMPUTER GENERATED

Honeywell
REV E 55939
DRAWING NO. 4066017-905
SHEET 5 OF 5
DATE 11/1/90
BY J. B. BENTLEY

4066017-905

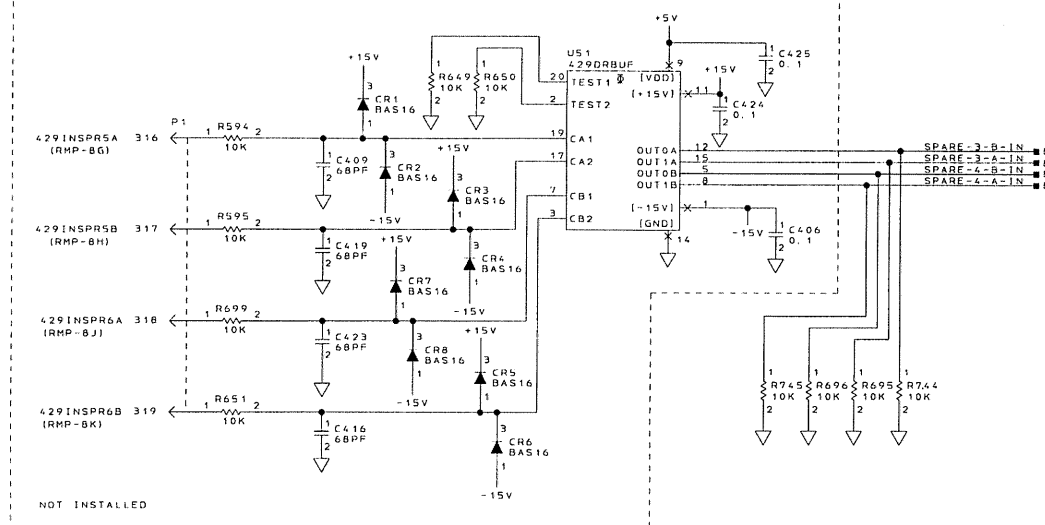
ZONE/REV		DESCRIPTION	DATE	APPROVED
A	1	UDTD SCHEM: ADD NETS AND LABELS TO U2 PINS 14 AND 6 RESP.	25 SEP 00	R. WICHARD
		SEE DWG SHEET 1.	26278 (M)	



CAS CPU RAM AND EEPROM MEMORY
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 6

COMPUTER GENERATED

ARINC 429 BUS RECEIVER BUFFER



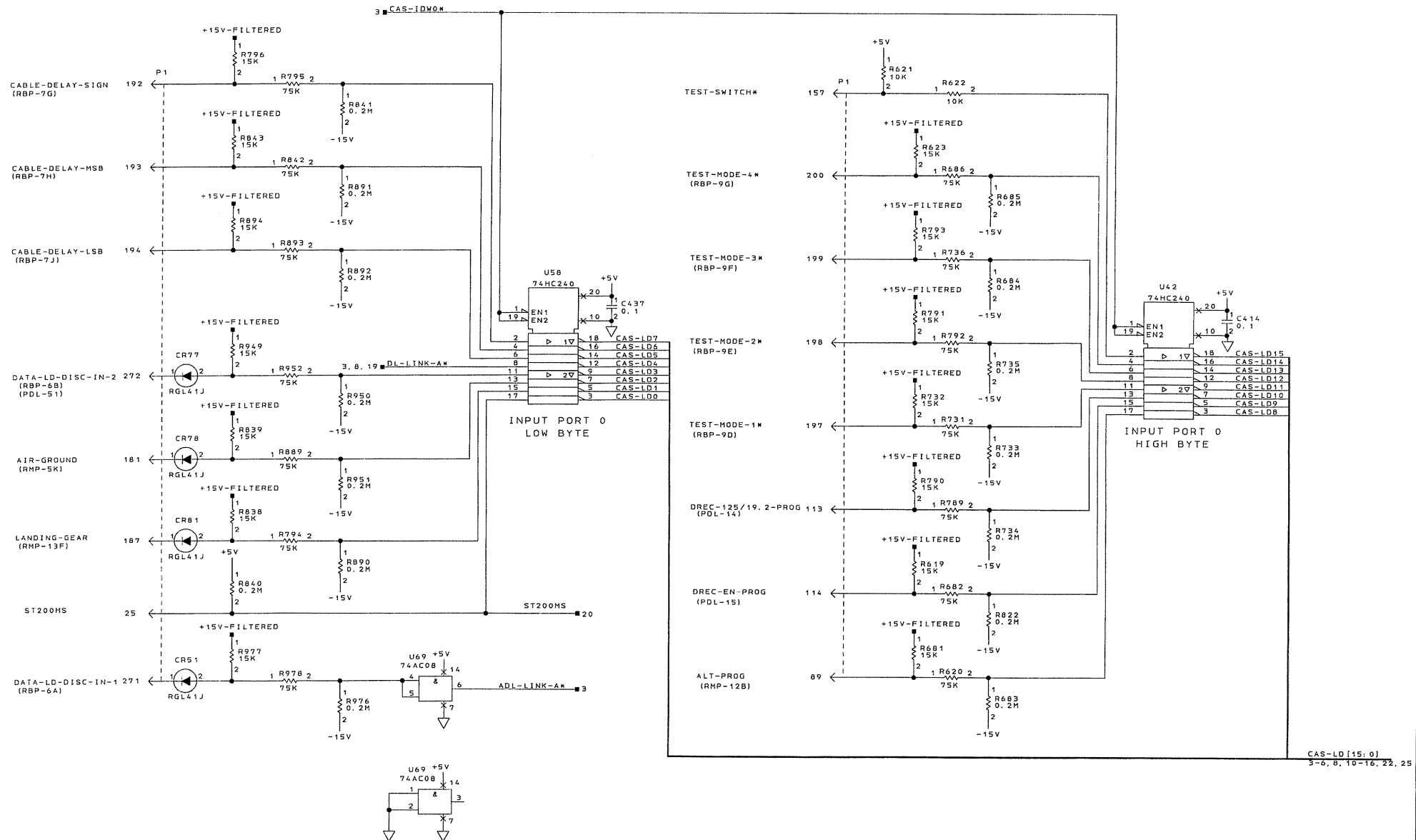
CAS CPU ARINC 429 RECEIVER BUFFERS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 7

COMPUTER GENERATED

SCHEMATIC DIAGRAM SHEET NO. 8

COMPUTER GENERATED

REVISIONS		DATE	APPROVED
C5	A	25 Sept 68	P. RICHARDS
DESCRIPTION		26278 (M)	
UDTD VIEW: ADD NET AND LABEL TO USB PIN 17		SEE DWG SHEET 1.	



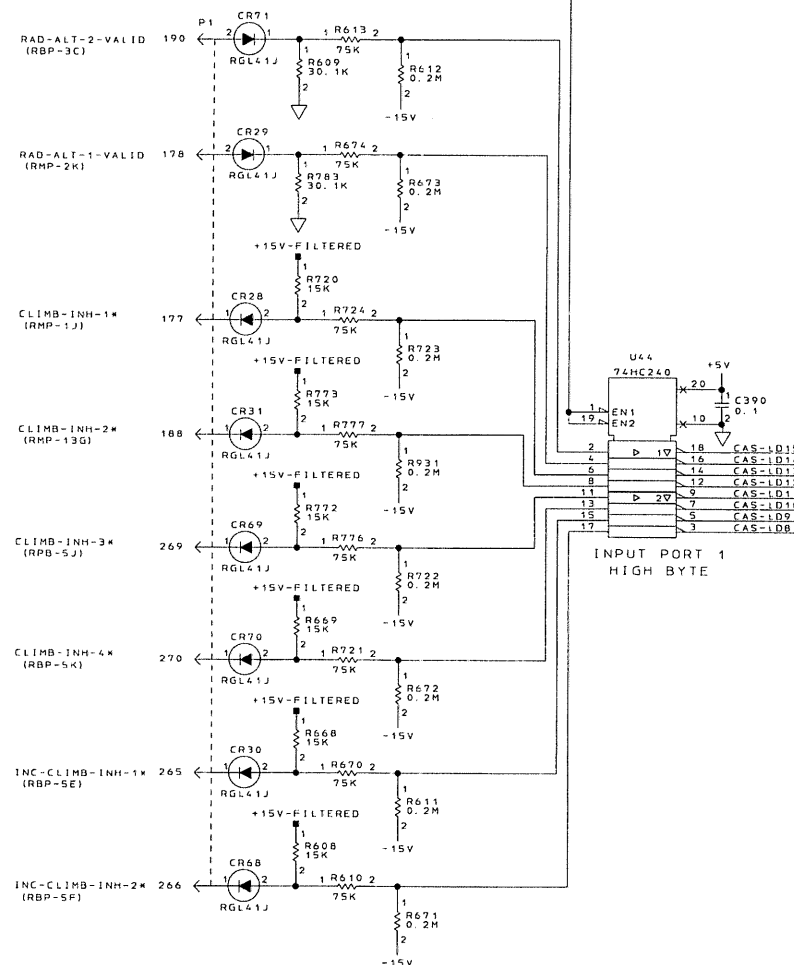
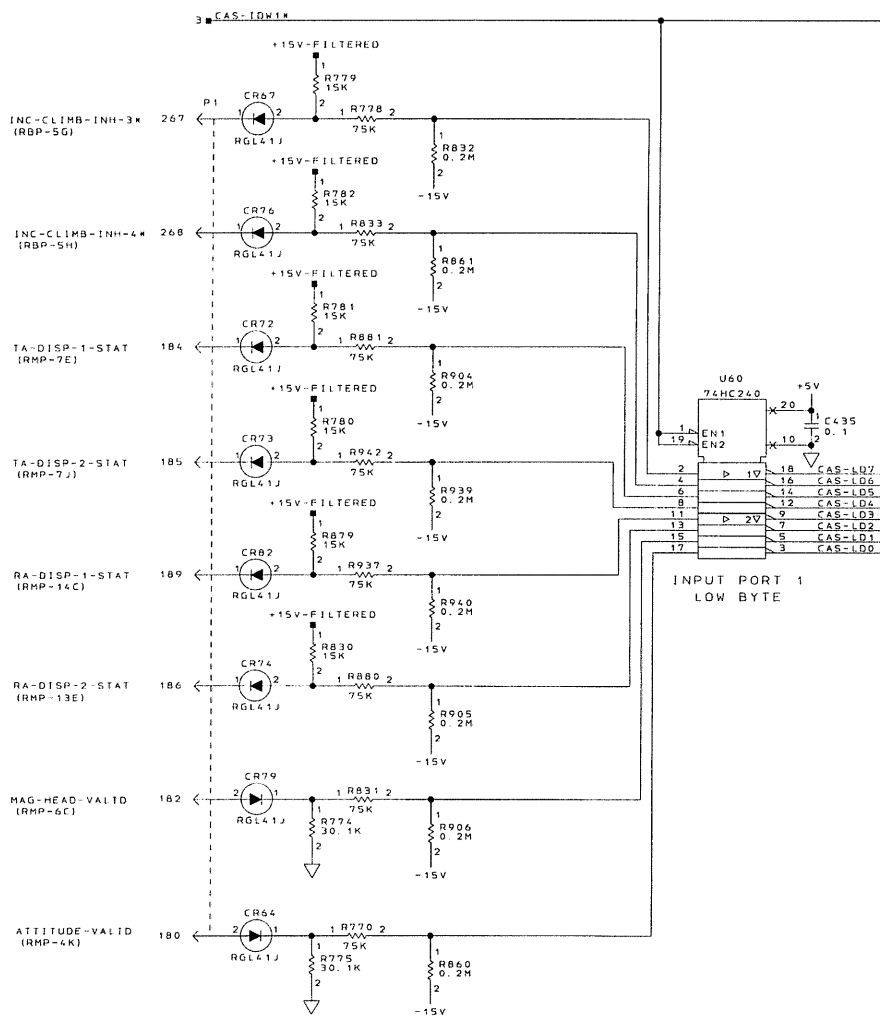
CAS-LD (15: 0)
3-6, 8, 10-16, 22, 25

CAS CPU DISCRETE INPUTS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 9

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE (4x6) CODE E 55939	DRAWING NO. 4066017-905	REV A
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4066017-905 12



CAS-ID (15:0)

3-6, 8, 9, 11-16, 22, 25

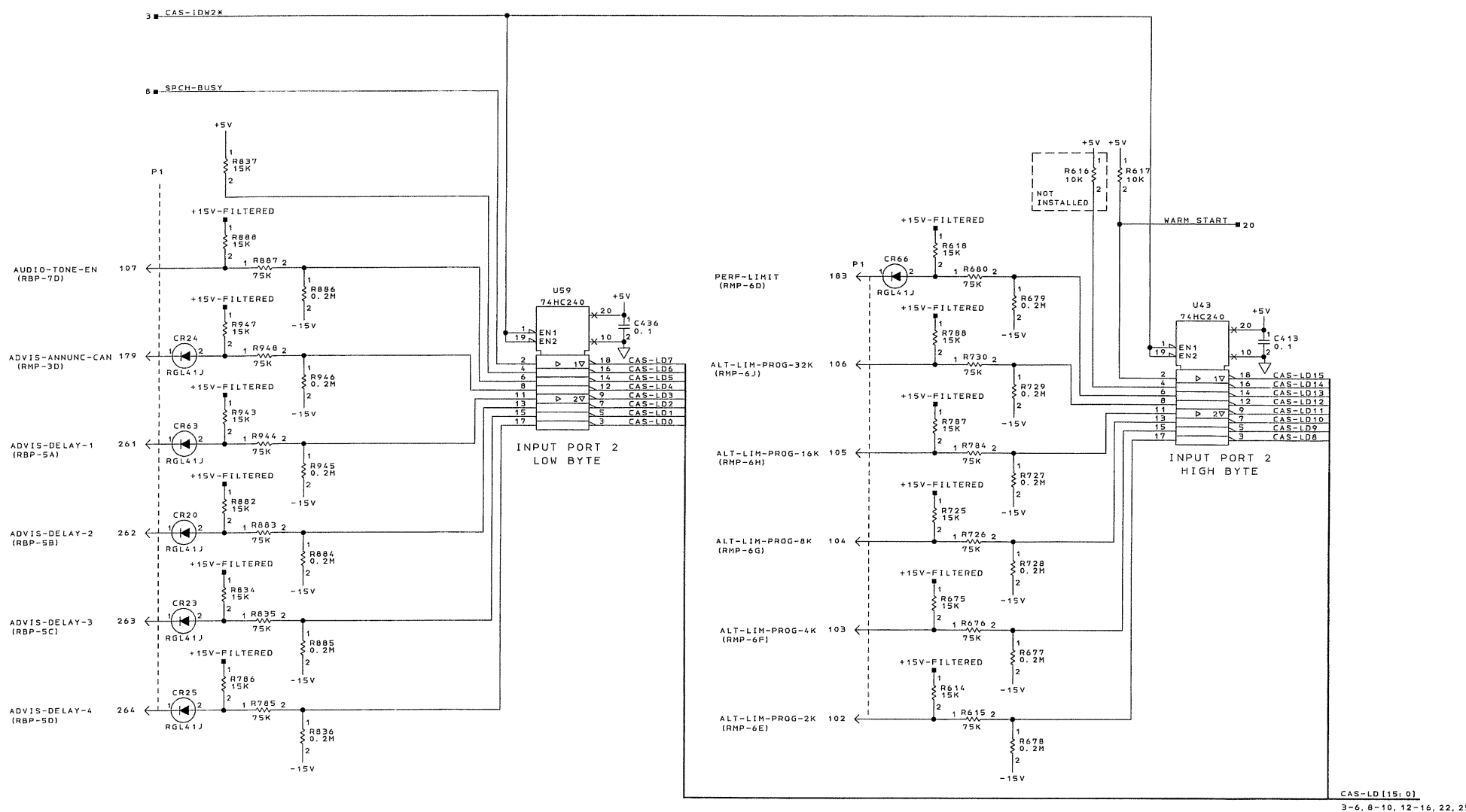
CAS CPU DISCRETE INPUTS

A2 PROCESSOR CCA

SCHEMATIC DIAGRAM SHEET NO. 10

COMPUTER GENERATED

REVISIONS		DATE	APPROVED
F2	A	25 SEP 78	R. RICHARDS
DESCRIPTION		26278 (H)	
UDTD VIEW: ADD NET AND LABEL TO R617 PIN 2.		SEE DWG SHEET 1.	



CAS CPU DISCRETE INPUTS
A2 PROCESSOR CCA
SCHEMATIC DIAGRAM SHEET NO. 11

COMPUTER GENERATED

Honeywell	SEE FIRST SHEET FOR CONTRACT NUMBER. USE OR DISCLOSURE OF INFORMATION ON THIS SHEET IS SUBJECT TO THE RESTRICTIONS ON THE FIRST SHEET OF THIS DOCUMENT.	SIZE (PAGE CODE) E 55939	DRAWING NO. 4066017-905	REV A
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3-6, 8-10, 12-16, 22, 25
11 506-1109907
REV 7-88 DATELINE
SHEET 16