



Circuit Description

REF.: 14 CHANNEL FRS

Q9 the 462.5625Mhz ~467.7125Mhz PLL oscillator drivers the base of Q8 buffer amplifier, and Q2, Q21 were the final driver for the Q1 Therefore power output has the matching network Consisting of C8,C7,L4,C6,L3,C5,D3,L1,C23,C2,C1 that limit the harmonic content and effect the Proper coupling of the antenna to the out stage.

U1 was the PLL Synthesize locked the Q9 frequency that control by MCU2 for the channel select, Q5 was the 1st. Amplifier for receive 462.5625Mhz~467.7125Mhz frequency to the base of Q6 The mixer, and Xf1 I.F. Filter through the I.F. Frequency to the base of Q7 I.F. Amplifier to the U2 (I.C.) I.F. Amplifier and detect output for the audio amplifier to the Speaker.

U5 was the (MIC) amplifier with the Limiter modulated Q9 Basic.

Q14, Q4 used control the MIC received functions, Q11,Q12 were the on/off switching for RX,TX, Control.

Disp17 was the functional indication,

Q13, Q16 clock alarm driver, Q3 was the back light driver, Q17 was the battery low driver.

Q19 was the temperate controller, Q15 control the Rx,Tx functioning MCU2.

Antenna , Ground, and Power Source

The antenna consists of a 7cm long telescoping rubber protection over the iron spring.

There is no external ground connection. The ground is only that of the printed circuit board.

Electric current is supplied by a 6V (um3 x4) storage cell.