

1. Block Diagram

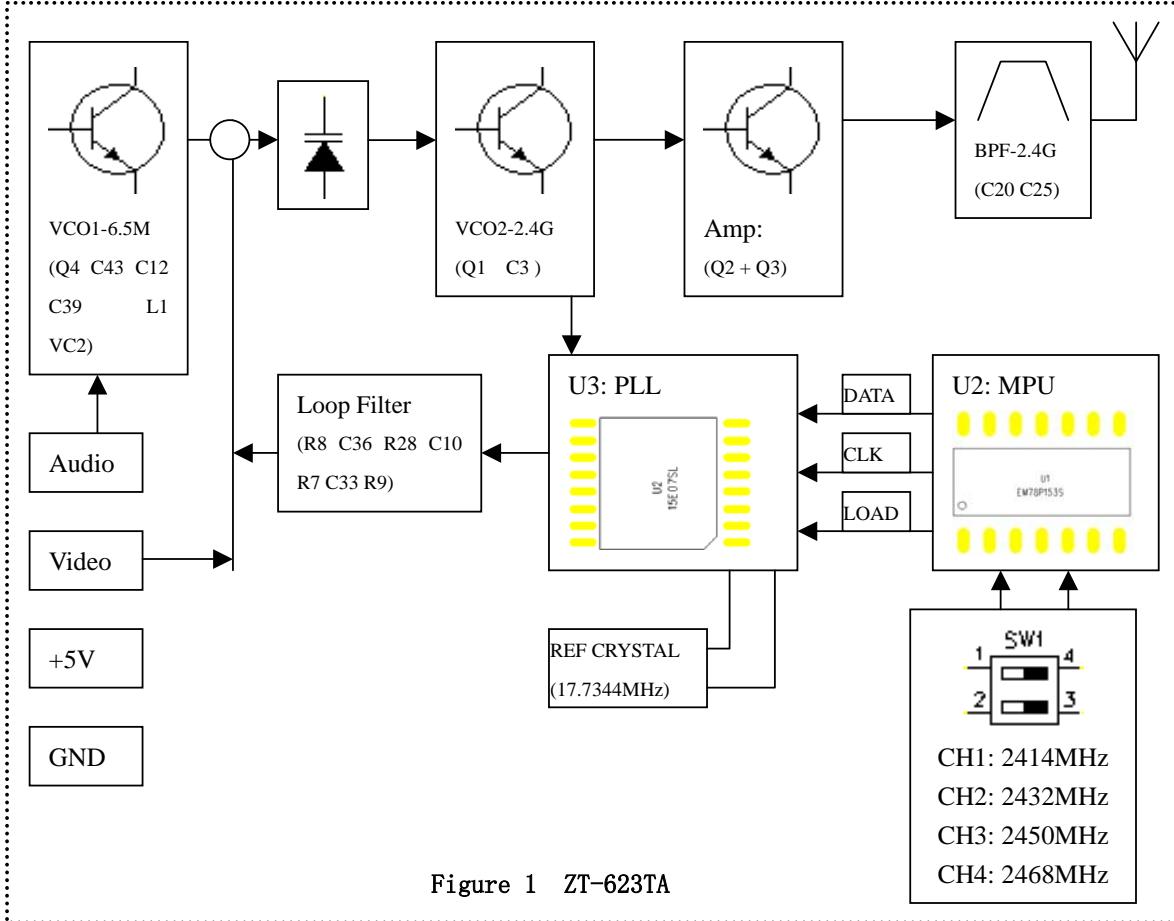


Figure 1 ZT-623TA

2. Circuit Description

The transmitter is made up of two parts: Audio & Video unit and RF unit. The audio signal from MIC1 is amplified, then modulates the audio carry frequency oscillator (6.5MHz), the modulated carry frequency is added in the VT of voltage controlled oscillator VCO2 (2.4GHz, D1, Q1). The video signal from CMOS camera passes through the sharp network(VR1, C5), also is added in the VT of VCO2, another signal is added in the VT of VCO2 is the error voltage output of loop filter (R8 C36 R28 C10 R7 C33 R9) of PLL(U3) which is programmed by CPU(U2), the channel data is decided by the encode switch(SW1) of CPU's peripheries. The feed back signal(R25,C47) from voltage controlled oscillator VCO2 is compared with reference crystal oscillator, the error frequency of oscillator VCO2 can be corrected. The carry frequency to be modulated by audio & video has been power amplified by Q2+Q3, filtered through the band pass filter(C20,C25, microstrip)which the higher harmonica of signal reduced to lower level. The purer signal is emitted by antenna(RFOUT).