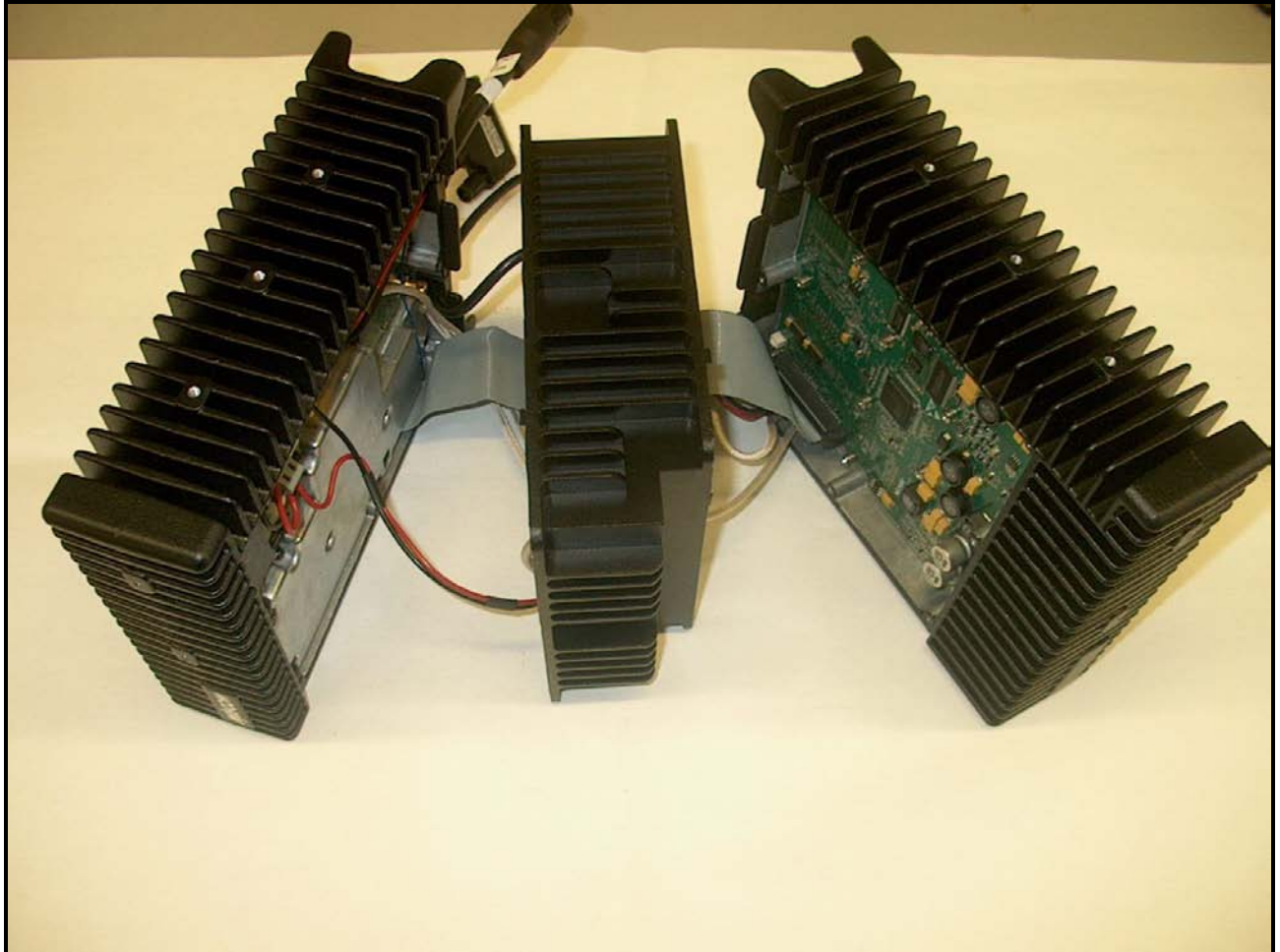
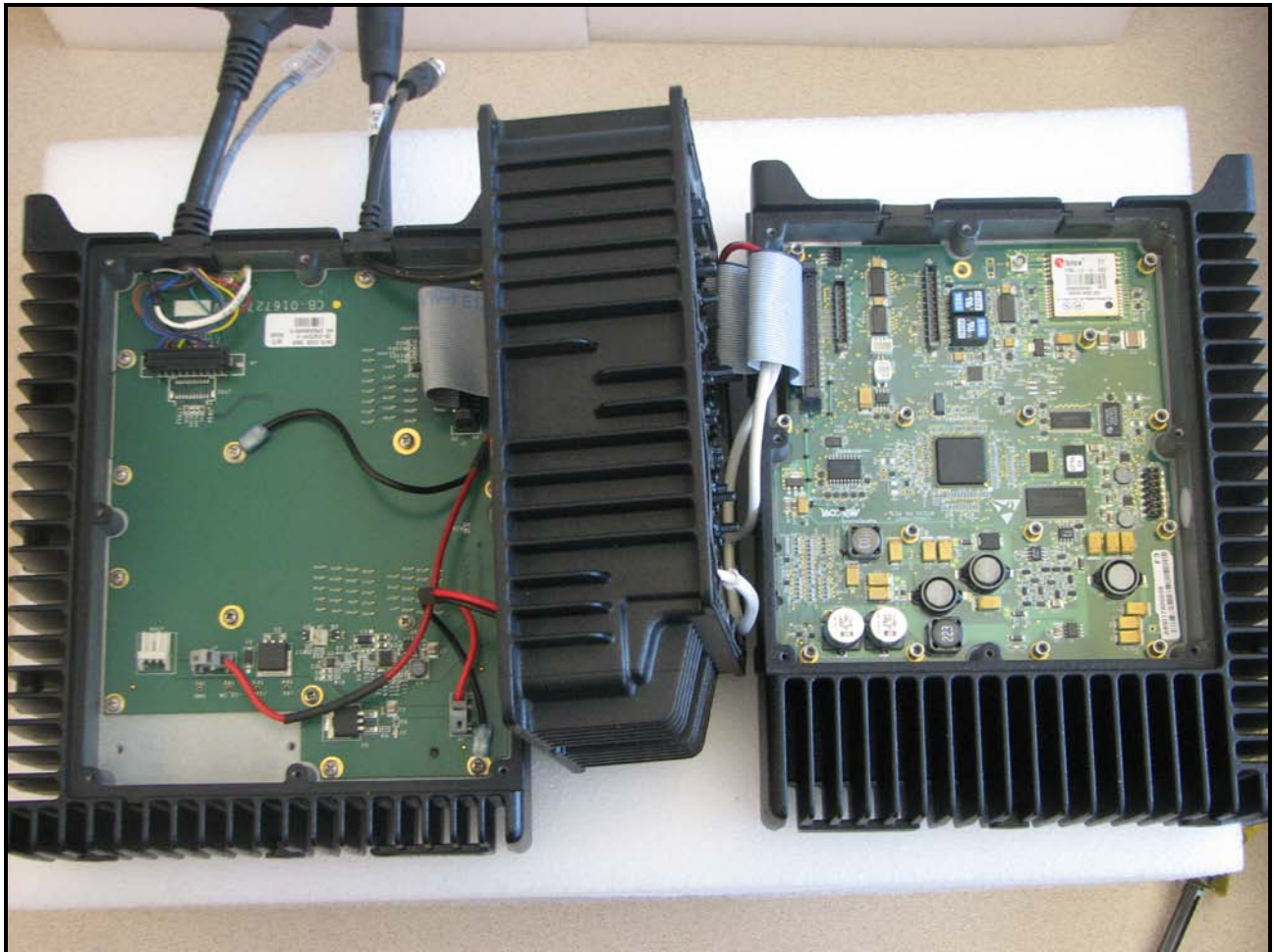


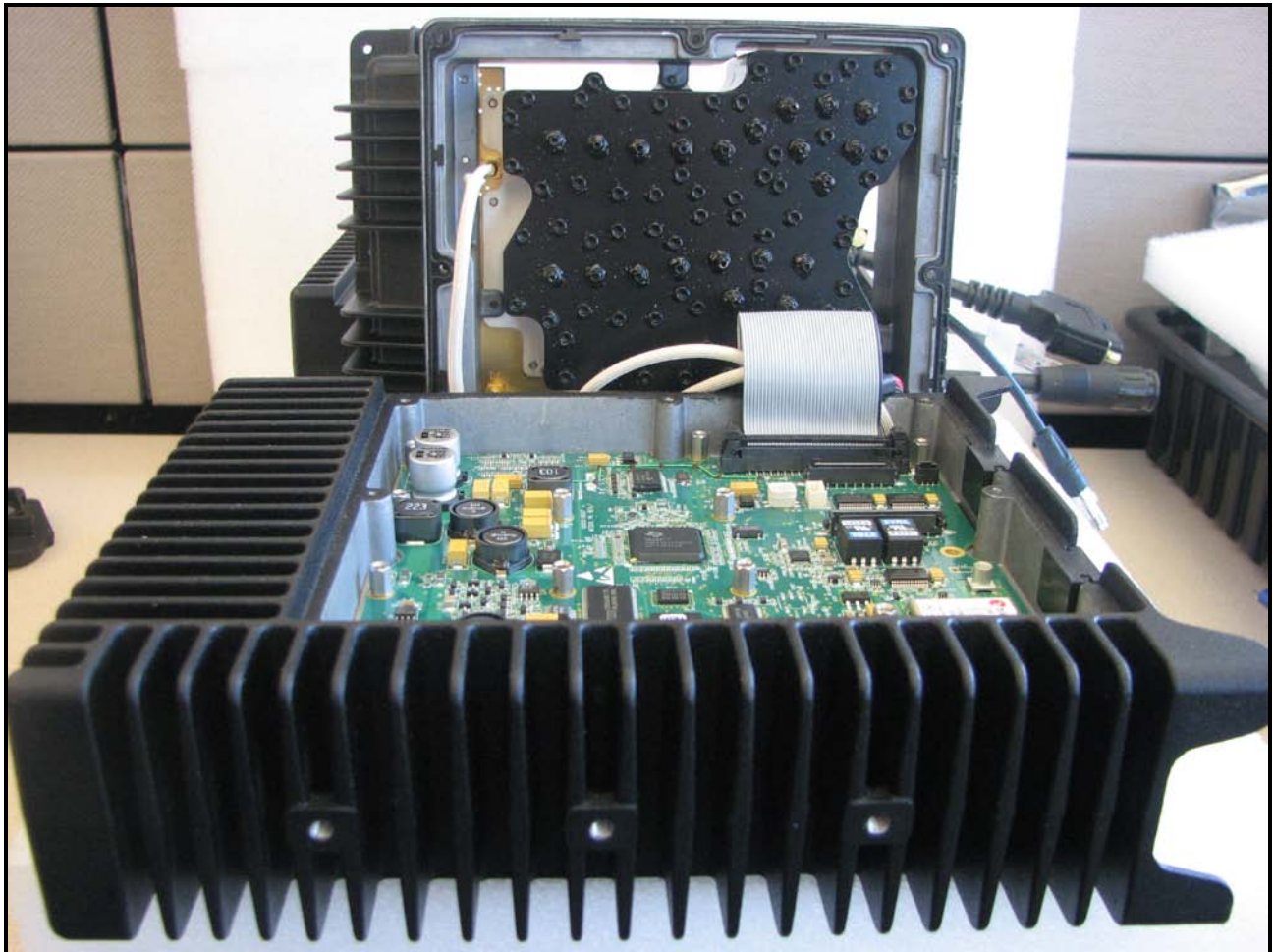
Appendix N: Internal Photographs



Photograph 10: EUT Disassembled 1



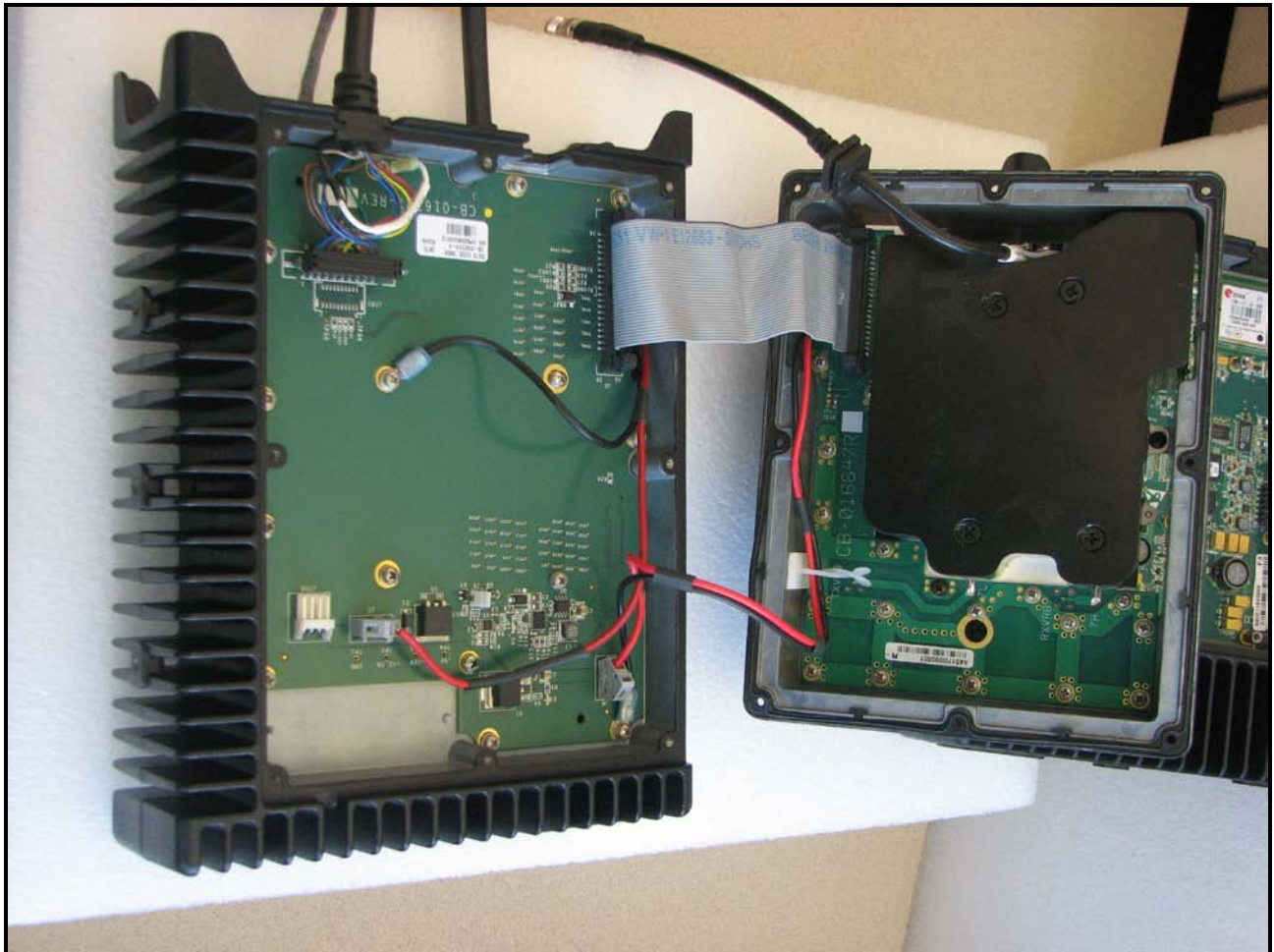
Photograph 11: **EUT Disassembled 2**



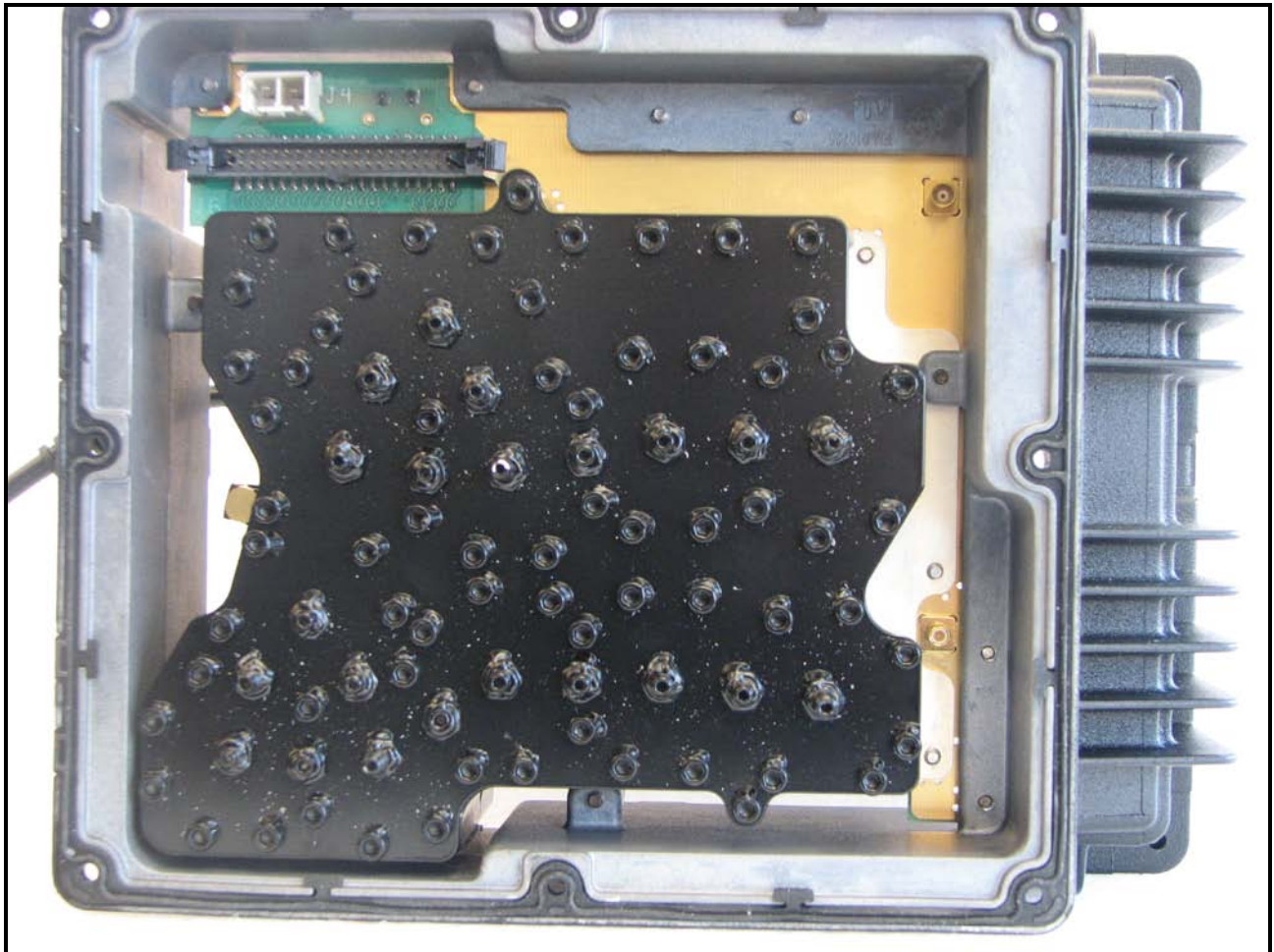
Photograph 12: EUT Disassembled 3



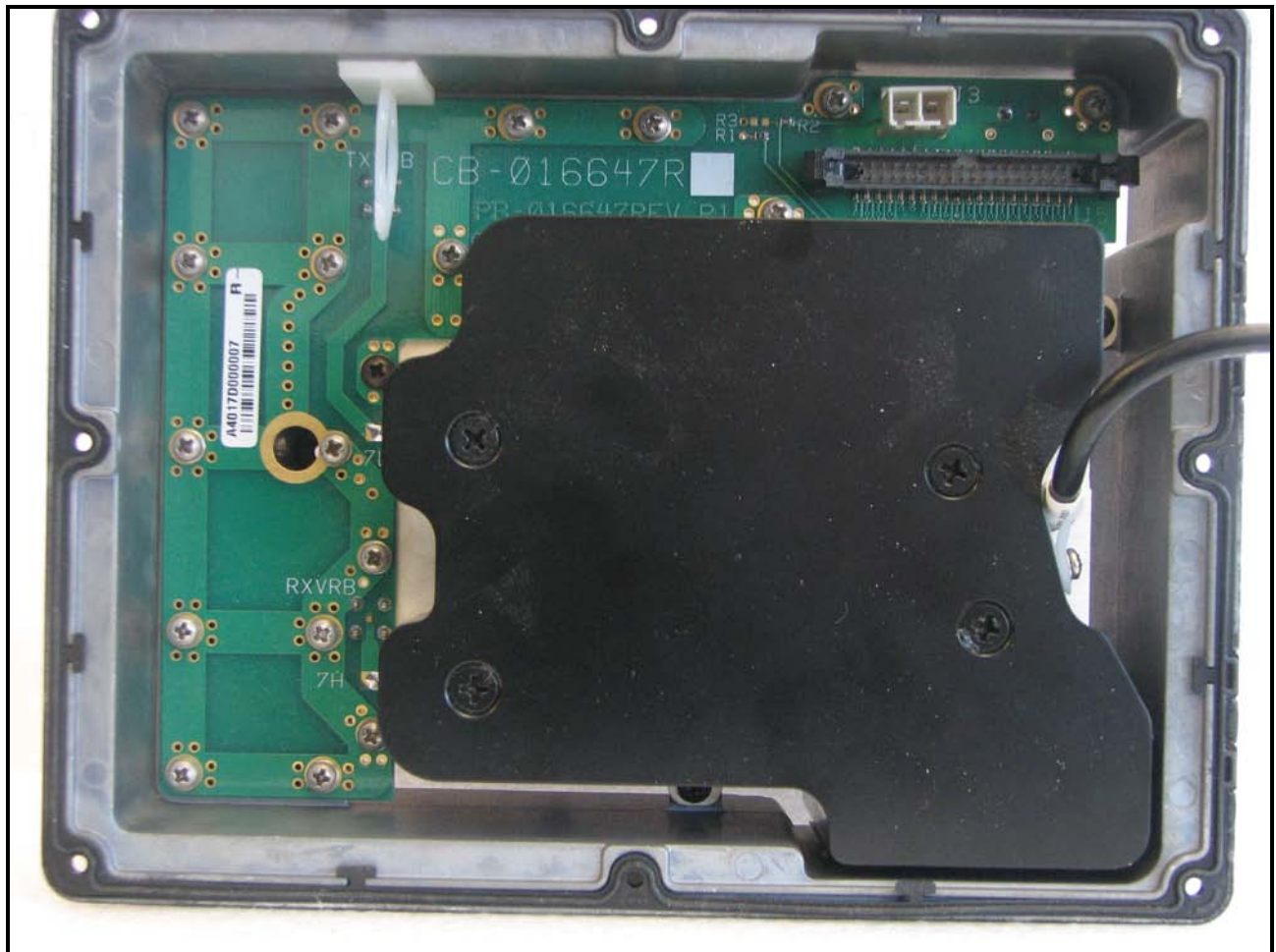
Photograph 13: EUT Disassembled 4



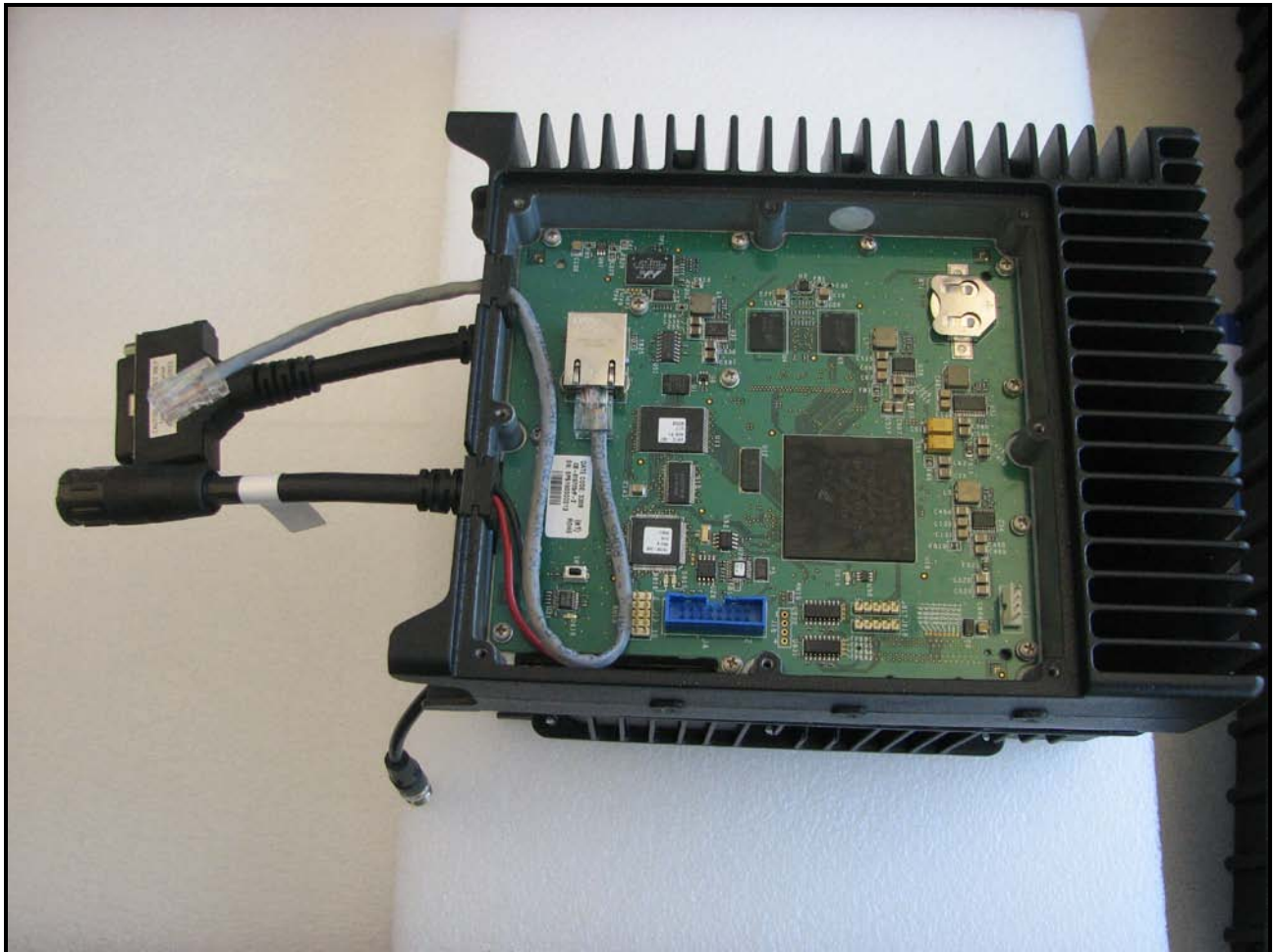
Photograph 14: EUT Disassembled 5



Photograph 15: Dup Filter TC Side



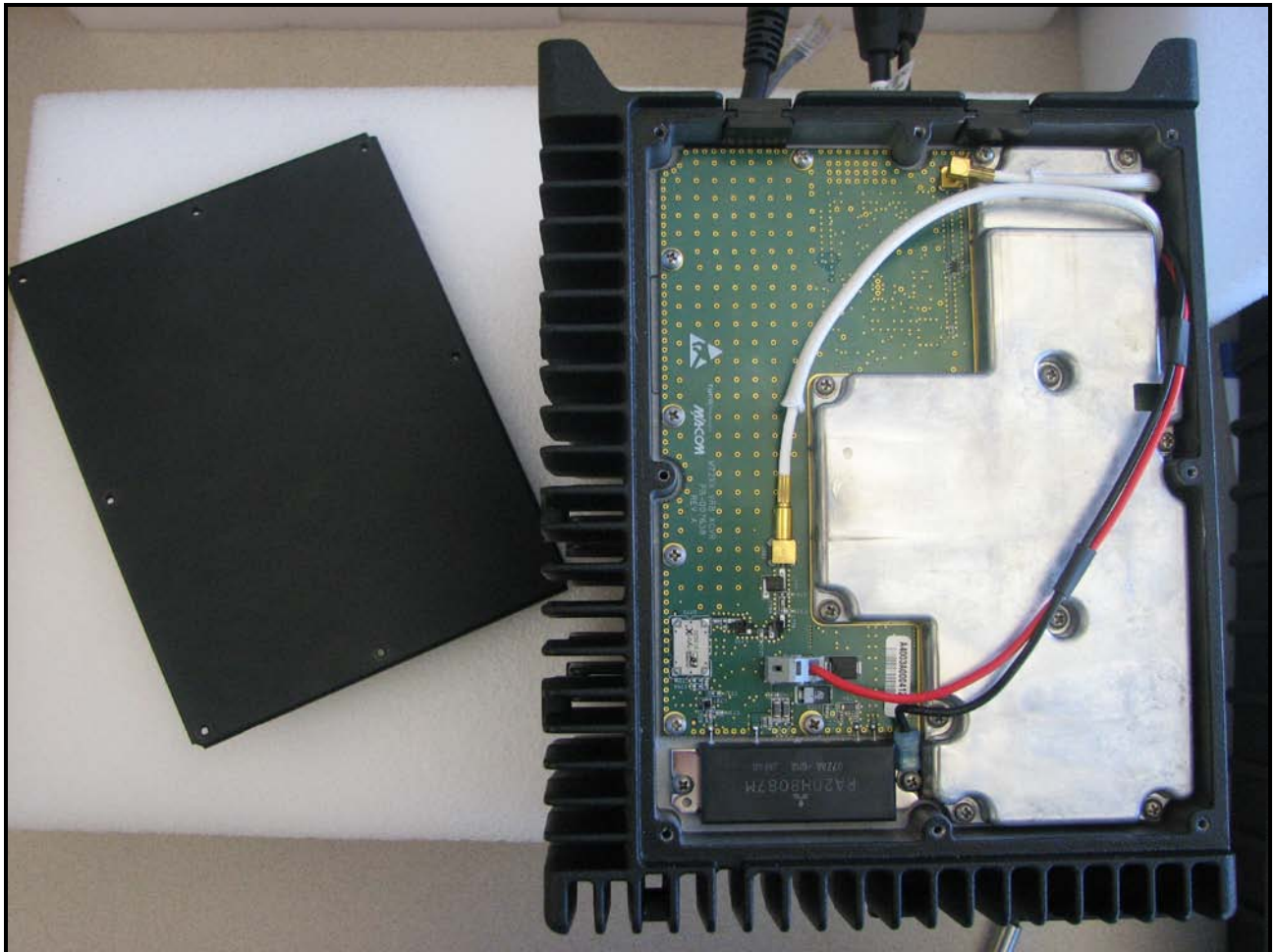
Photograph 16: Dup Filter VRB Side



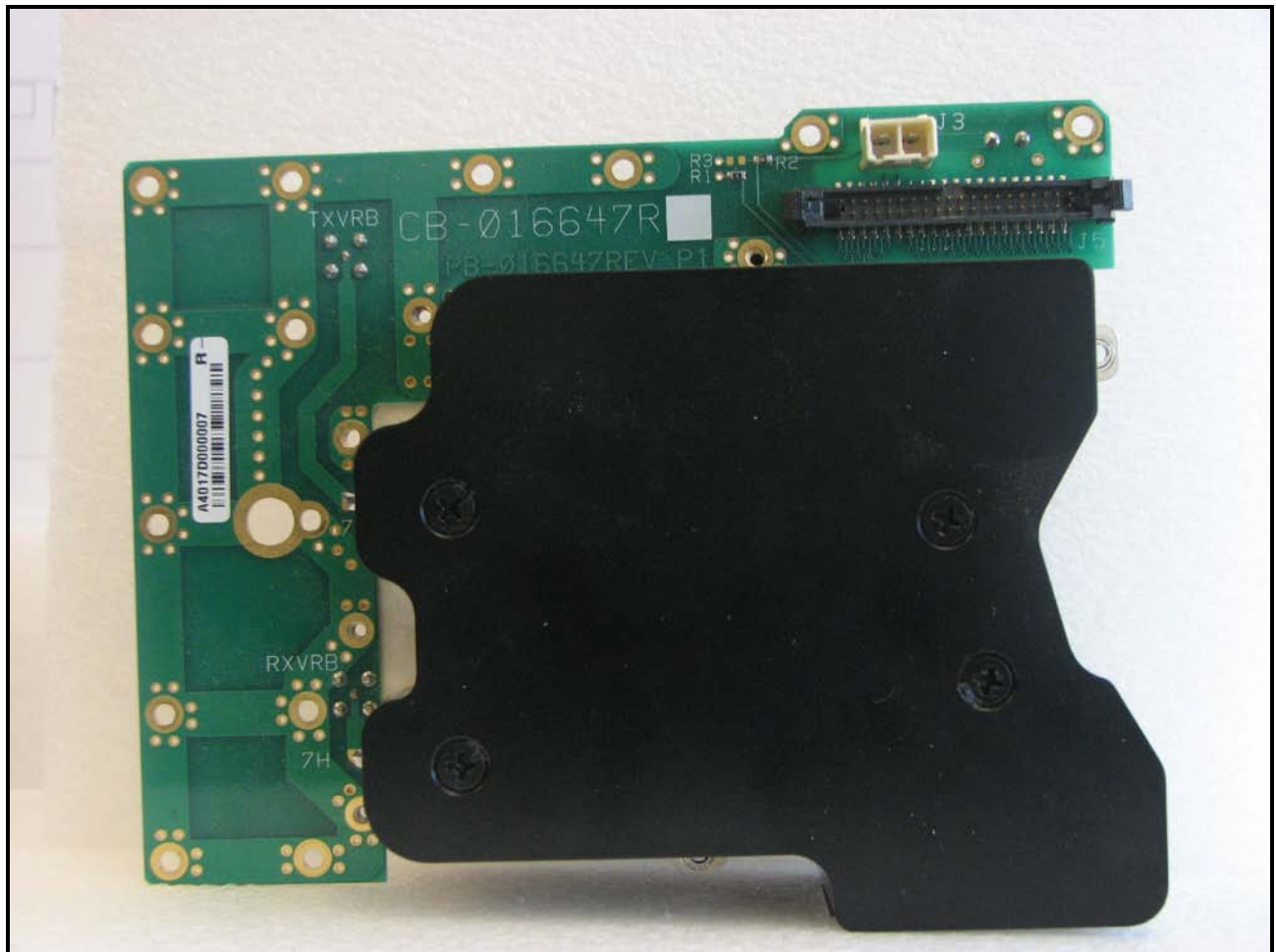
Photograph 17: **TC Main**



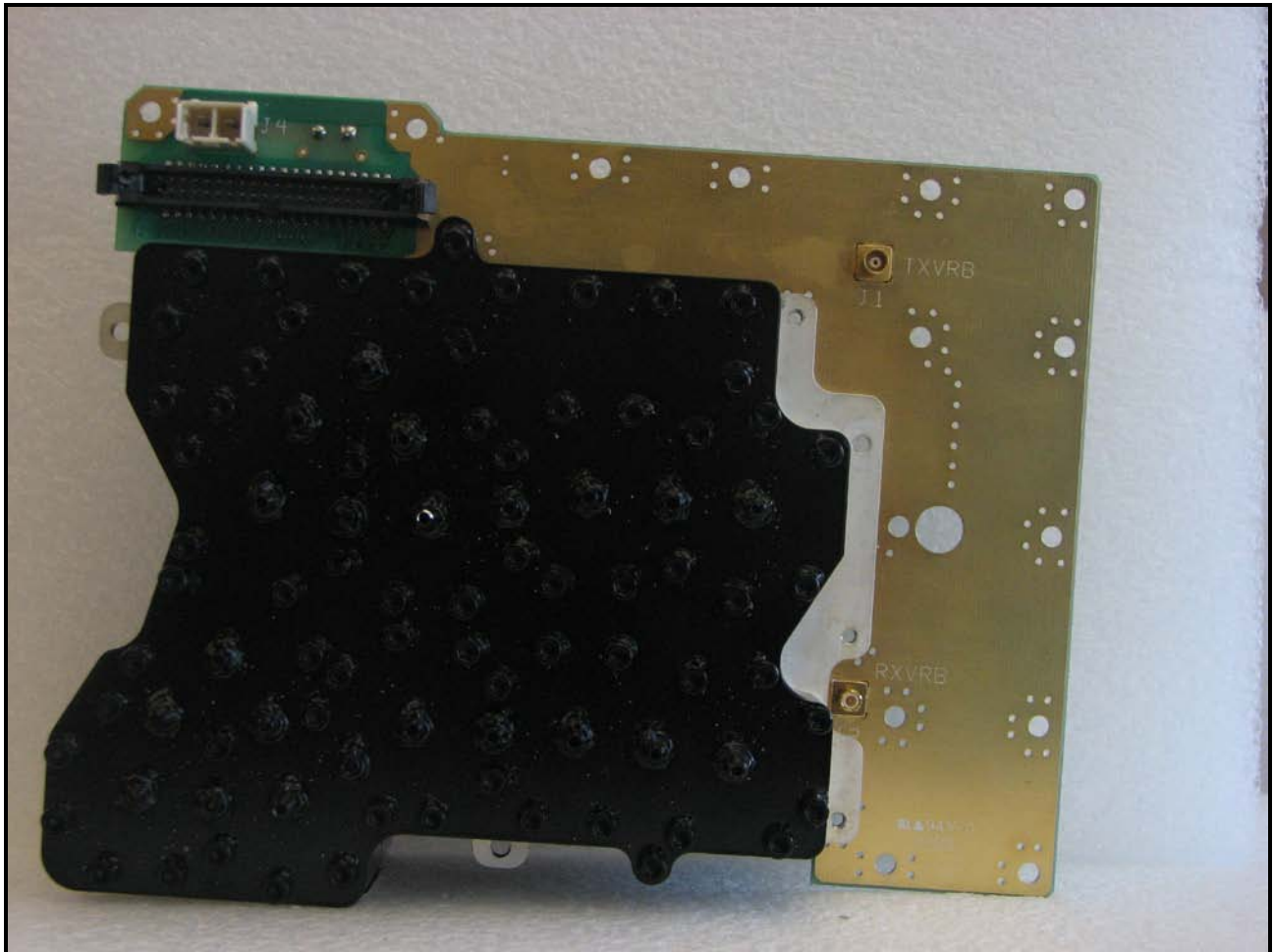
Photograph 18: TC IO



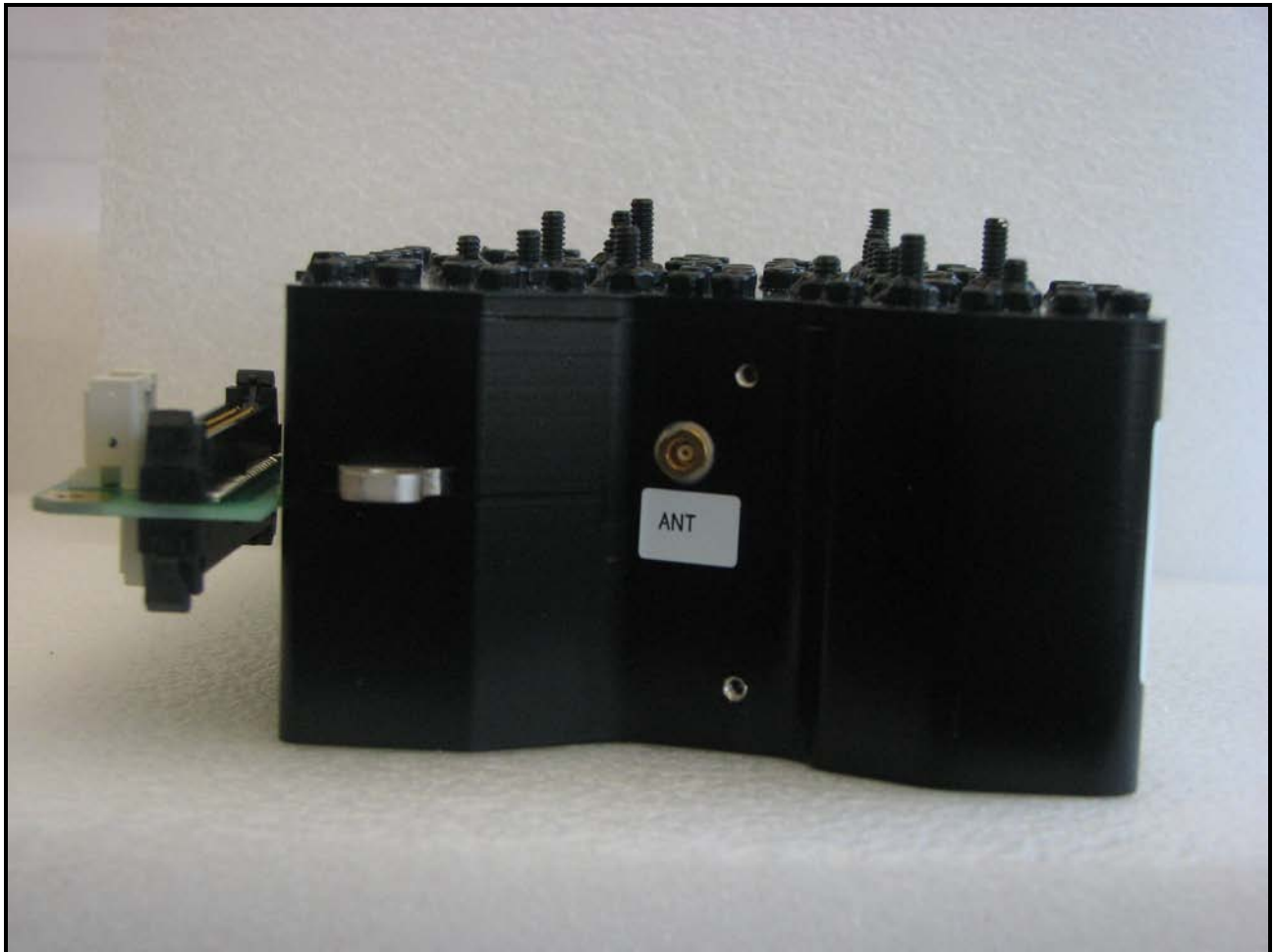
Photograph 19: TRX



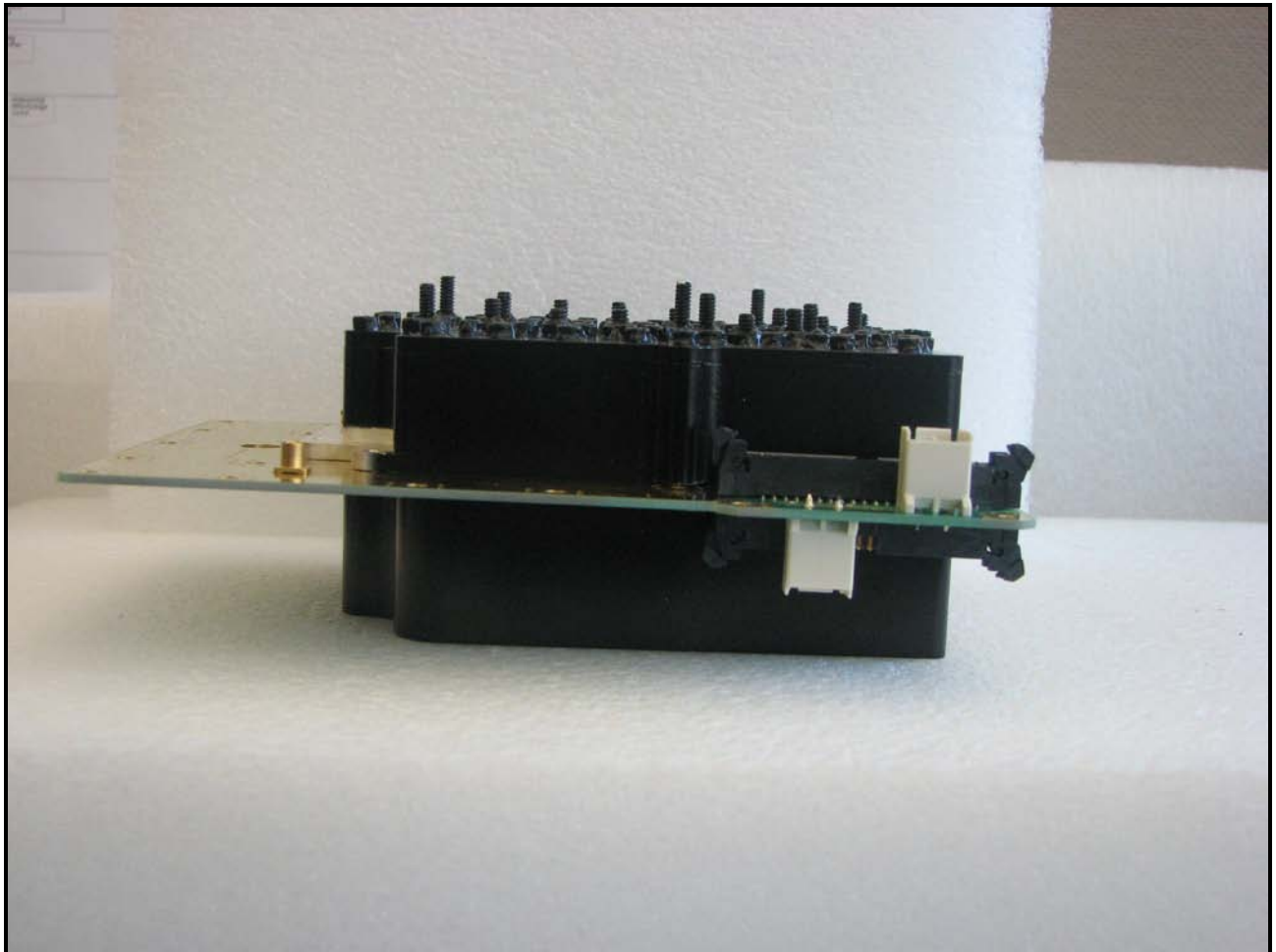
Photograph 20: Dup Filter Side 1



Photograph 21: **Dup Filter Side 2**



Photograph 22: **Dup Filter Side 3**



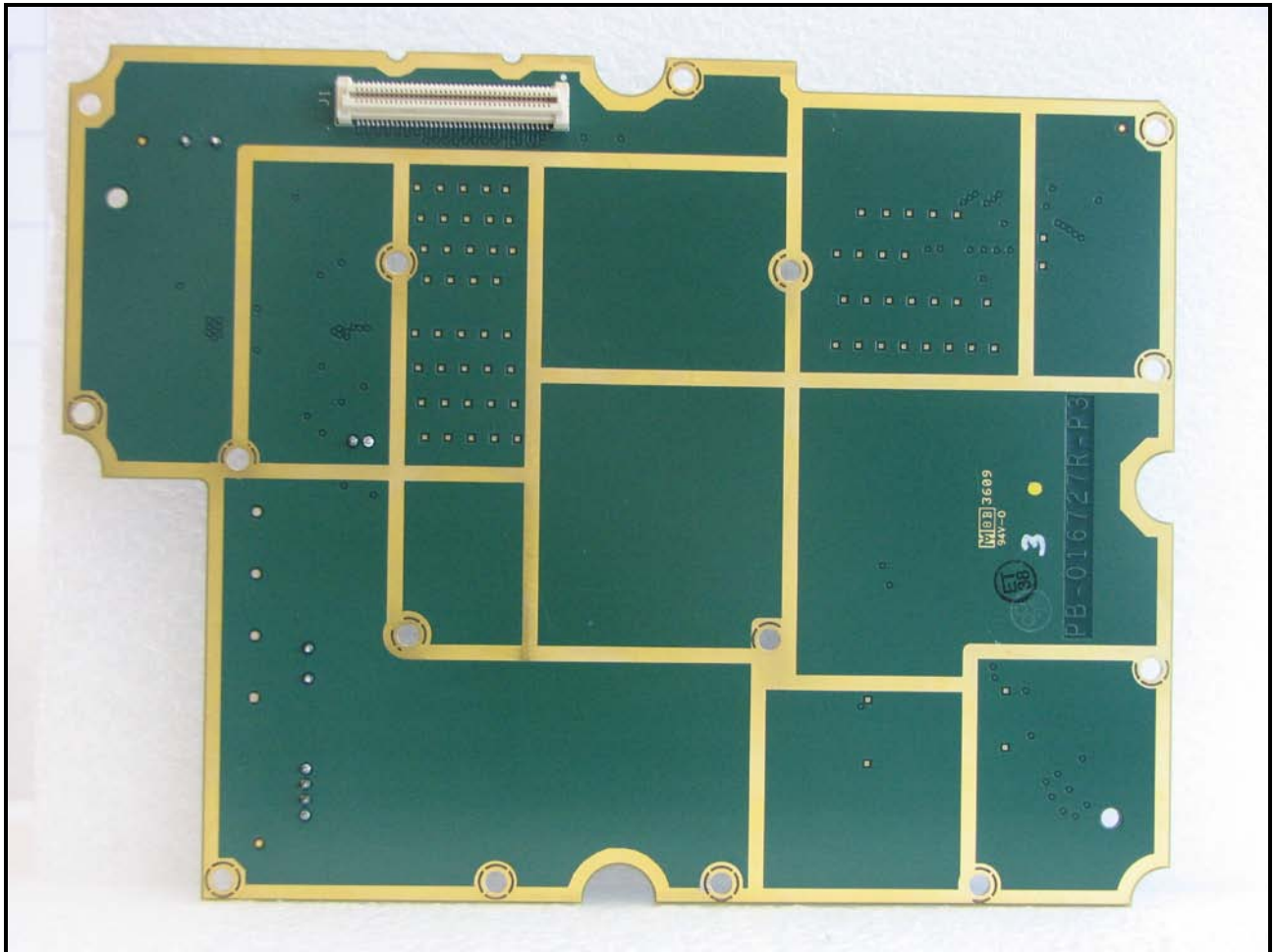
Photograph 23: **Dup Filter Side 4**



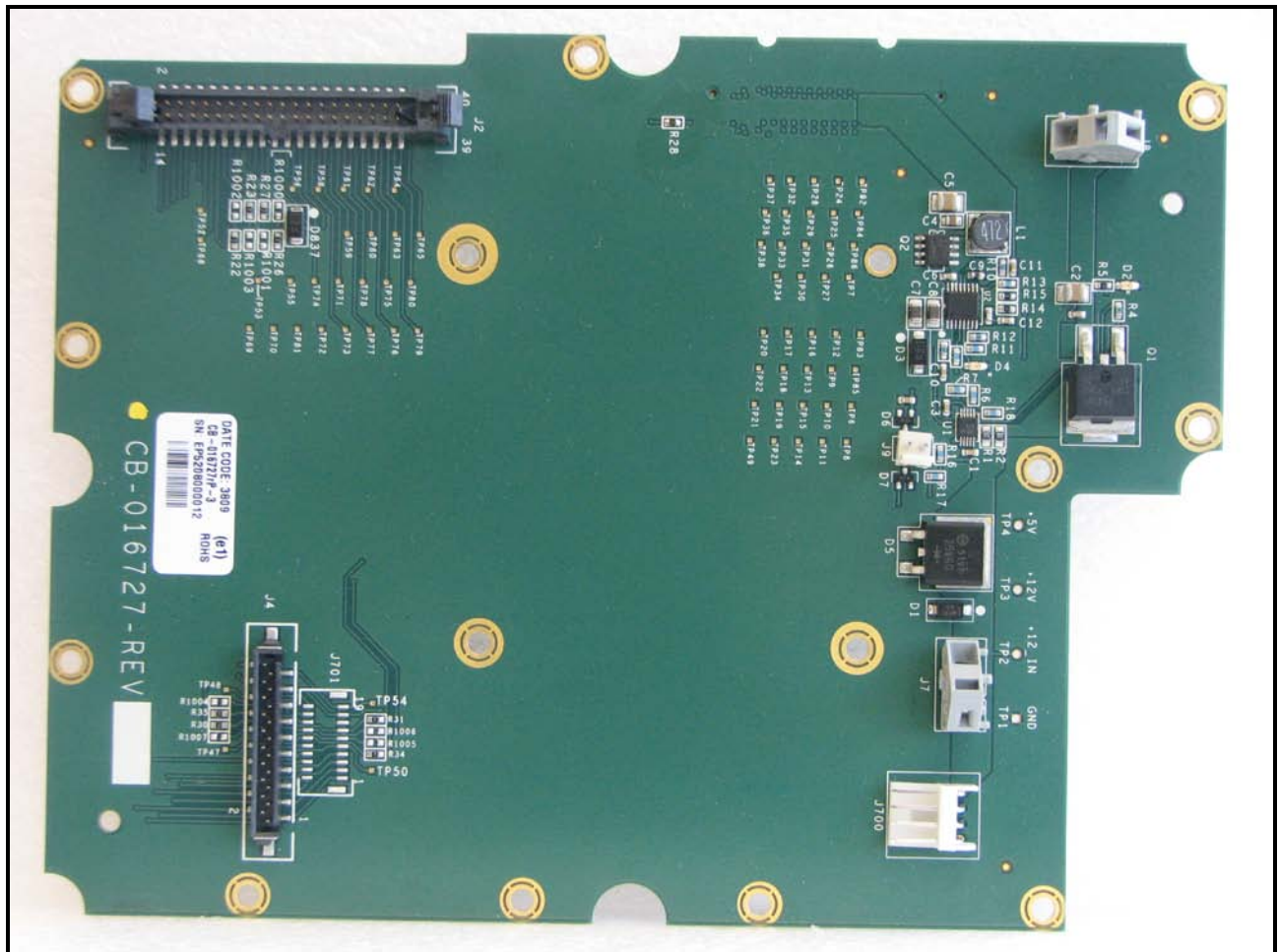
Photograph 24: **Dup Filter Side 5**

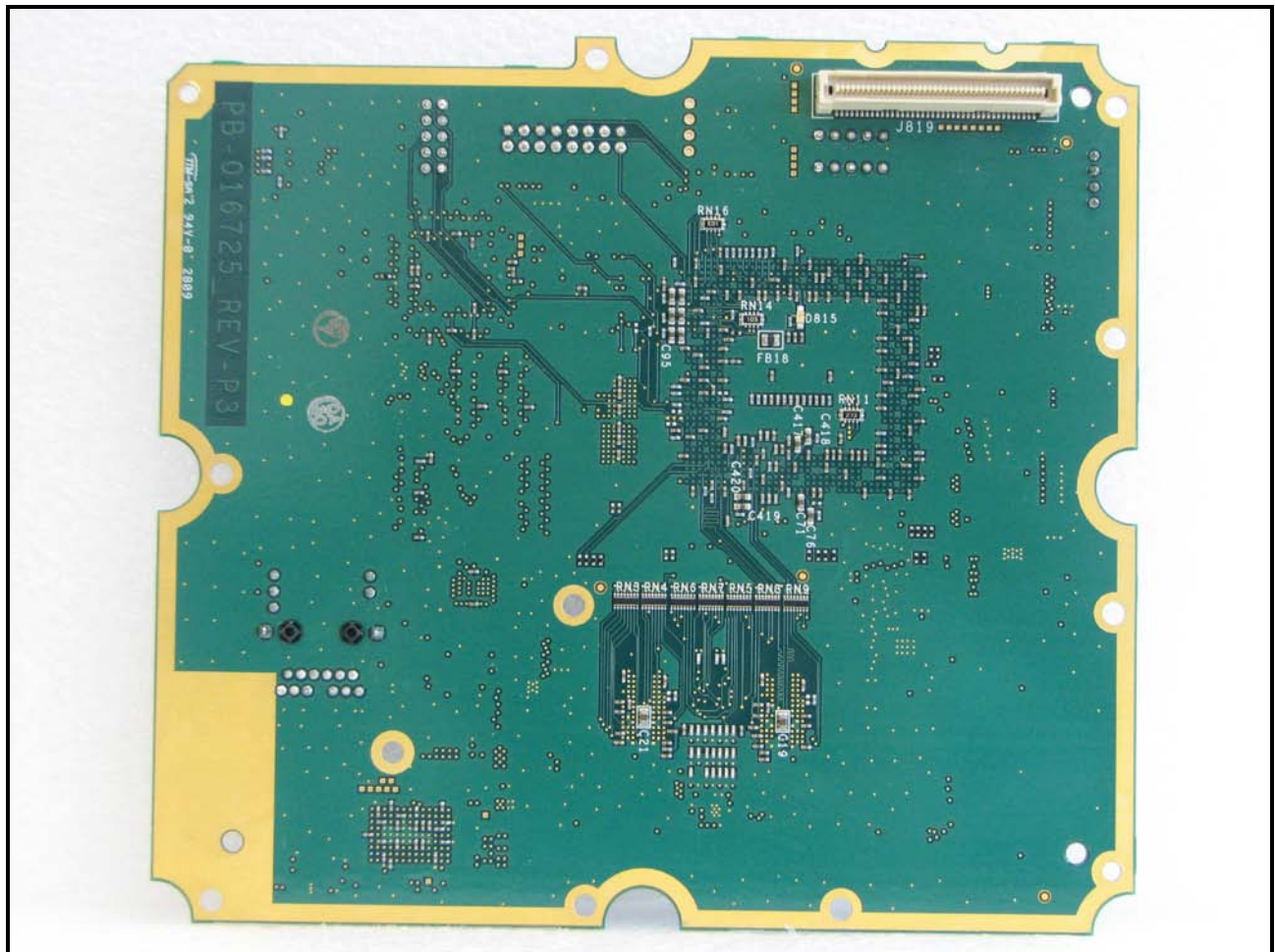


Photograph 25: **Dup Filter Side 6**

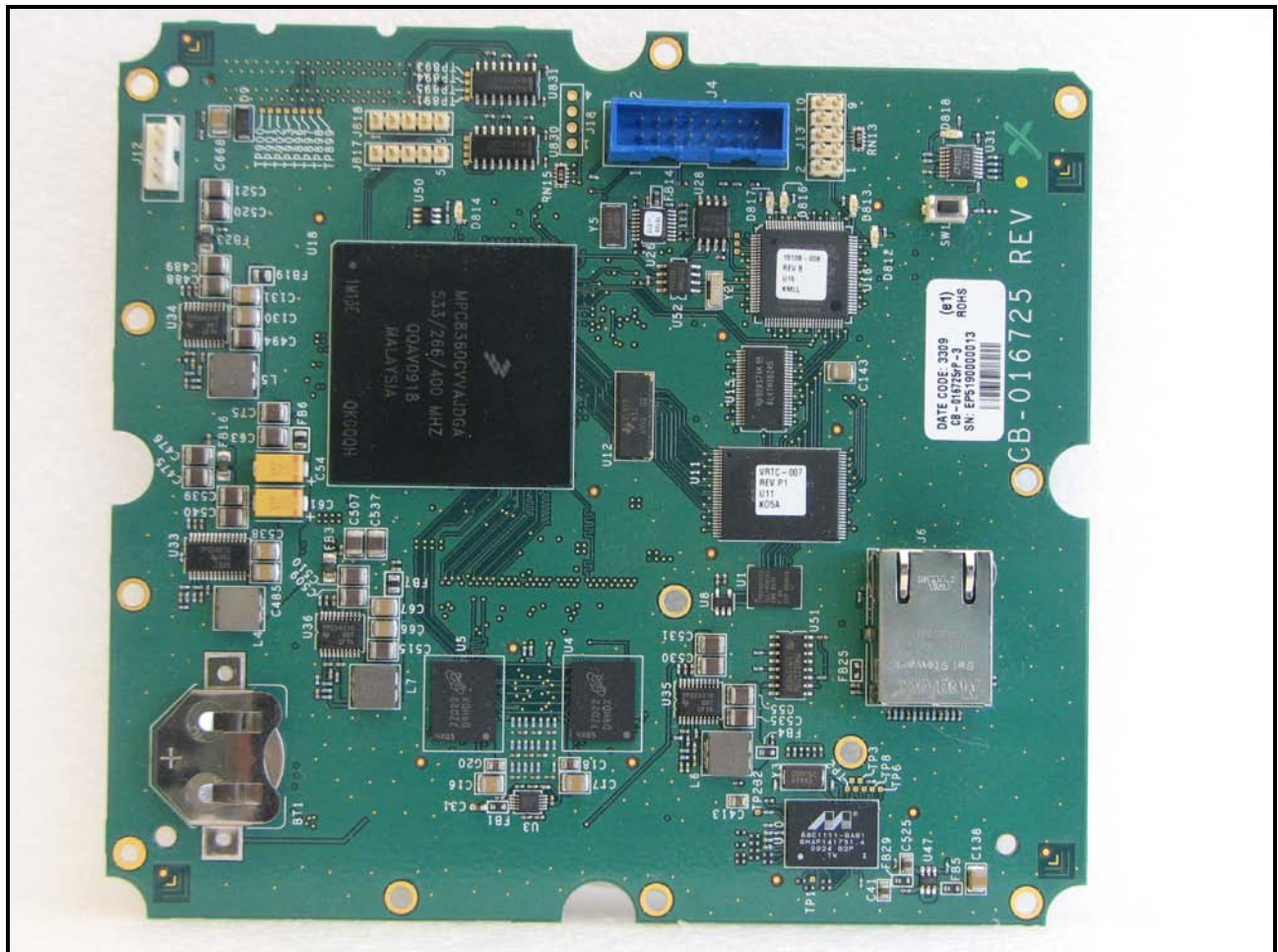


Photograph 26: TC IO Back

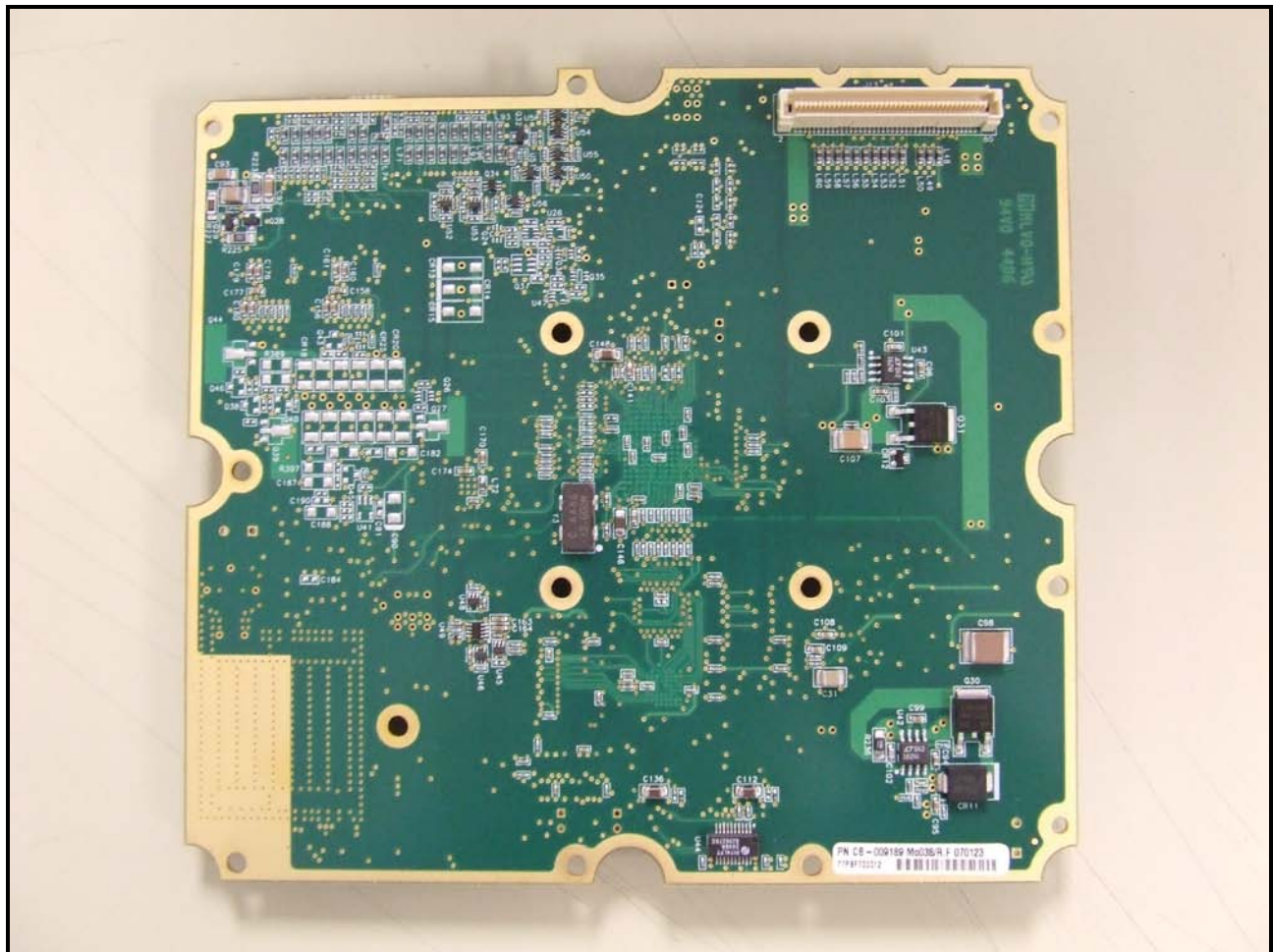




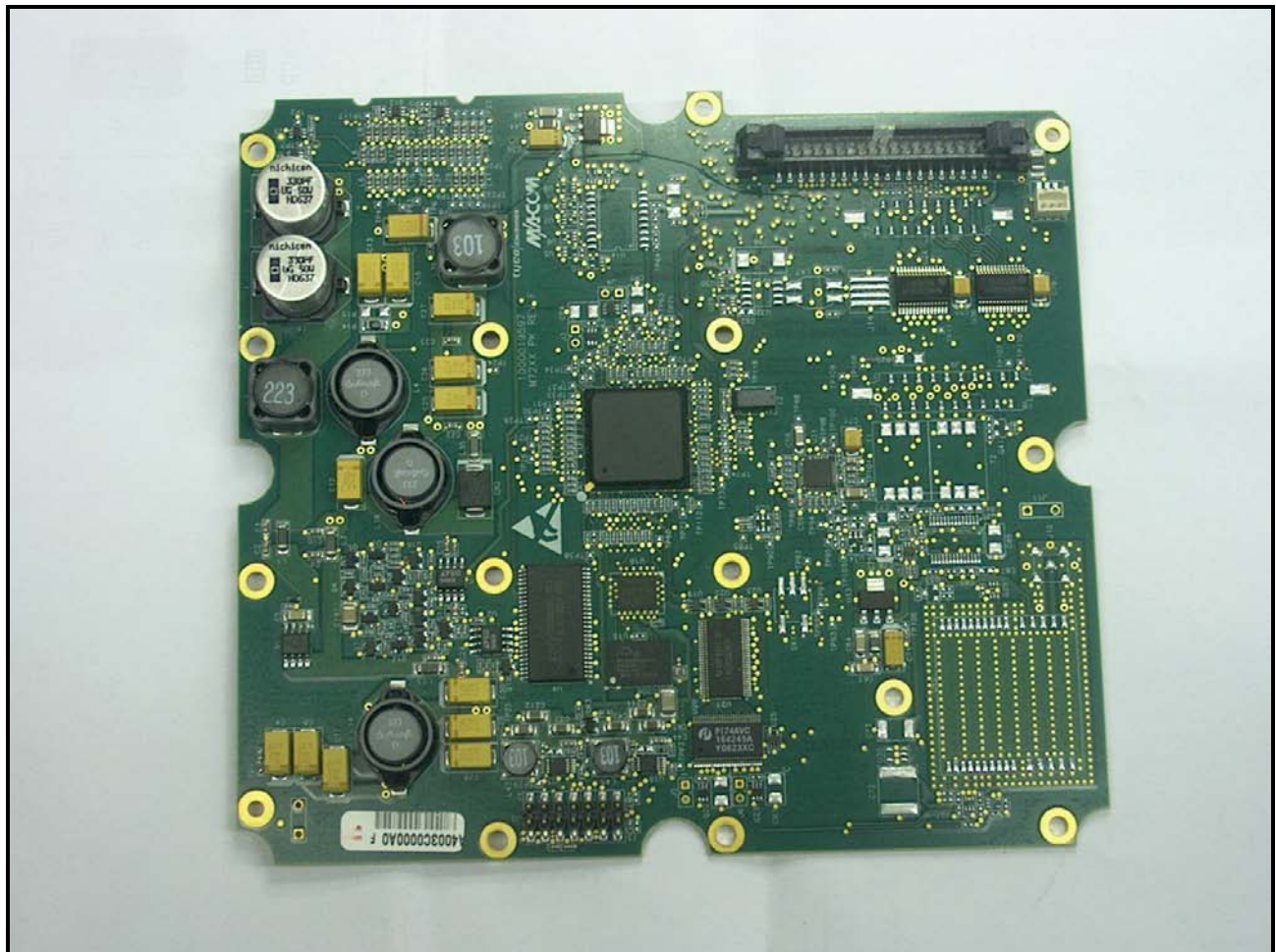
Photograph 28: TC Main Back



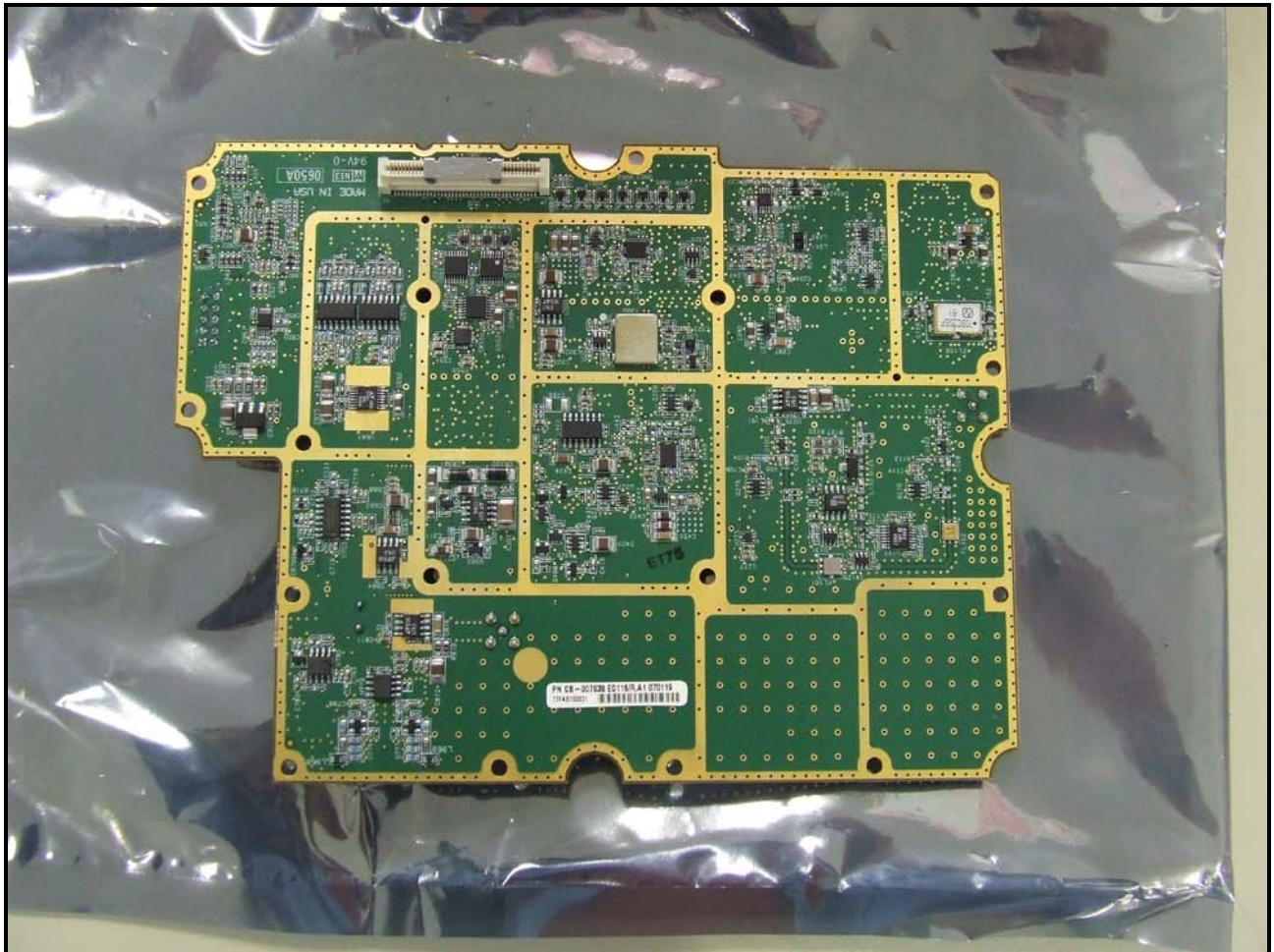
Photograph 29: TC Main Top



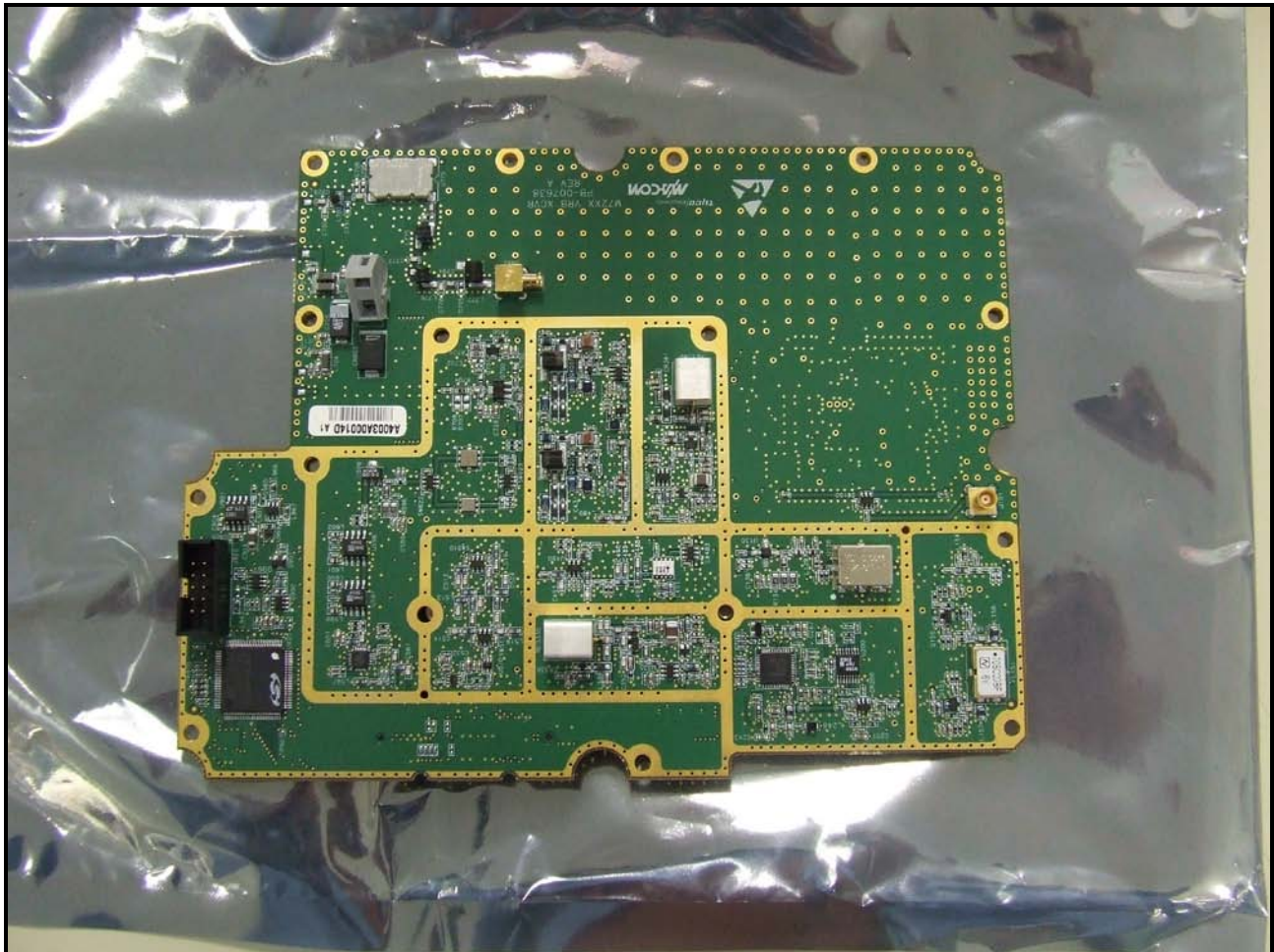
Photograph 30: VRB Digital Processing Kernel Board Bottom



Photograph 31: VRB Digital Processing Kernel Board Top



Photograph 32: VRB RF XCVR Board Bottom



Photograph 33: VRB RF XCVR Board Top