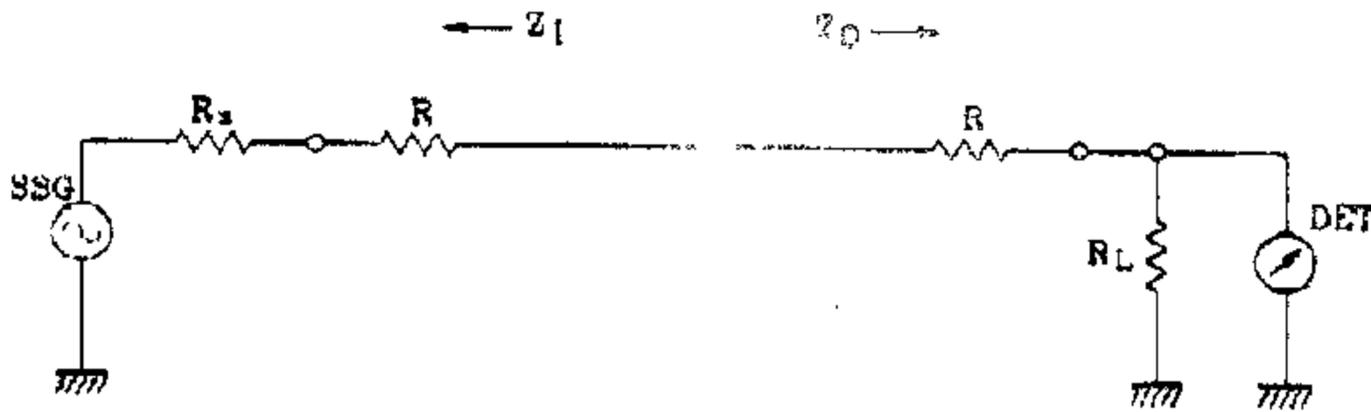


5

KF448BS/BV

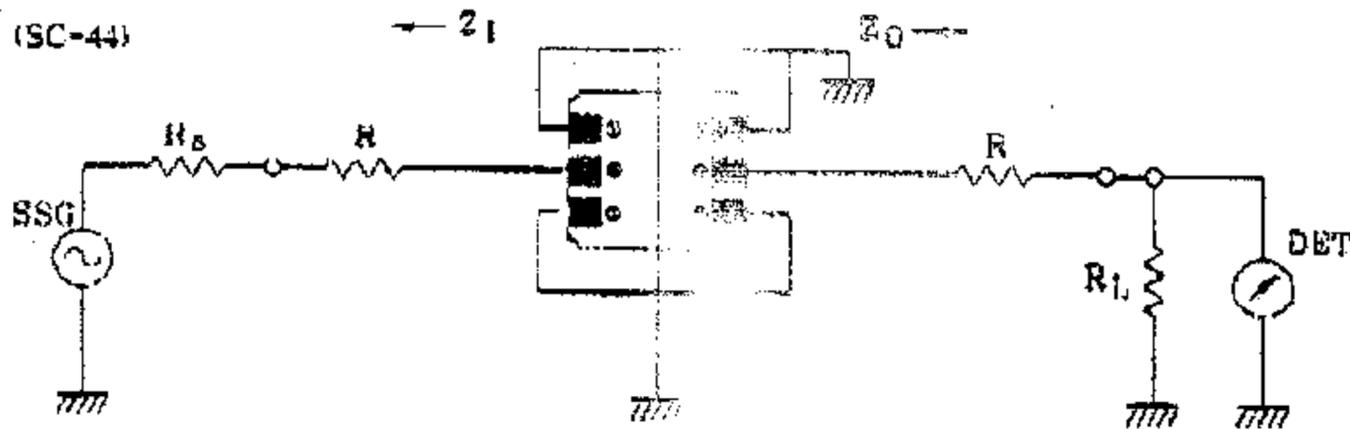
TEST CIRCUIT

REFERENCE LEVEL TEST CIRCUIT



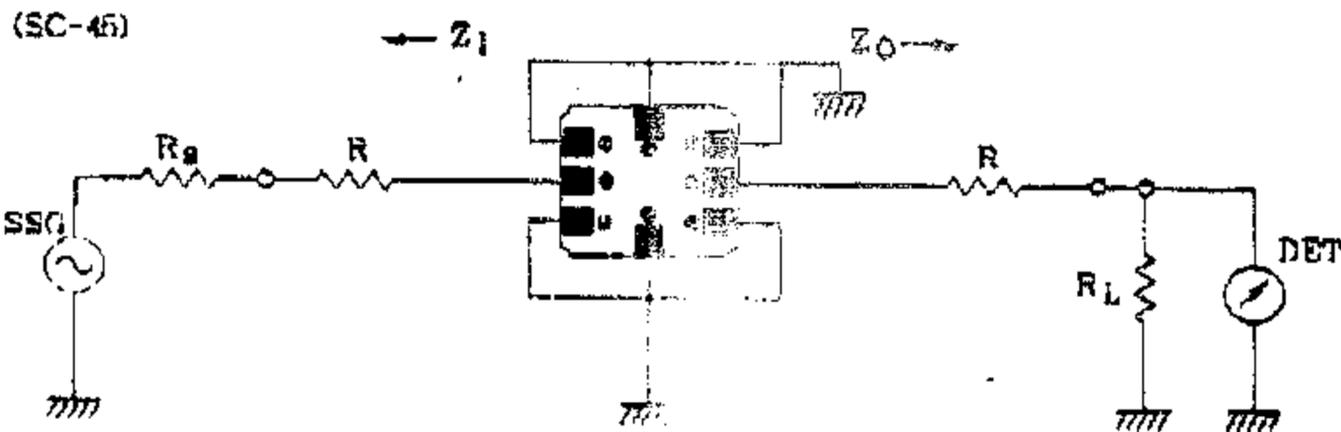
$R_s, R_L : 50\Omega$ (Internal Impedance of Source and Load)
 $R : 100\Omega$
 $Z_1(Z_0) = R_s(R_L) + R$

MEASUREMENT CIRCUIT



1: Input 2,3: Ground 4: Output

$R_s, R_L : 60\Omega$ (Internal Impedance of Source and Load)
 $R : 100\Omega$
 $Z_1(Z_0) = R_s(R_L) + R$



2: Input 1,3,4,6,7,8: Ground 5: Output