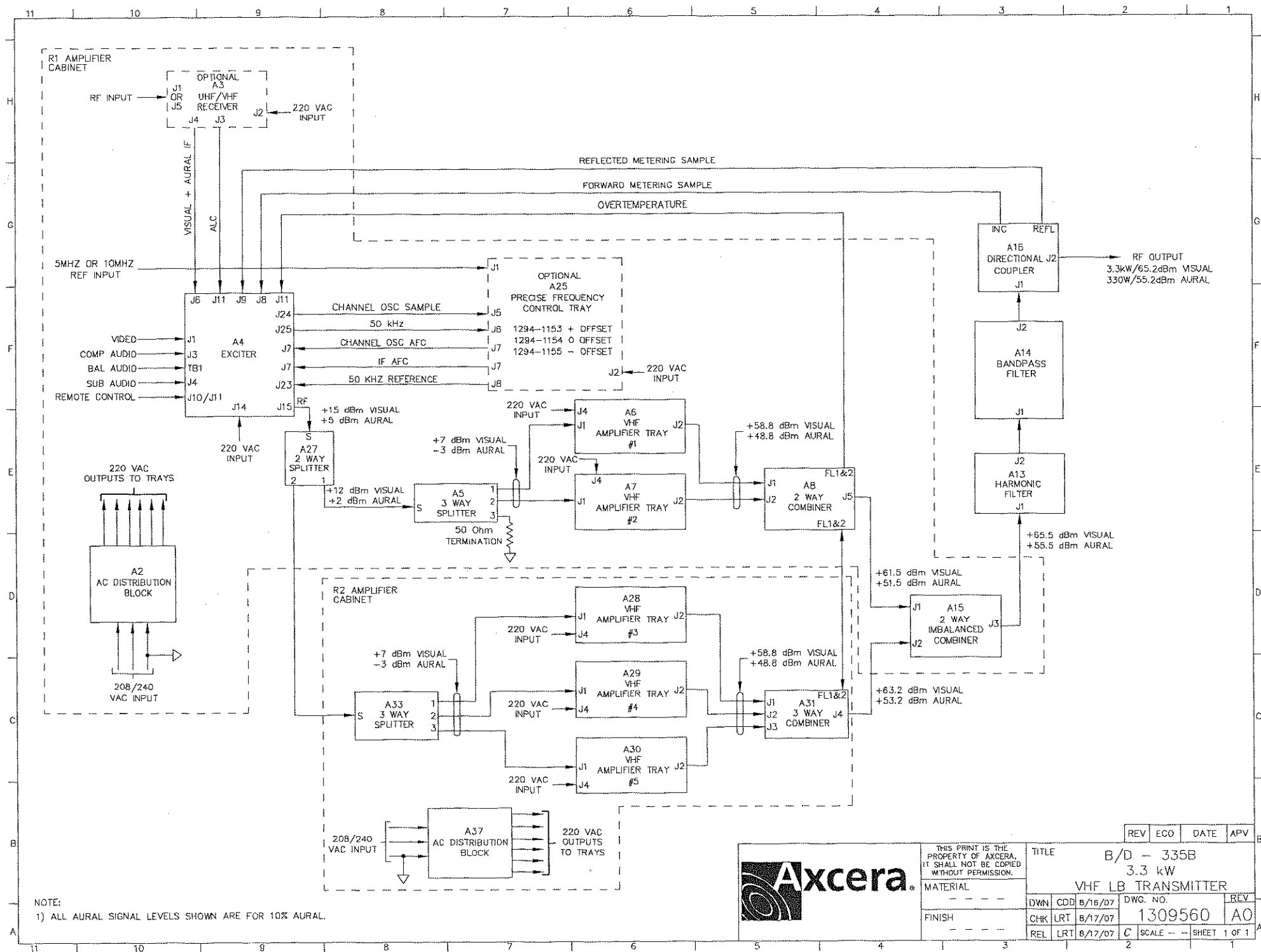
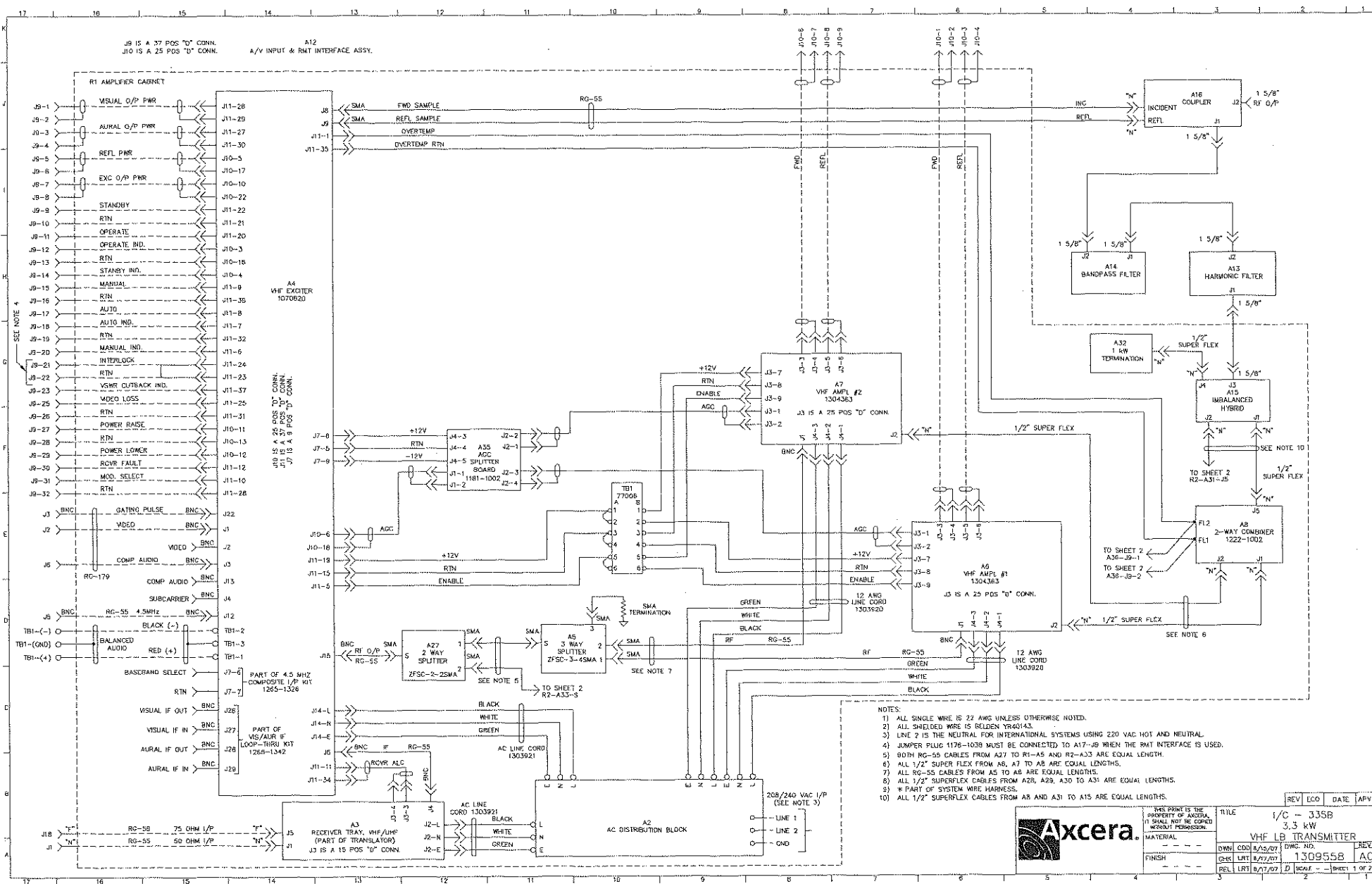
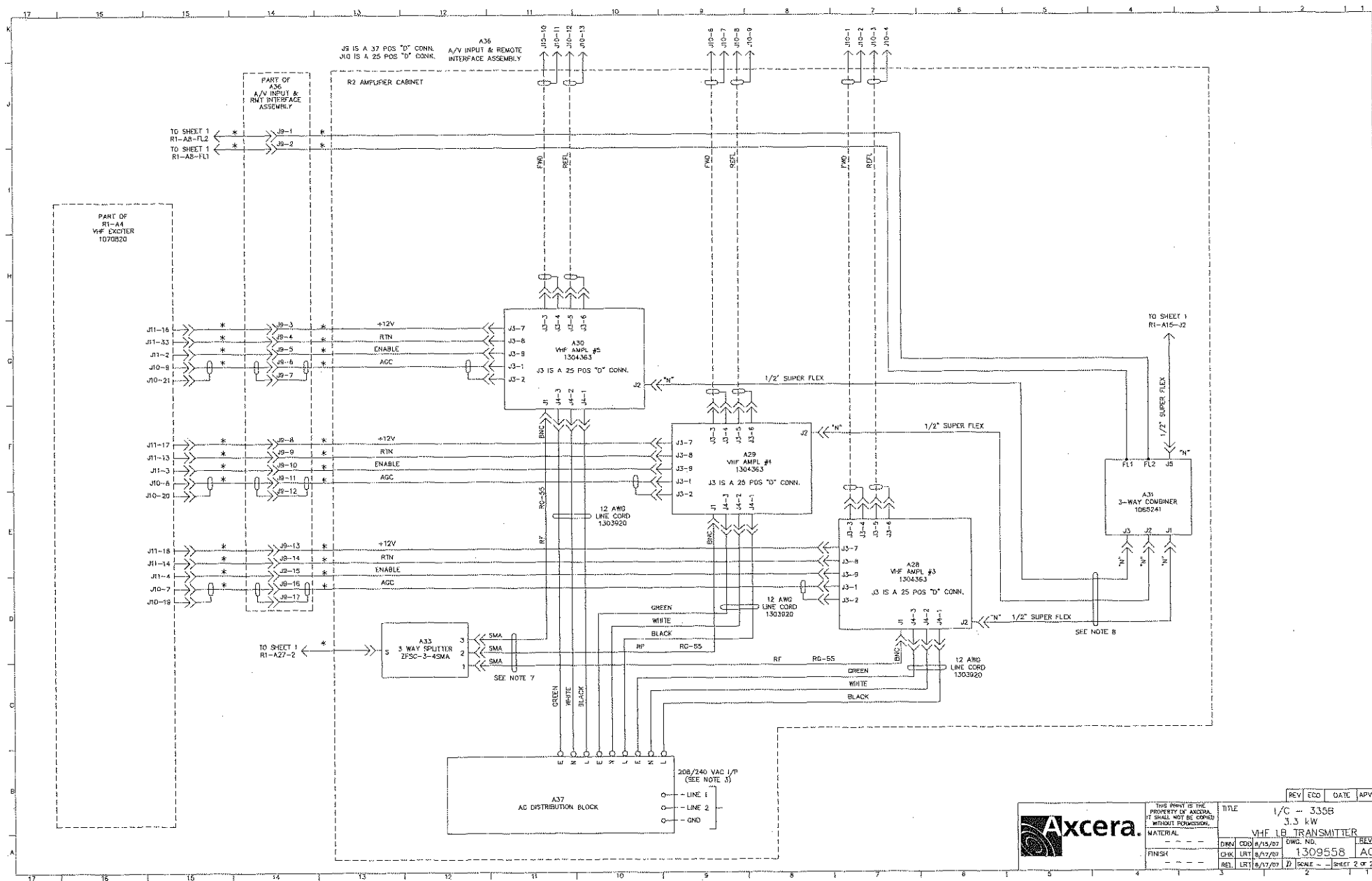
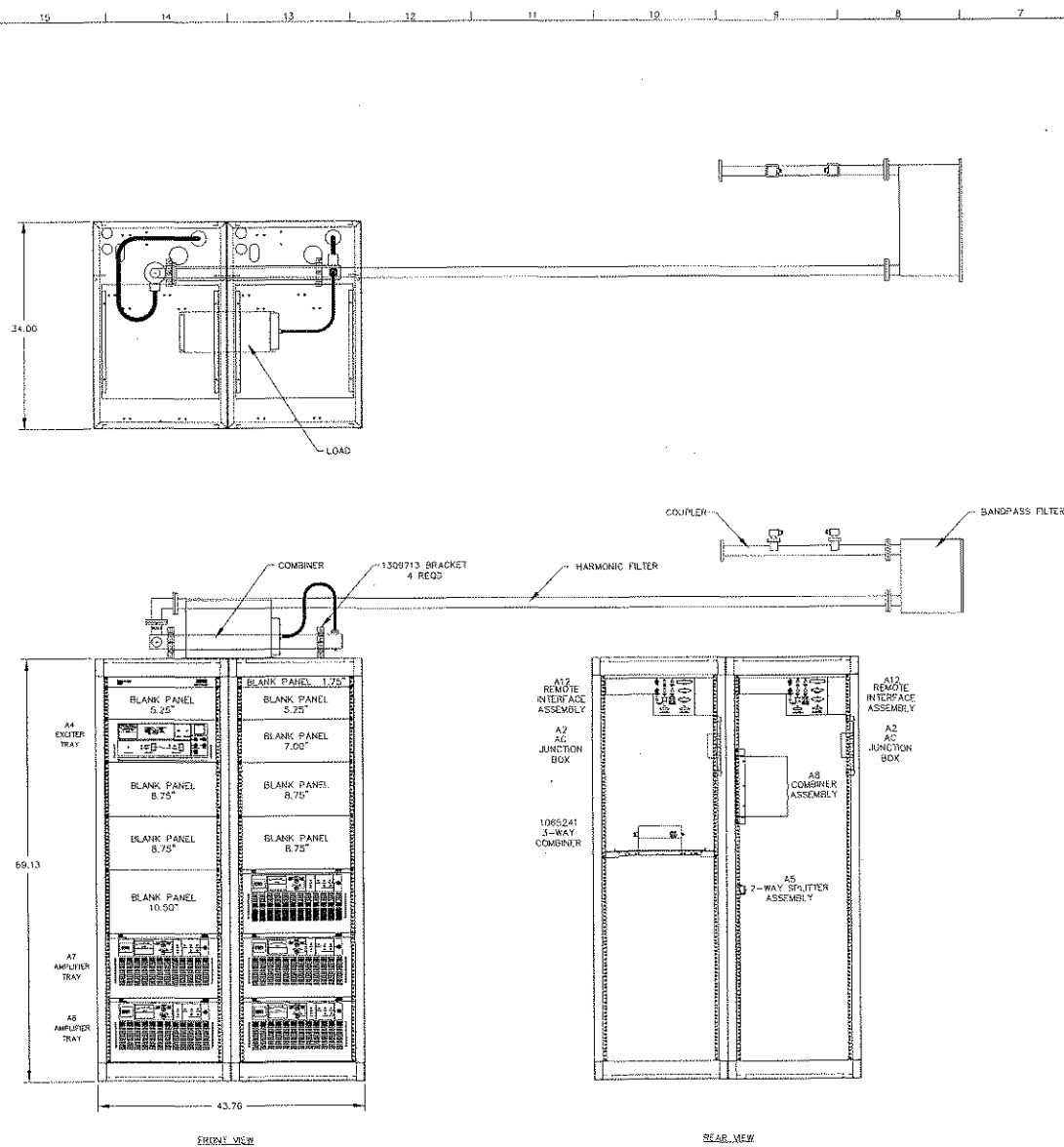




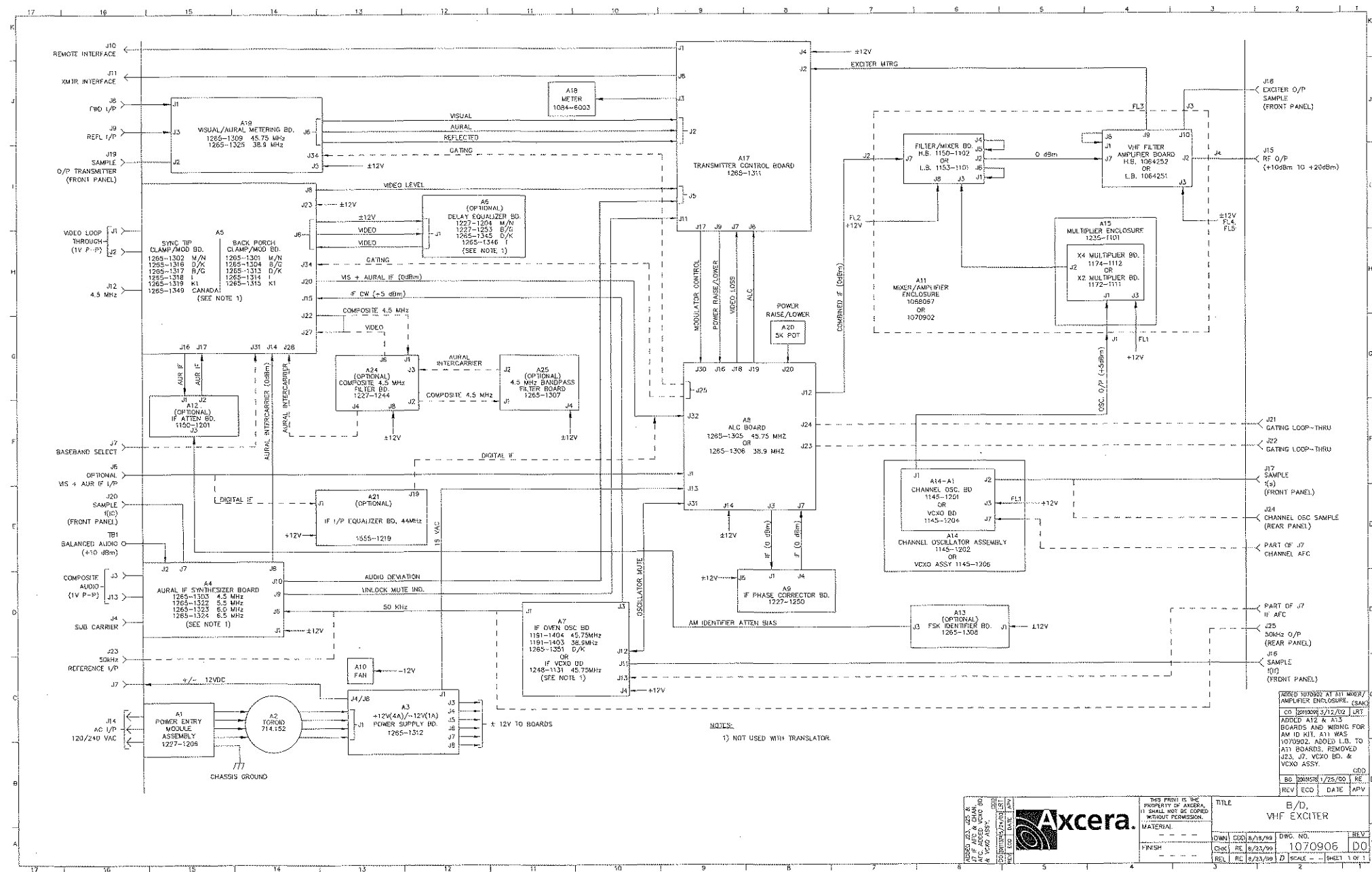


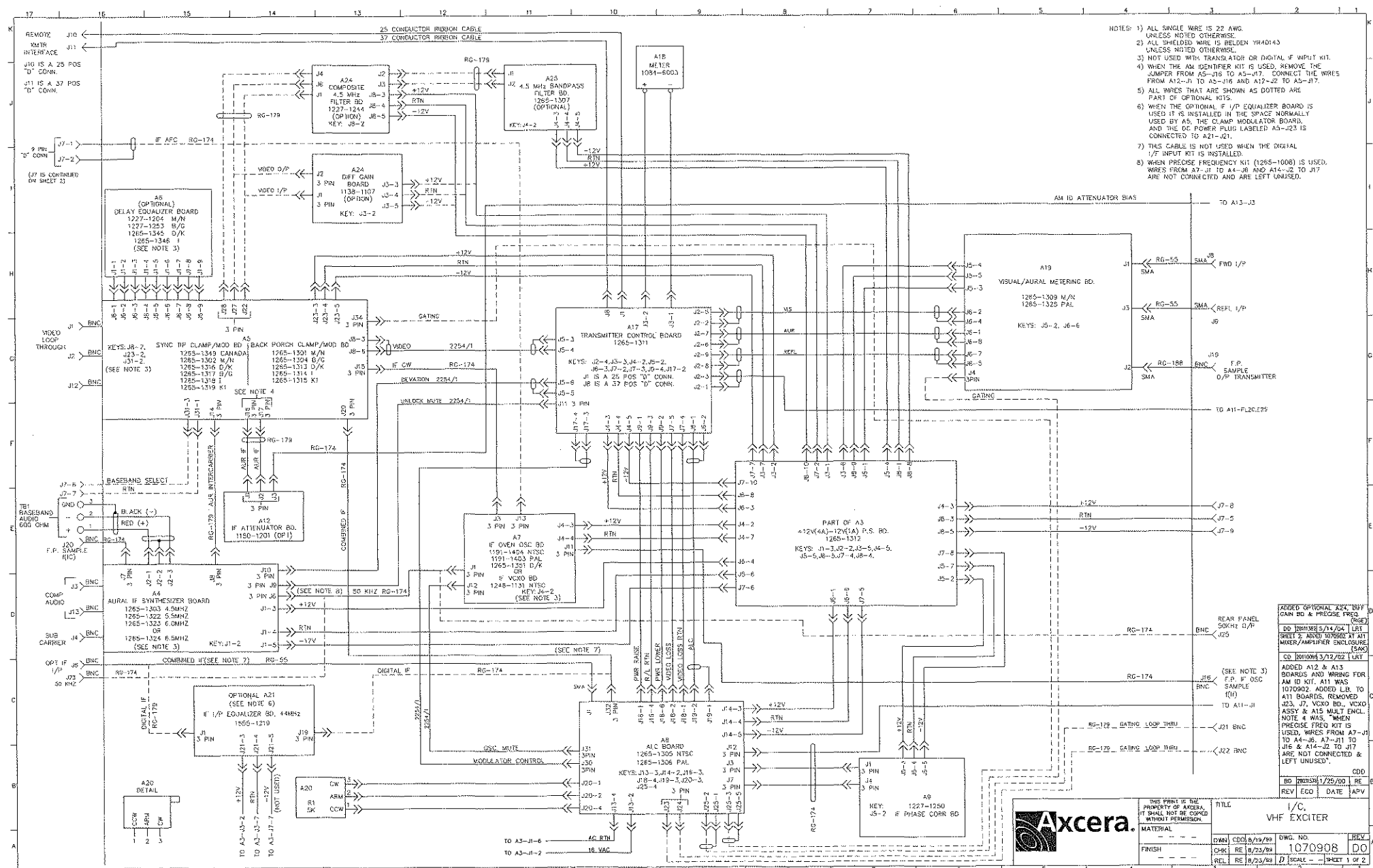


		THE DRAWING IS THE PROPERTY OF AXCERA. IT SHALL NOT BE COPIED OR REPRODUCED WITHOUT PERMISSION.		REV ECD DATE APV	
		TITLE 1/C -- 335B 3.3 kW VHF 1B TRANSMITTER		DWG. NO. 1309558	
MATERIAL		DRAWN	8/15/07	DATE	REV
FINISH		CHK	8/17/07	DATE	REV
		REL	8/17/07	DATE	REV
		SCALE		SHEET 2 OF 2	

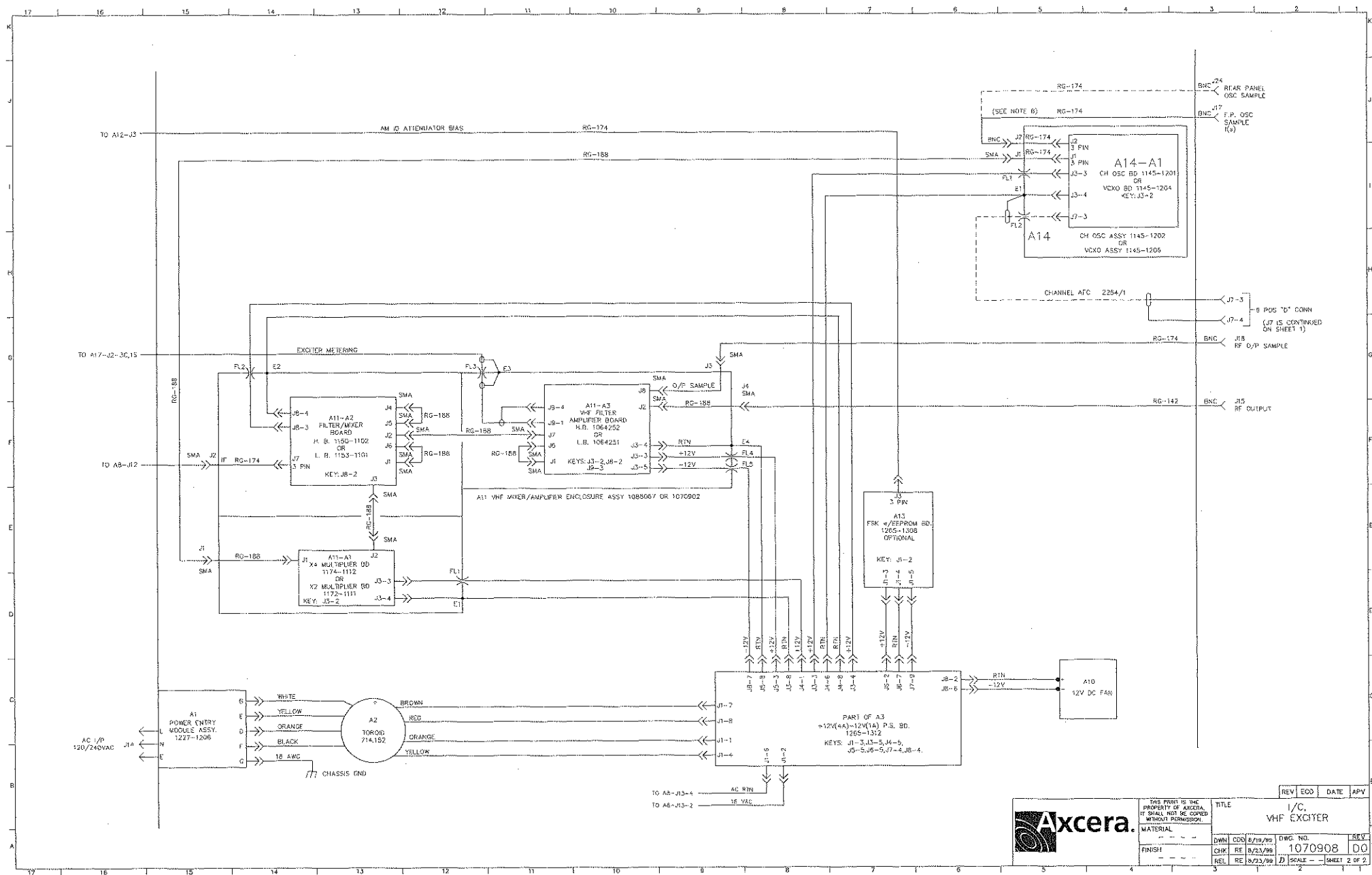


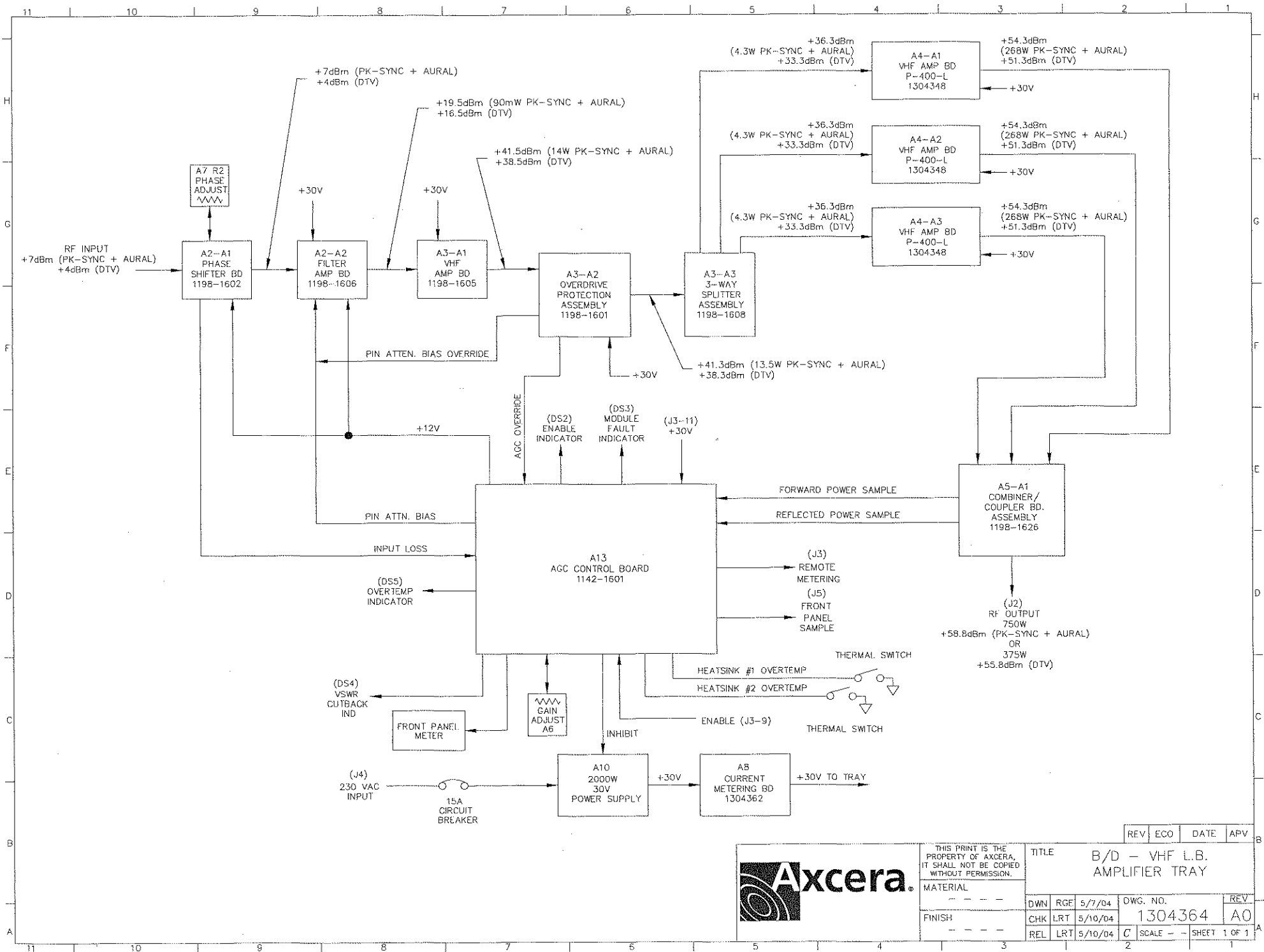
		REV		ECG	DATE	APV
		TITLE RACKING PLAN, 3358 VHF TRANSMITTER				
MATERIAL _____ _____ _____		DWG. NO. 1309656		DESIGNED BY AA		
CHECKED BY _____		DATE 9/14/07		SCALE 1 OF 1		





TITLE		I/C, VHF EXCITER	
DWG.	CD	8/19/88	DWG. NO.
CHK	RE	8/23/88	1070908
REL	RE	8/23/88	D SCALE - - SHEET 1 OF 2

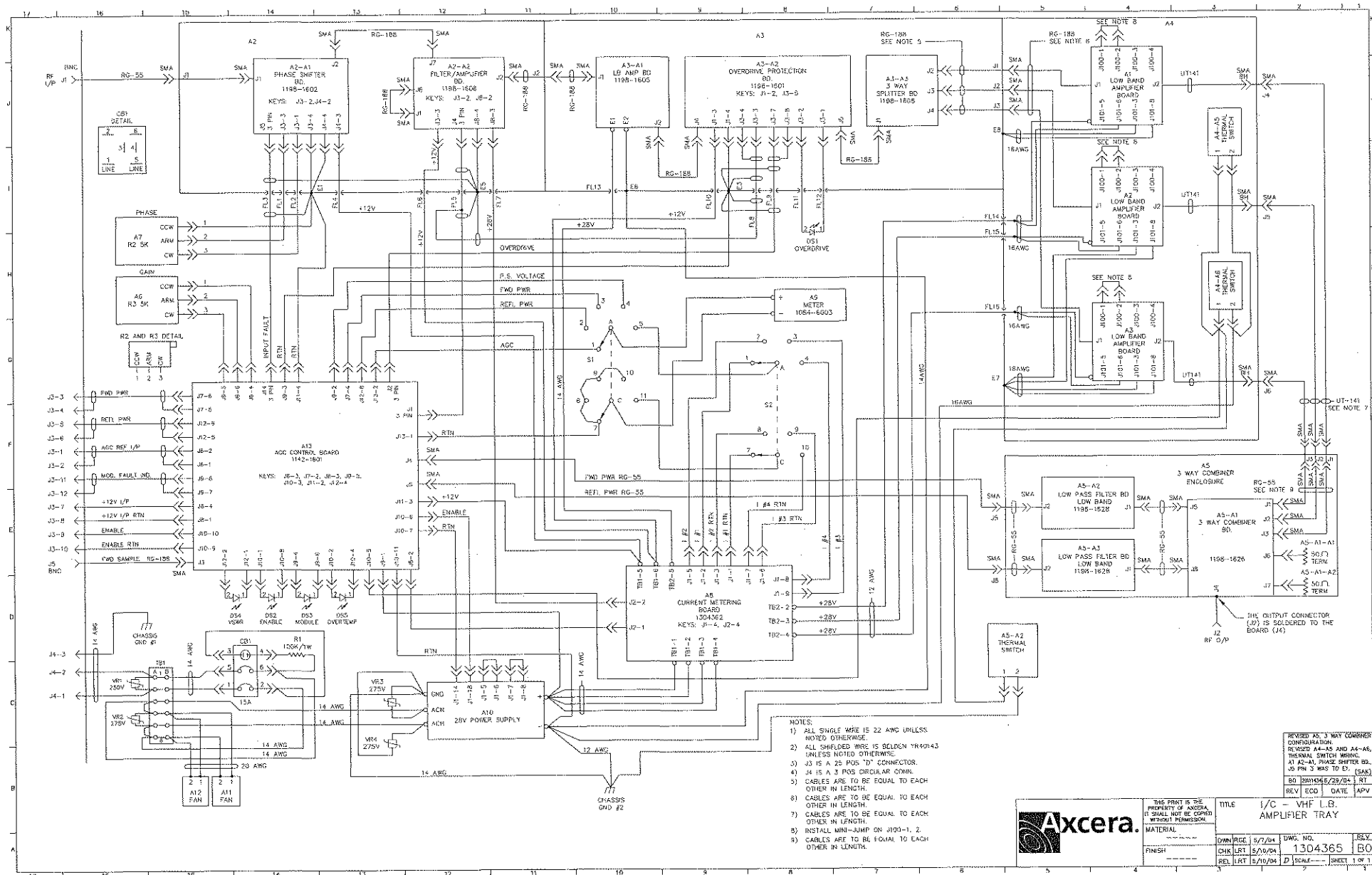




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MATERIAL
FINISH

TITLE				REV	
B/D - VHF L.B. AMPLIFIER TRAY				ECO	APV
DWN	RGE	5/7/04	DWG. NO.	REV	
CHK	LRT	5/10/04	1304364	A0	
REL	LRT	5/10/04	C	SCALE	SHEET 1 OF 1



NOTES:

- 1) ALL SINGLE WIRE IS 22 AWG UNLESS NOTED OTHERWISE.
- 2) ALL SHIELDED WIRE IS Belden 9440-43 UNLESS NOTED OTHERWISE.
- 3) J3 IS A 25 POS "D" CONNECTOR.
- 4) J4 IS A 3 POS CIRCULAR CONN.
- 5) CABLES ARE TO BE EQUAL TO EACH OTHER IN LENGTH.
- 6) CABLES ARE TO BE EQUAL TO EACH OTHER IN LENGTH.
- 7) CABLES ARE TO BE EQUAL TO EACH OTHER IN LENGTH.
- 8) INSTALL MINI-JUMP ON J100-1, 2.
- 9) CABLES ARE TO BE EQUAL TO EACH OTHER IN LENGTH.

REVISED A5, 3 WAY CORNER
CONFIGURATION.
REVISED A4-A5 AND A5-A6,
THERMAL SWITCH WIRING.
A7 A2-A1, PHASE SHIFTER BD.
J5 PIN 3 WAS TO E7. (SAK)

BD	2011/03/29/04	RT
REV	ECO	DATE APV

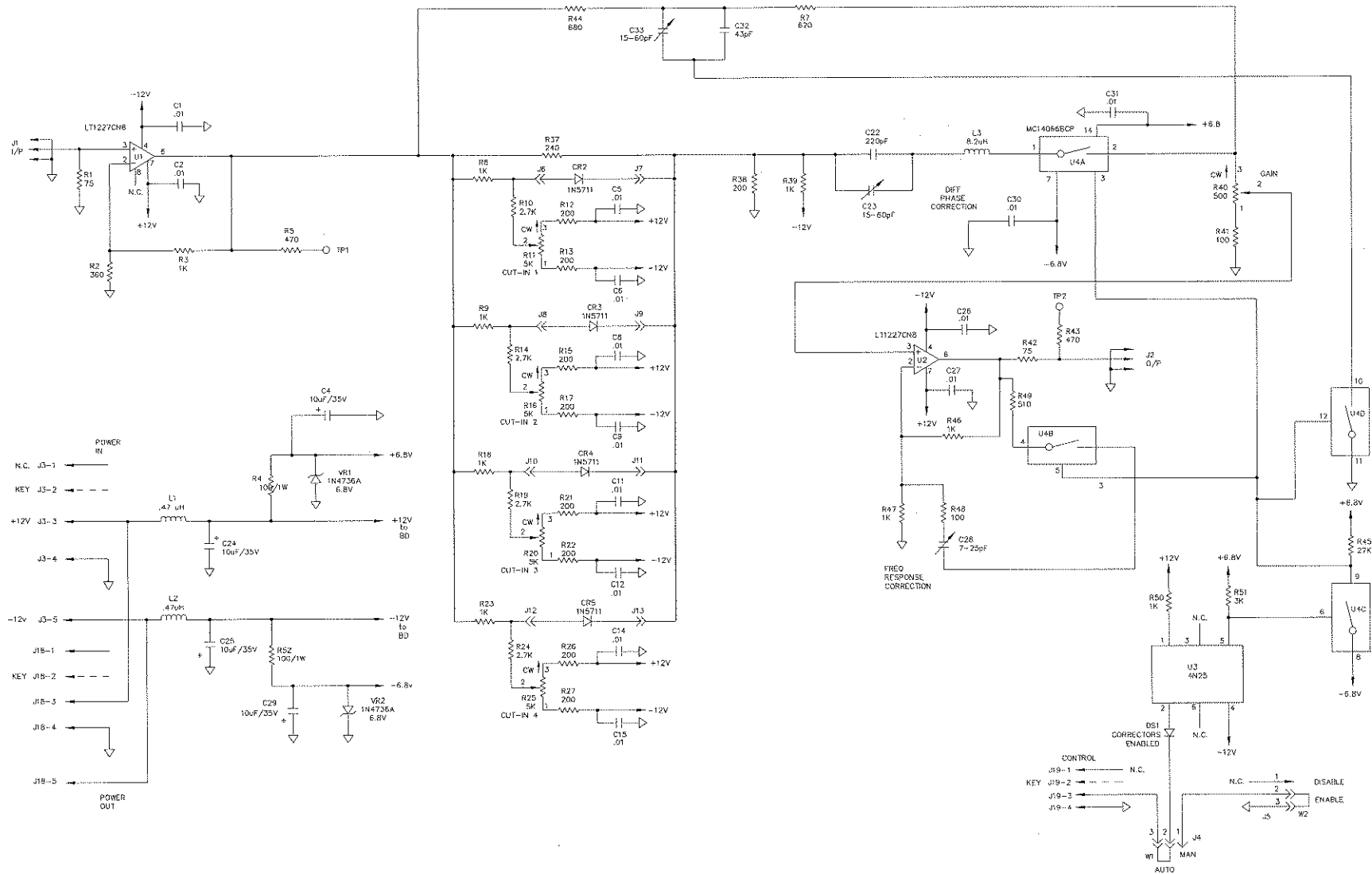
- VHF L.B.

AMPLIFIER TRAY

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DWG. NO.	REV.
1304365	BO

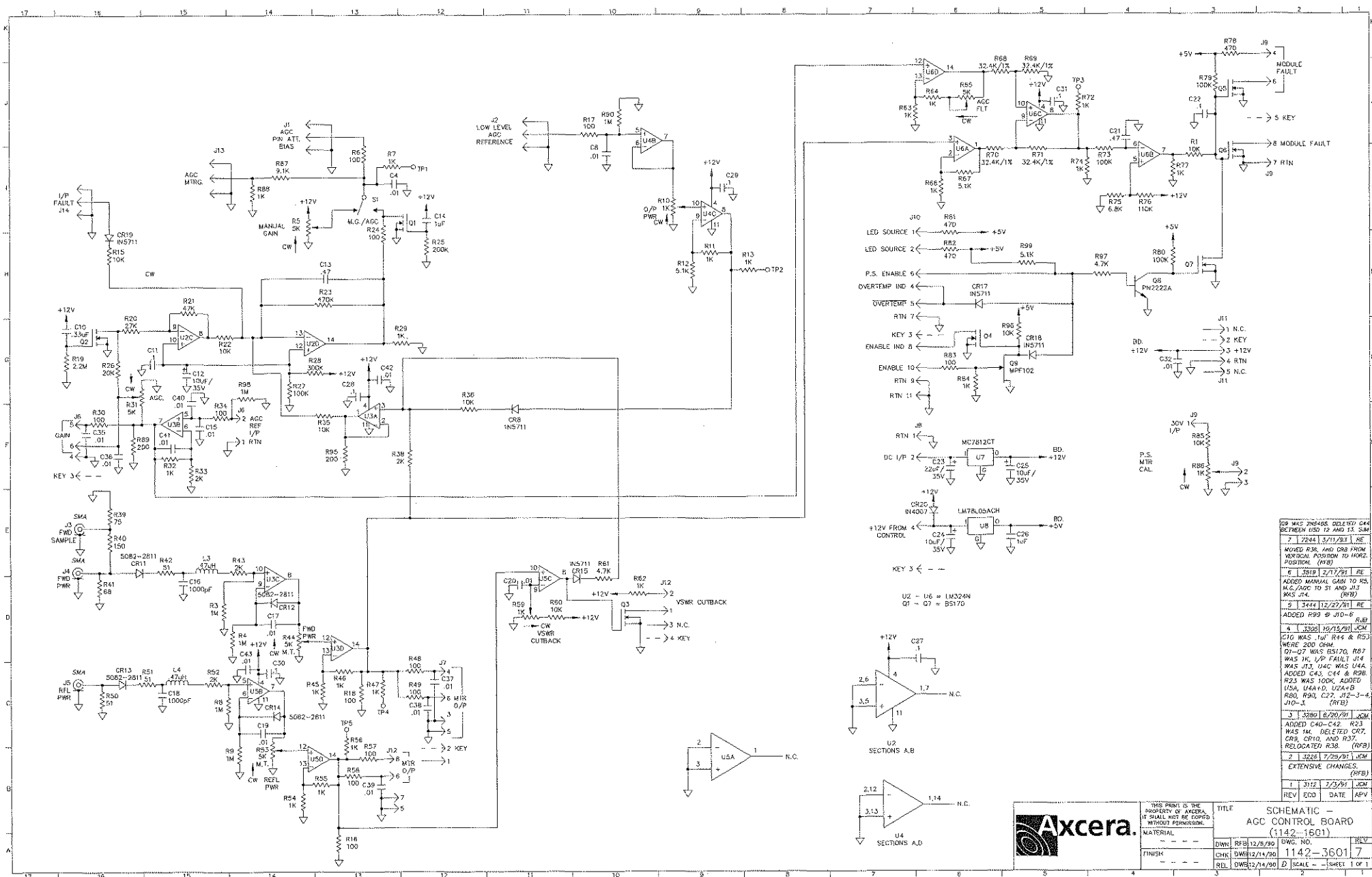
D	SCALE	SHEET 1 OF 1
---	-------	--------------



CHG0 LABELING OF
C30 43pF TO C32,
C31 15-60pF, U1 & U2 LT1227
TO LT1227CN8. ADDED
LABELING TO VR1, VR2,
CR2-CR5, AND U4.
CHG0 C28 17-25 TO
7-25pF. (PJK)

7746 3/13/85 JAG
REV ECO DATE APV

		THIS PRINT IS THE PROPERTY OF AXCERA. IT SHALL NOT BE COPIED WITHOUT PERMISSION.		TITLE SCHEMATIC -- DIFFERENTIAL GAIN CORRECTOR BOARD	
		MATERIAL FINISH		DWN MM 12/20/85 CHK JAG 2/21/85 REL JAG 2/21/85	
DWG NO. 1138-3107		REV 1		SCALE - 1 OF 1	



09 WAS 2N4406, DELETED C44
BETWEEN U5D 12 AND 13. 54M
MOVED R36 AND CR8 FROM
VERTICAL POSITION TO HORIZ.
POSITION. (RFB)

2 1284 3/11/91 RE
ADDED MANUAL GAIN TO R3.
M.C./AGC TO S1 AND J13
WAS J14. (RFB)

3 1285 2/17/91 RE
ADDED R99 @ J10-B
R49

4 1300 10/15/91 JCM
C10 WAS TYP. R44 & R53
WERE 200 OHM.
01-07 WAS BS170, R67
WAS 1K, I/P FAULT J14
WAS J13, U4C WAS U4A.
ADDED C43, C44 & R98.
R21 WAS 100K, ADDED
U5A, U4A+D, U2A+B
R80, R90, C27, J12-3-4,
J10-3. (RFB)

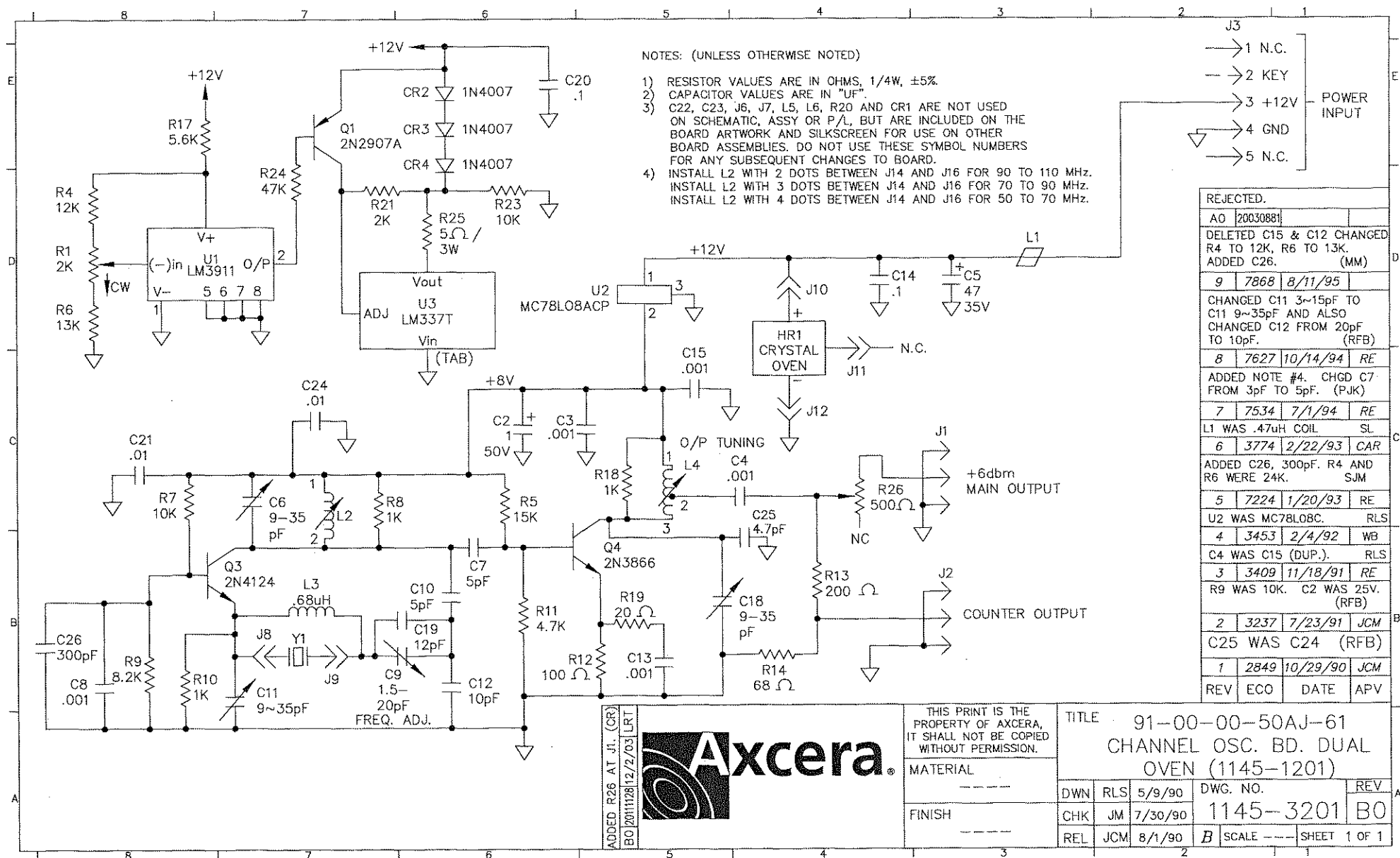
3 1320 6/29/91 JCM
ADDED C40-C42, R23
WAS 1M, DELETED CR7,
CR8, CR10, AND H37.
RELOCATED R38. (RFB)

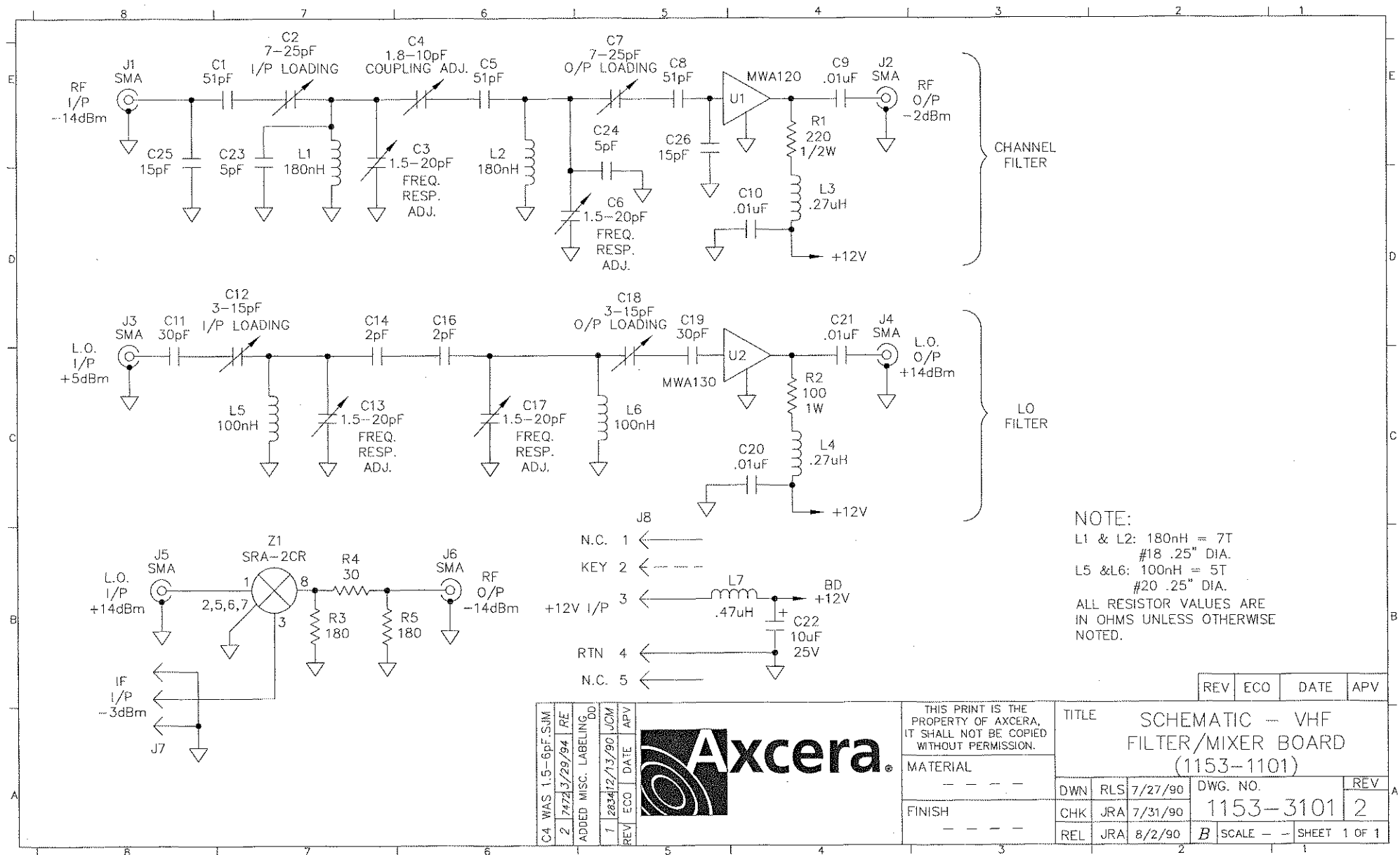
2 1426 7/29/91 JCM
EXTENSIVE CHANGES. (RFB)

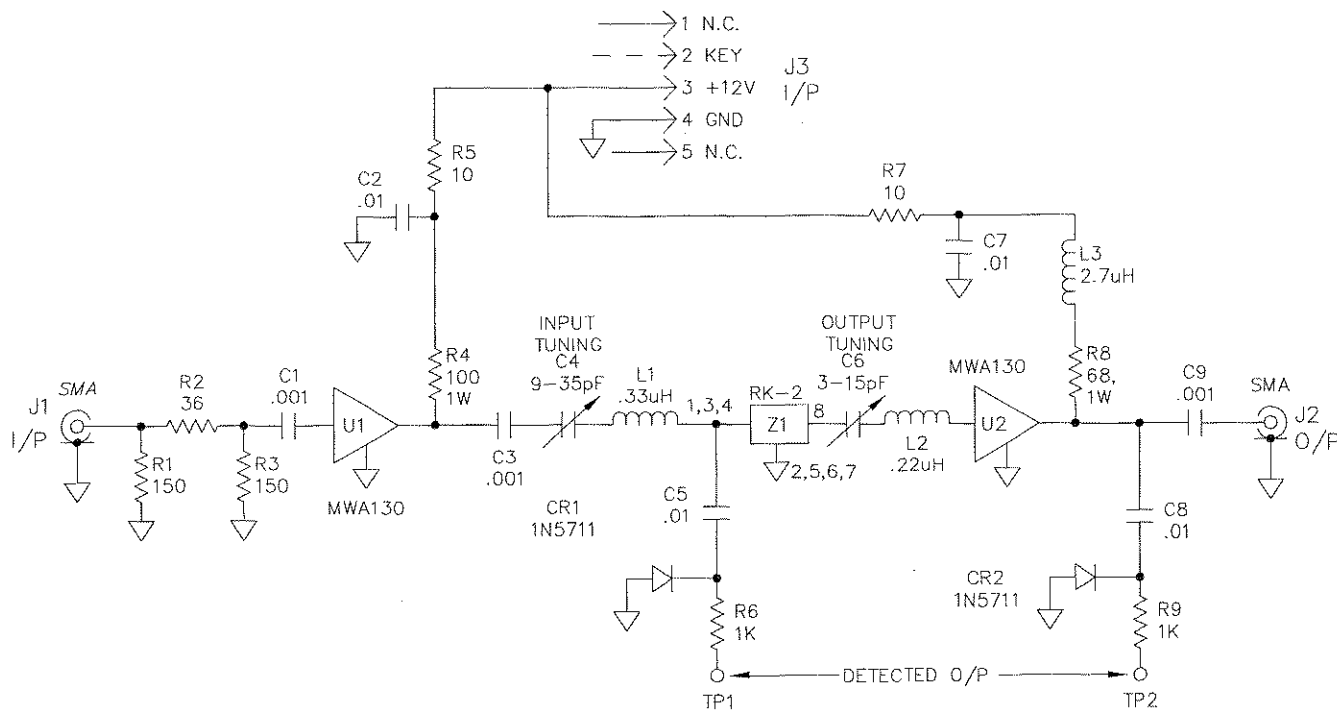
1 3112 7/3/91 JCM
REV EDD DATE APV



TITLE			
SCHEMATIC - AGC CONTROL BOARD (1142-1601)			
MATERIAL	DWG. NO.	REV	
FINISH	CHK	DWG	1142-3601 7
	REL	DWG	1142-3601 7







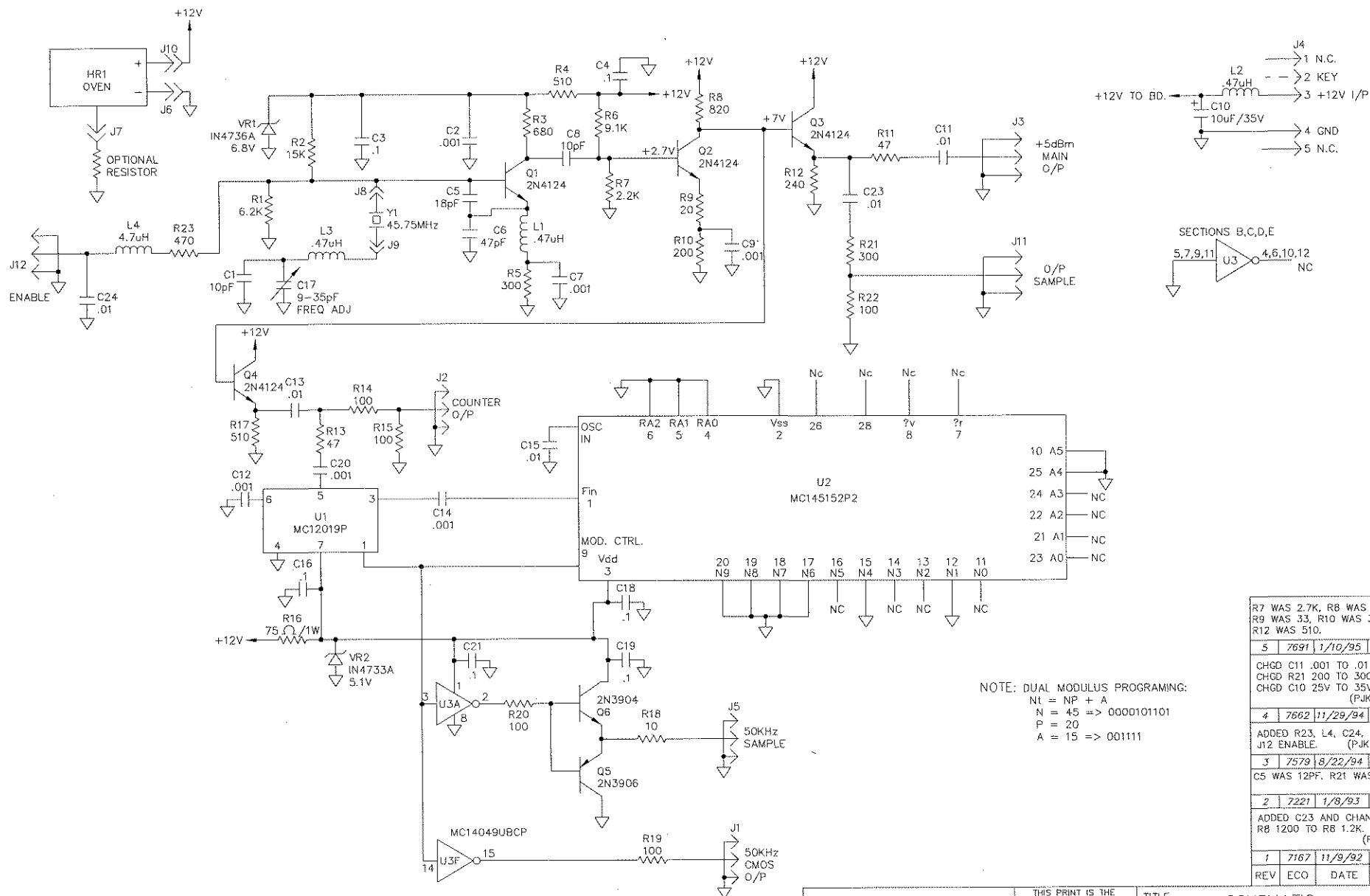
THIS PRINT IS THE
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MATERIAL

FINISH

TITLE
 SCHEMATIC -
 X2 MULTIPLIER BD, L.B. VHF
 (1172-1111)

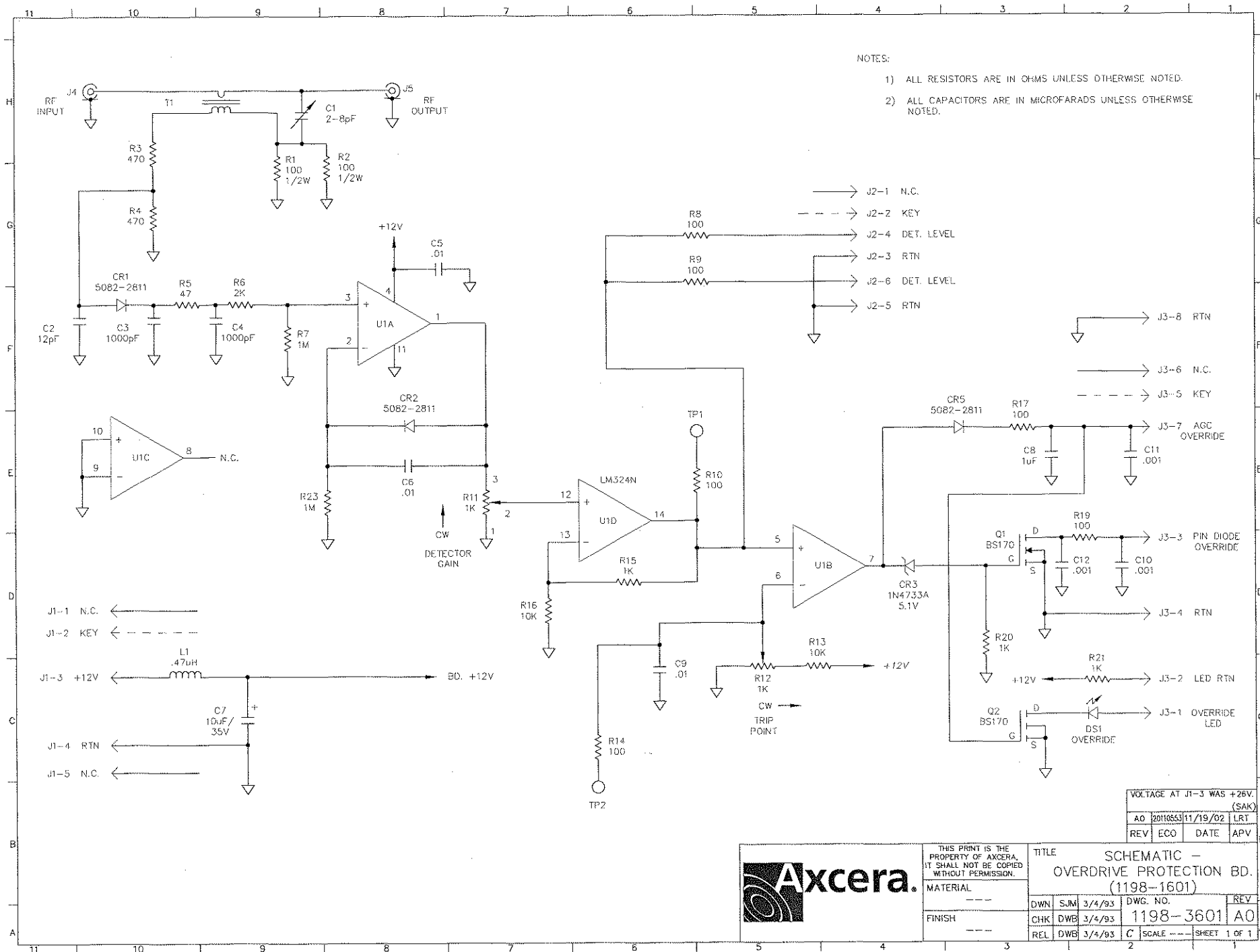
REV	ECO	DATE	APV
DWN	SJM	9/17/93	DWG. NO.
CHK	DWB	9/17/93	1172-3111
REL	DWB	9/17/93	B SCALE - - SHEET 1 OF 1



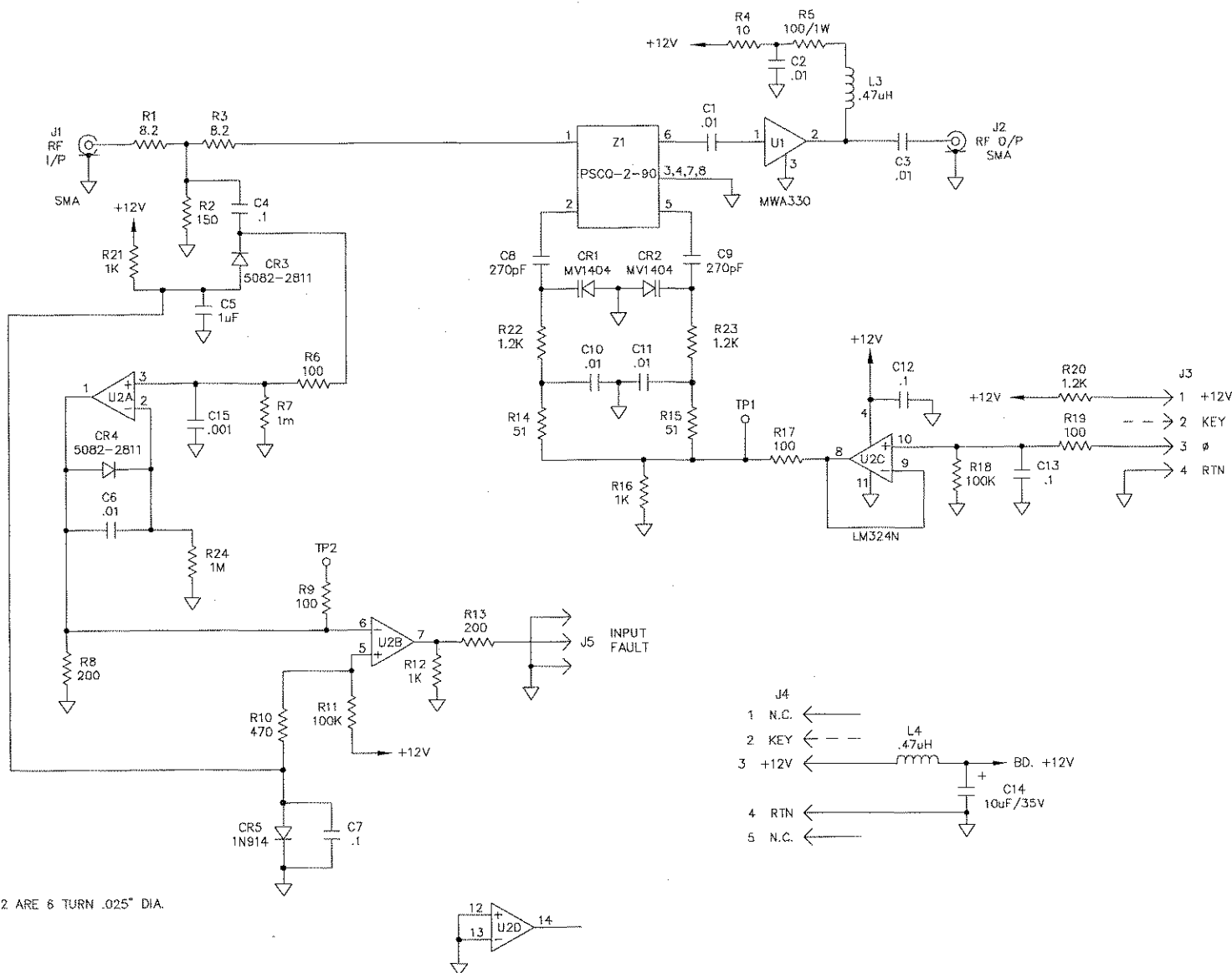
5	7691	1/10/95	RE
CHGD C11 .001 TO .01 CHGD R21 200 TO 300. CHGD C10 25V TO 35V. (PJK)			
4	7662	11/29/94	RE
ADDED R23, L4, C24, AND J12 ENABLE (PJK)			
3	7579	8/22/94	DWB
C5 WAS 12PF, R21 WAS 100 SJM			
2	7221	1/9/93	RE
ADDED C23 AND CHANGED R8 1200 TO R8 1.2K. (RFB)			
1	7167	11/9/92	RE
REV	ECO	DATE	APV



THIS PRINT IS THE PROPERTY OF AXCERA, IT SHALL NOT BE COPIED WITHOUT PERMISSION.				TITLE SCHEMATIC - IF CARRIER OSC. BD. 45.75MHz (1191-1404)			
MATERIAL - - - -				DWN	RFB	9/28/92	DWG. NO.
FINISH - - - -				CHK	RE	9/29/92	1191-3404
				REL	RE	9/29/92	C SCALE - - SHEET 1 OF 1



L1 AND L2 ARE 6 TURN .025" DIA.

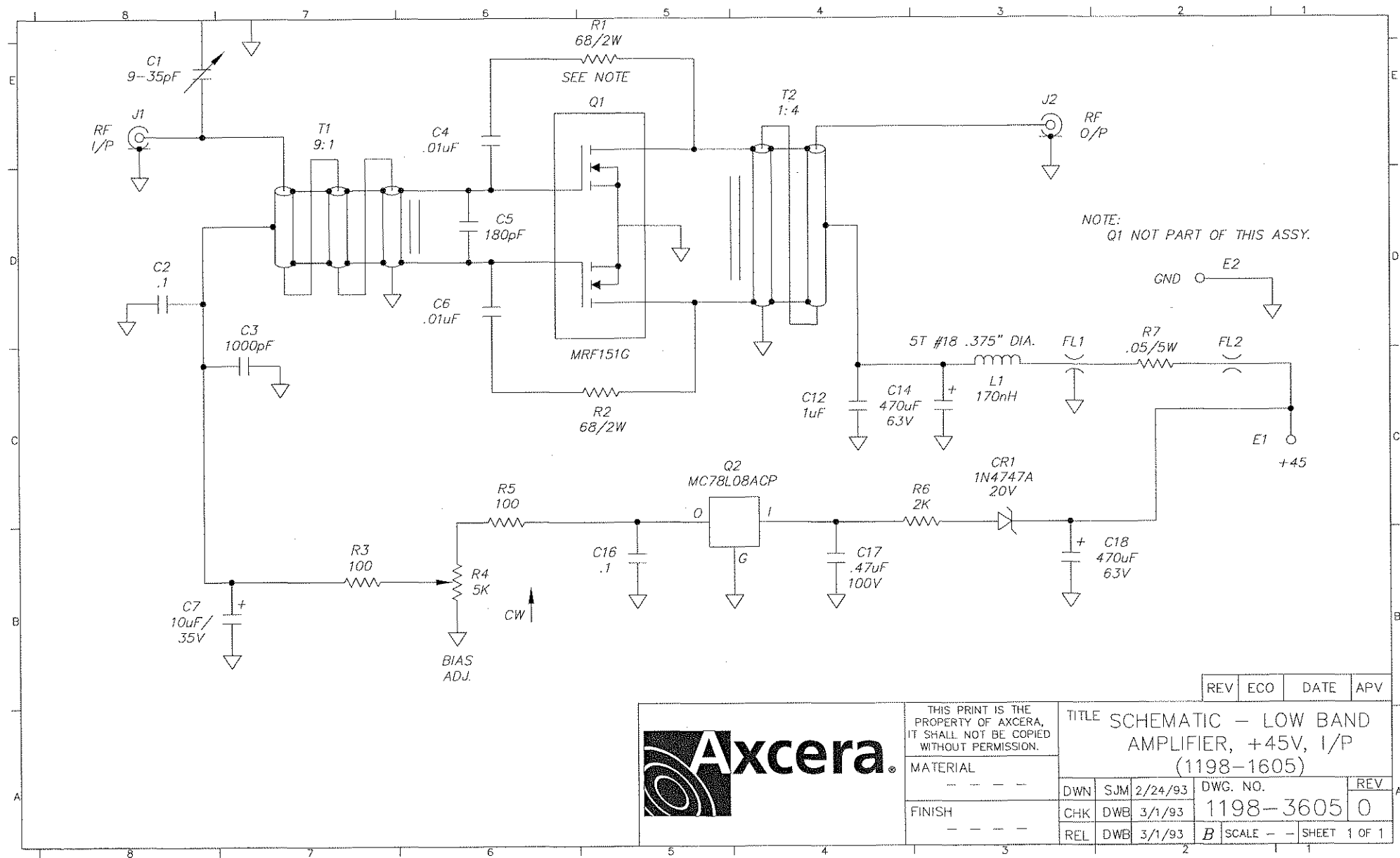


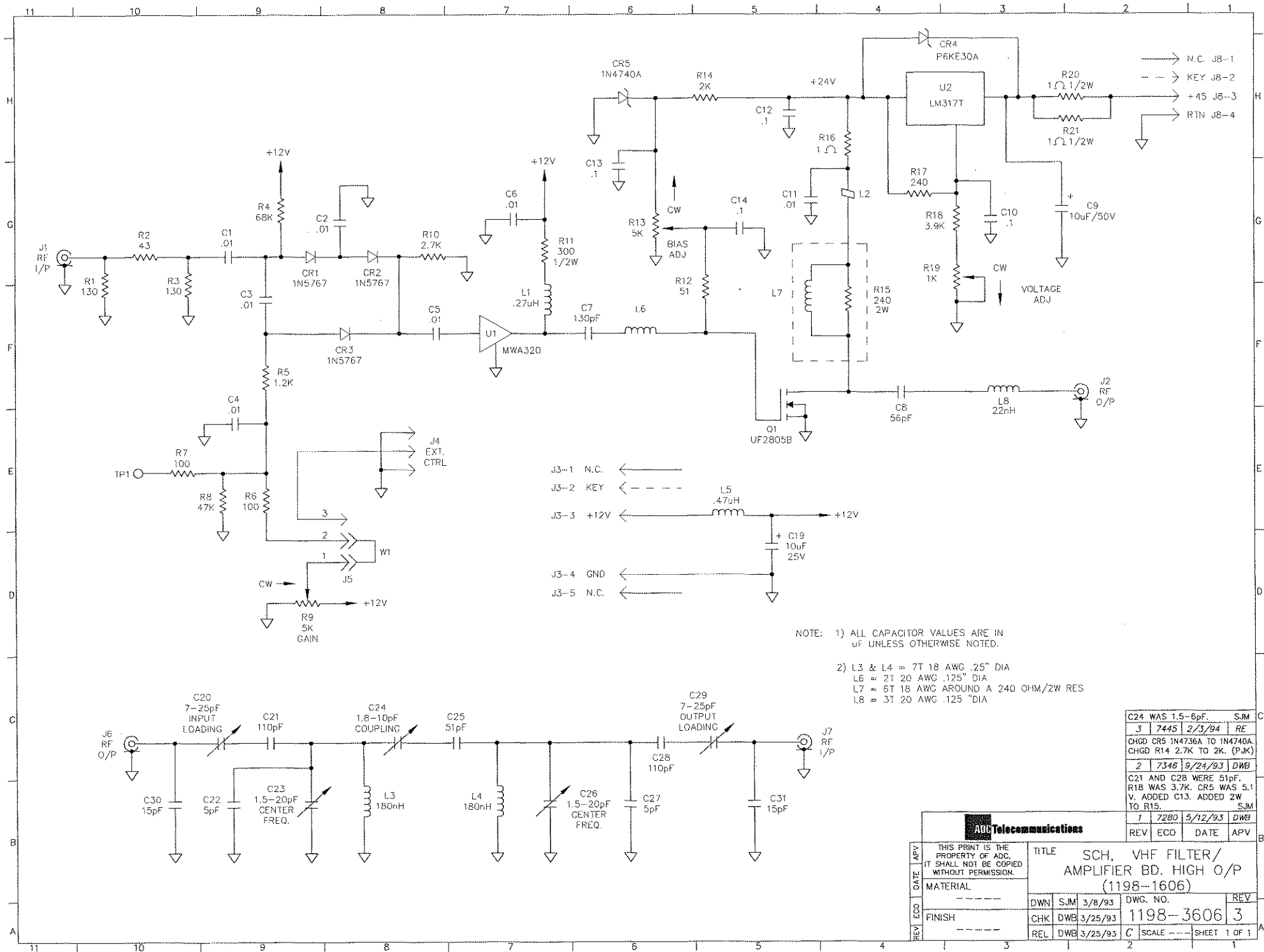
- J4
- 1 N.C. ←
 - 2 KEY ←
 - 3 +12V ←
 - 4 RTN ←
 - 5 N.C. ←
- L4 .47uH
- BD. +12V
- C14 10uF/35V

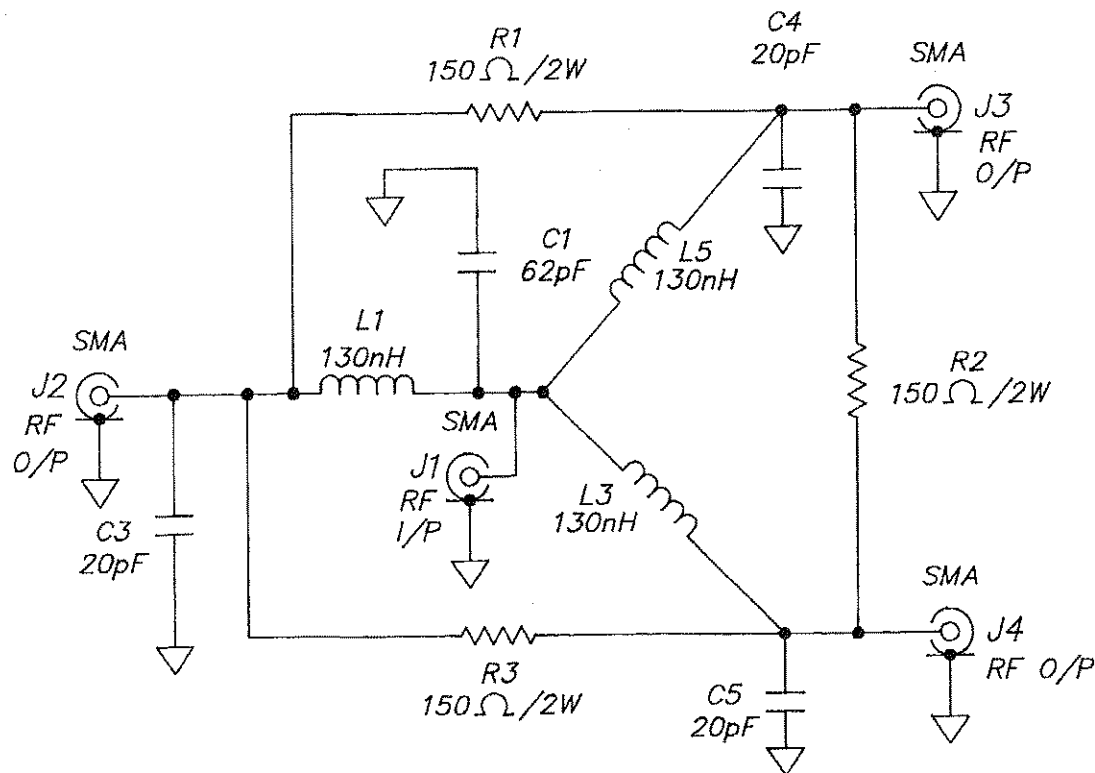
CR3 WAS CR4. U2 WAS LM-324.

1	7327	8/8/93	
REV	ECN	DATE	APV

THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.				TITLE			
MATERIAL				SCHEMATIC -			
FINISH				PHASE SHIFTER BOARD			
REL				LOW BAND (1198-1602)			
DWG. NO.				REV			
1198-3602				1			
SCALE				SHEET 1 OF 1			

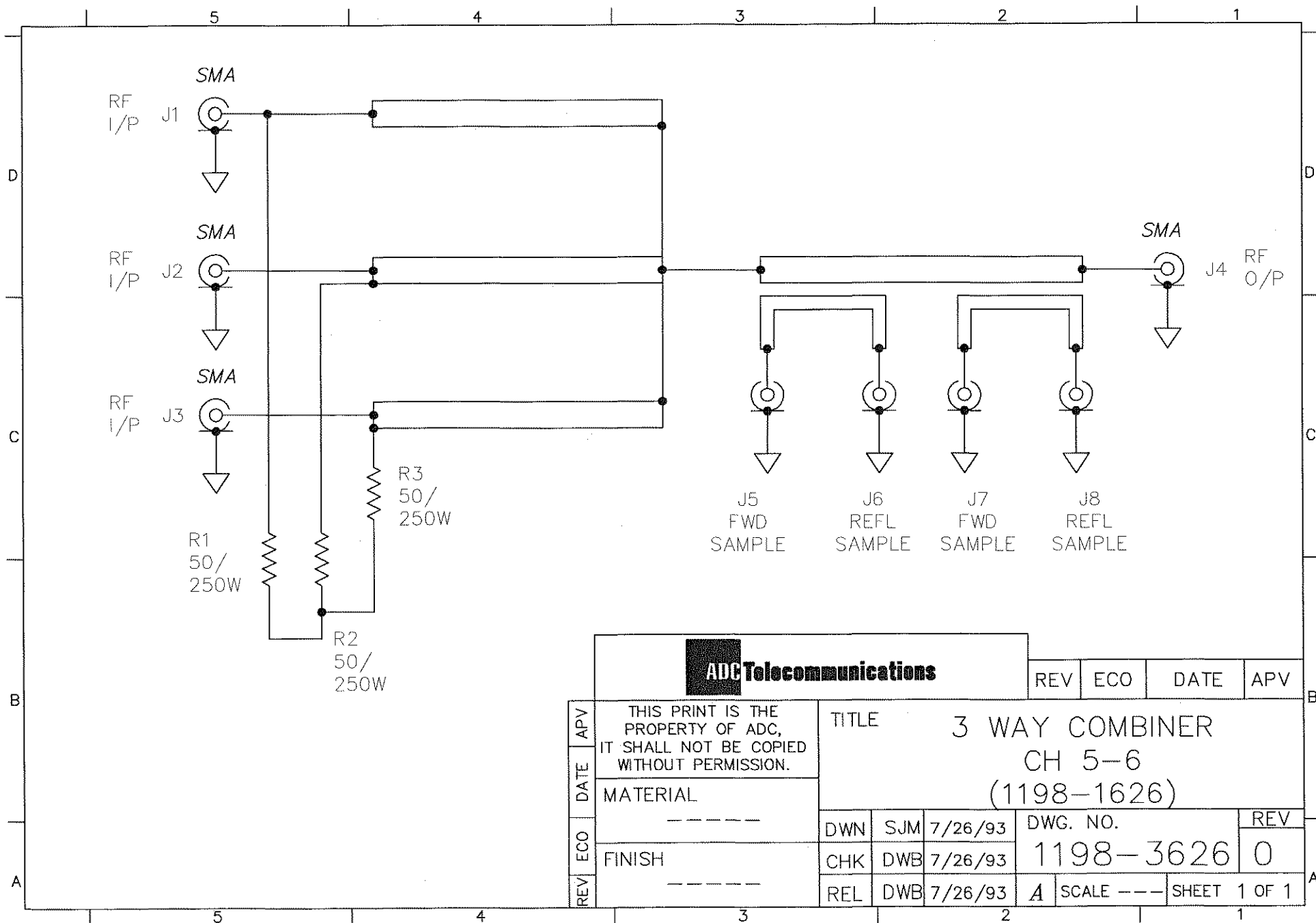


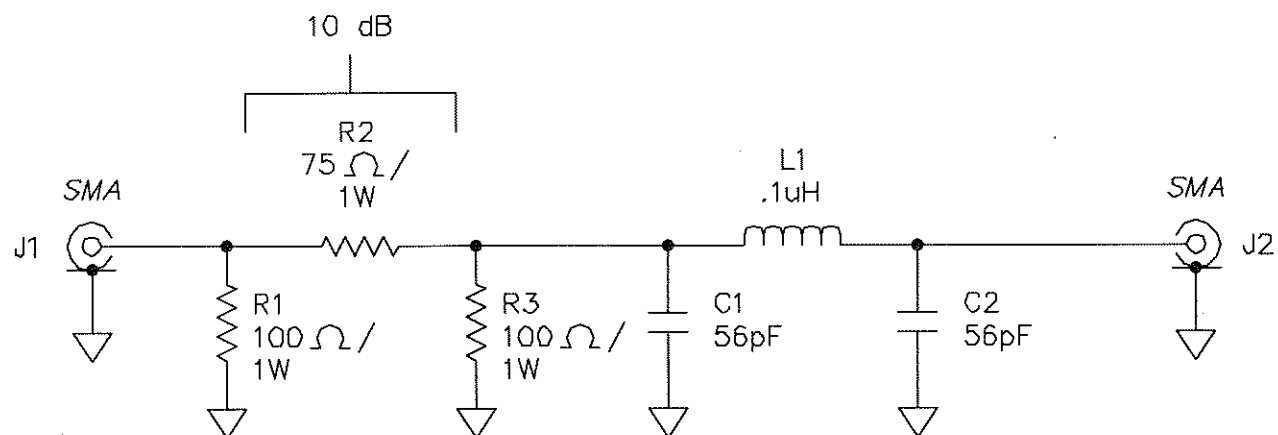




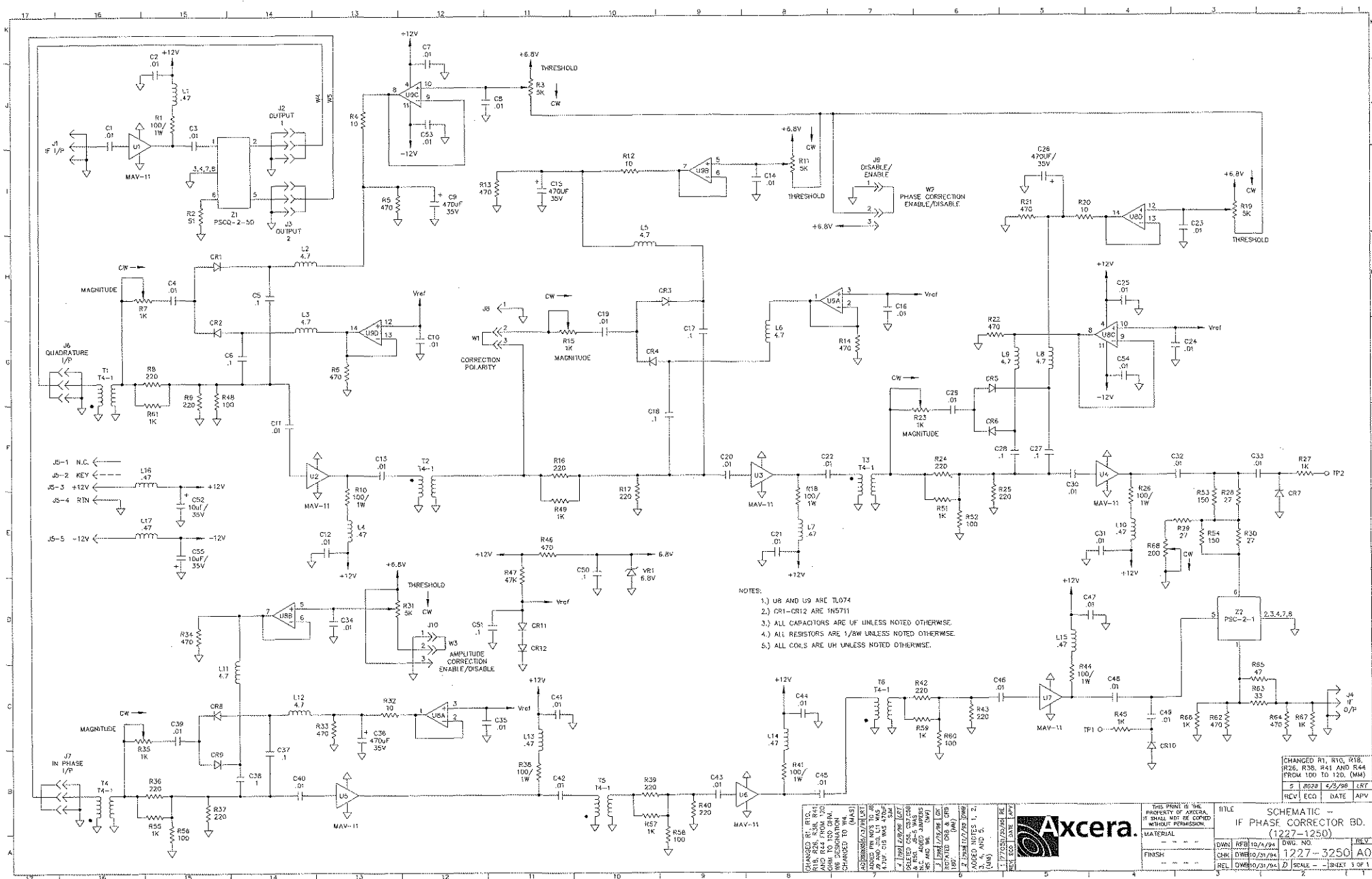
NOTE: L1-L3 4T #18 AWG .375" DIA

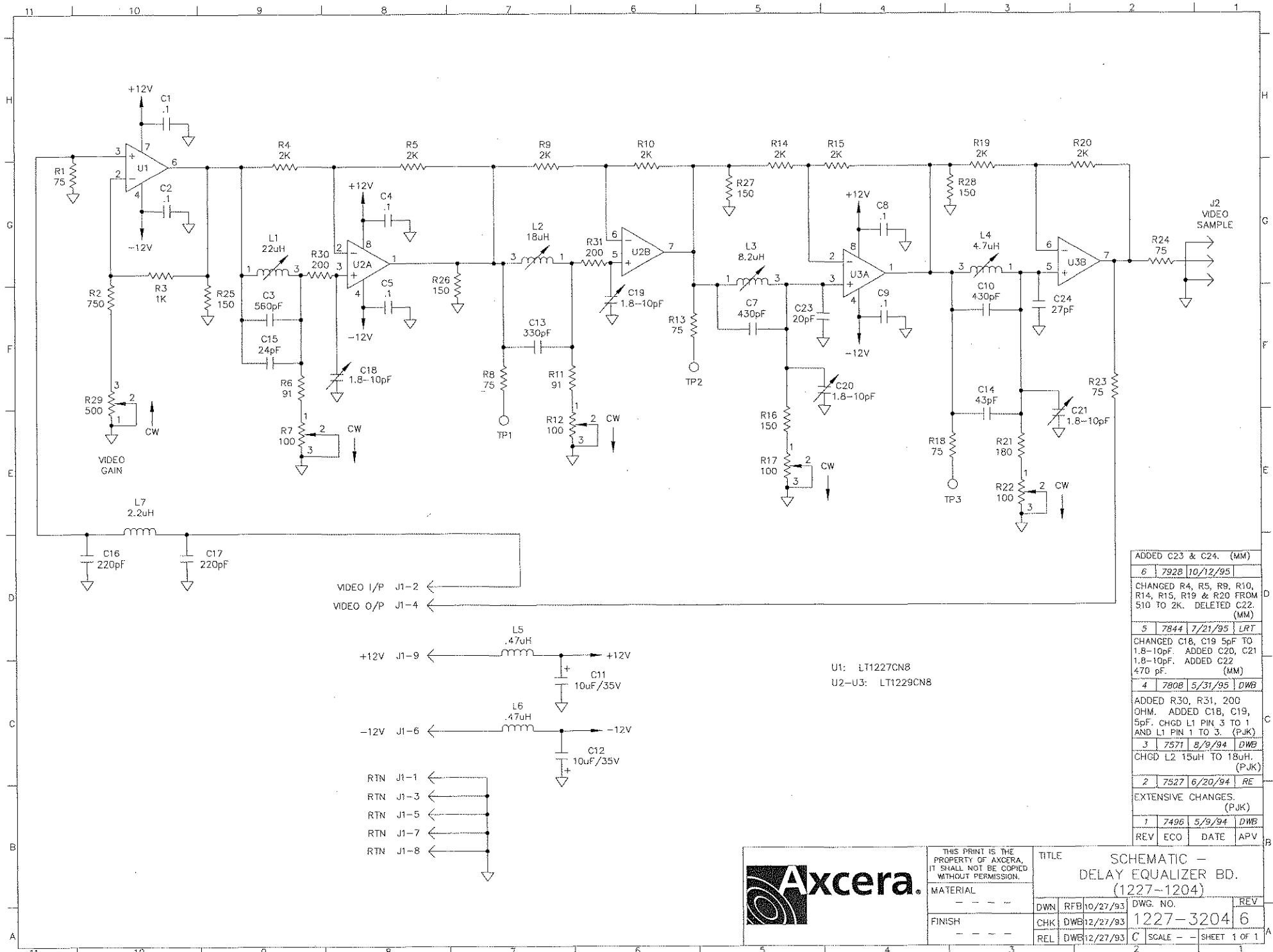
ITS CORPORATION				REV	ECN	DATE	APV
REV	ECN	DATE	APV	TITLE			
				SCHEMATIC			
				3 WAY SPLITTER BD			
				CH 5-6 (1198-1608)			
DWN	SJM	3/12/93	DWG. NO.		REV		
CHK	DWB	3-24-93	1198-3608		0		
REL	DWB	3-24-93	A	SCALE	SHEET 1 OF 1		





ITS CORPORATION				REV	ECN	DATE	APV
REV	ECN	DATE	APV	THIS PRINT IS THE PROPERTY OF ITS CORP. IT SHALL NOT BE COPIED WITHOUT PERMISSION.			
				MATERIAL			
				FINISH			
DWN	SJM	7/26/93	DWG. NO.		REV		
CHK	DWB	7/26/93	1198-3628		0		
REL	DWB	7/26/93	A	SCALE---	SHEET 1 OF 1		





ADDED C23 & C24. (MM)			
6	7928	10/12/95	
CHANGED R4, R5, R9, R10, R14, R15, R19 & R20 FROM 510 TO 2K. DELETED C22. (MM)			
5	7844	7/21/95	LRT
CHANGED C18, C19 5pF TO 1.8-10pF. ADDED C20, C21 1.8-10pF. ADDED C22 470 pF. (MM)			
4	7808	5/31/95	DWB
ADDED R30, R31, 200 OHM. ADDED C18, C19, 5pF. CHGD L1 PIN 3 TO 1 AND L1 PIN 1 TO 3. (PJK)			
3	7571	8/9/94	DWB
CHGD L2 15uH TO 18uH. (PJK)			
2	7527	6/20/94	RE
EXTENSIVE CHANGES. (PJK)			
1	7496	5/9/94	DWB
REV	ECO	DATE	APV

U1: LT1227CN8
U2-U3: LT1229CN8

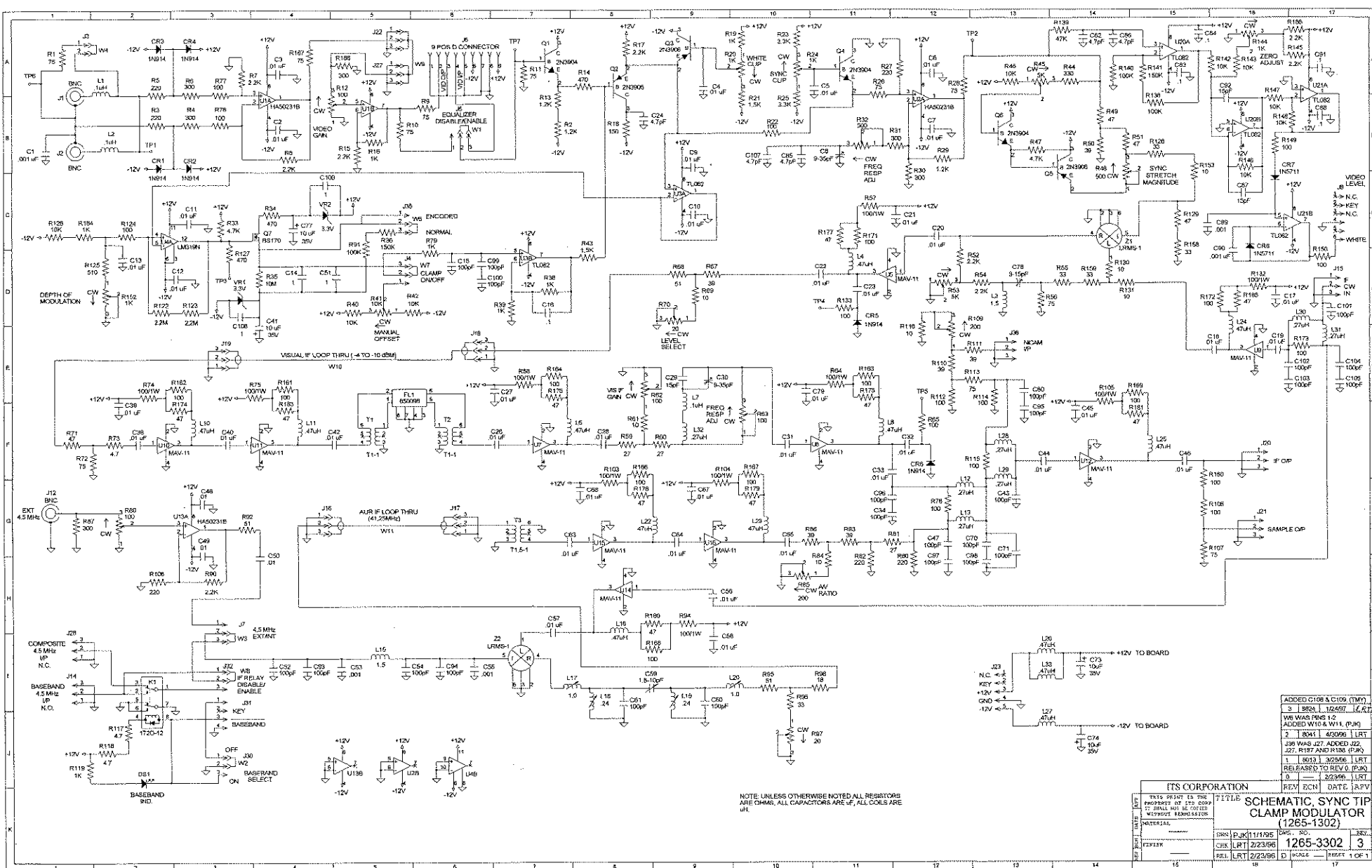


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TITLE

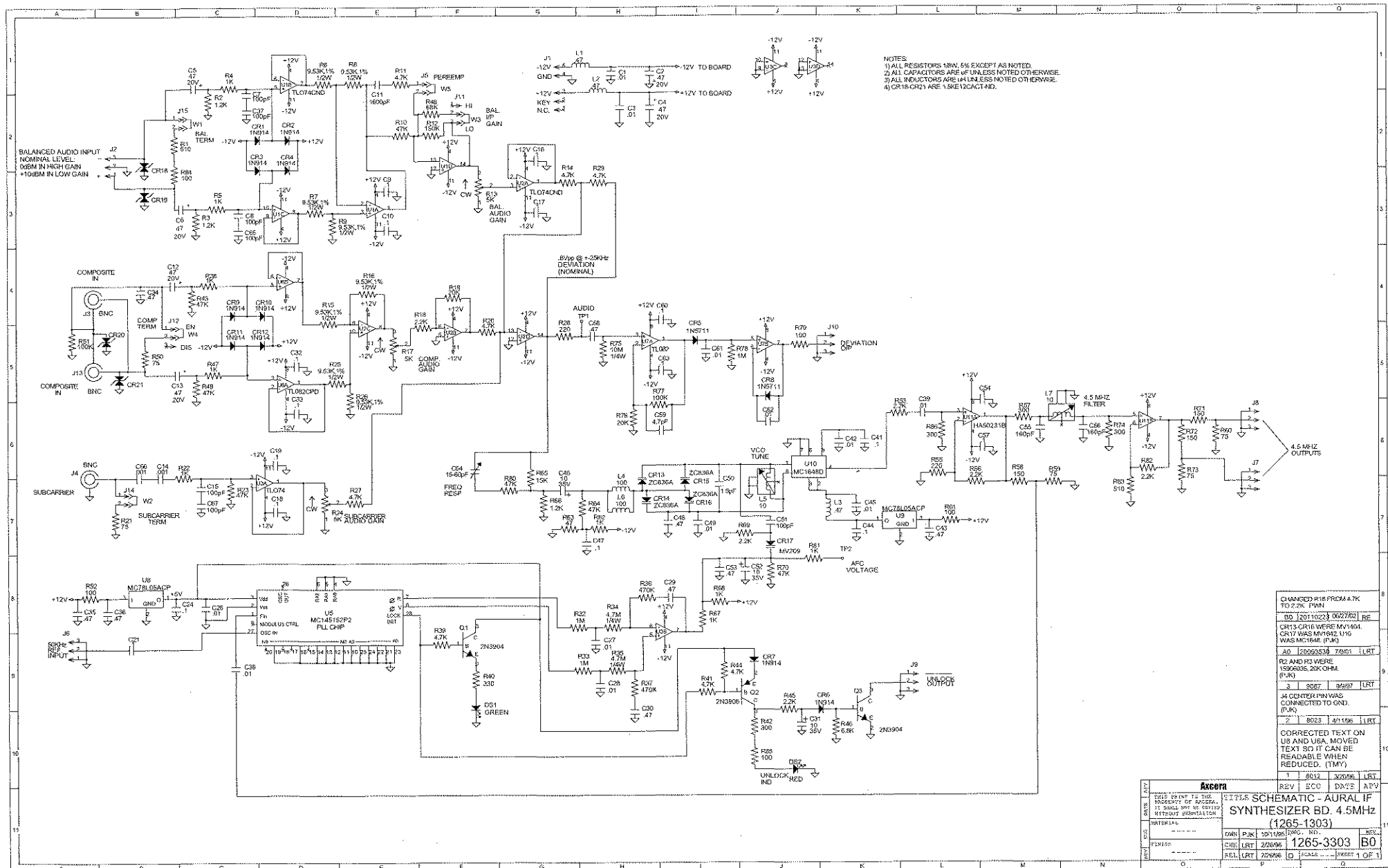
**SCHEMATIC -
DELAY EQUALIZER BD.
(1227-1204)**

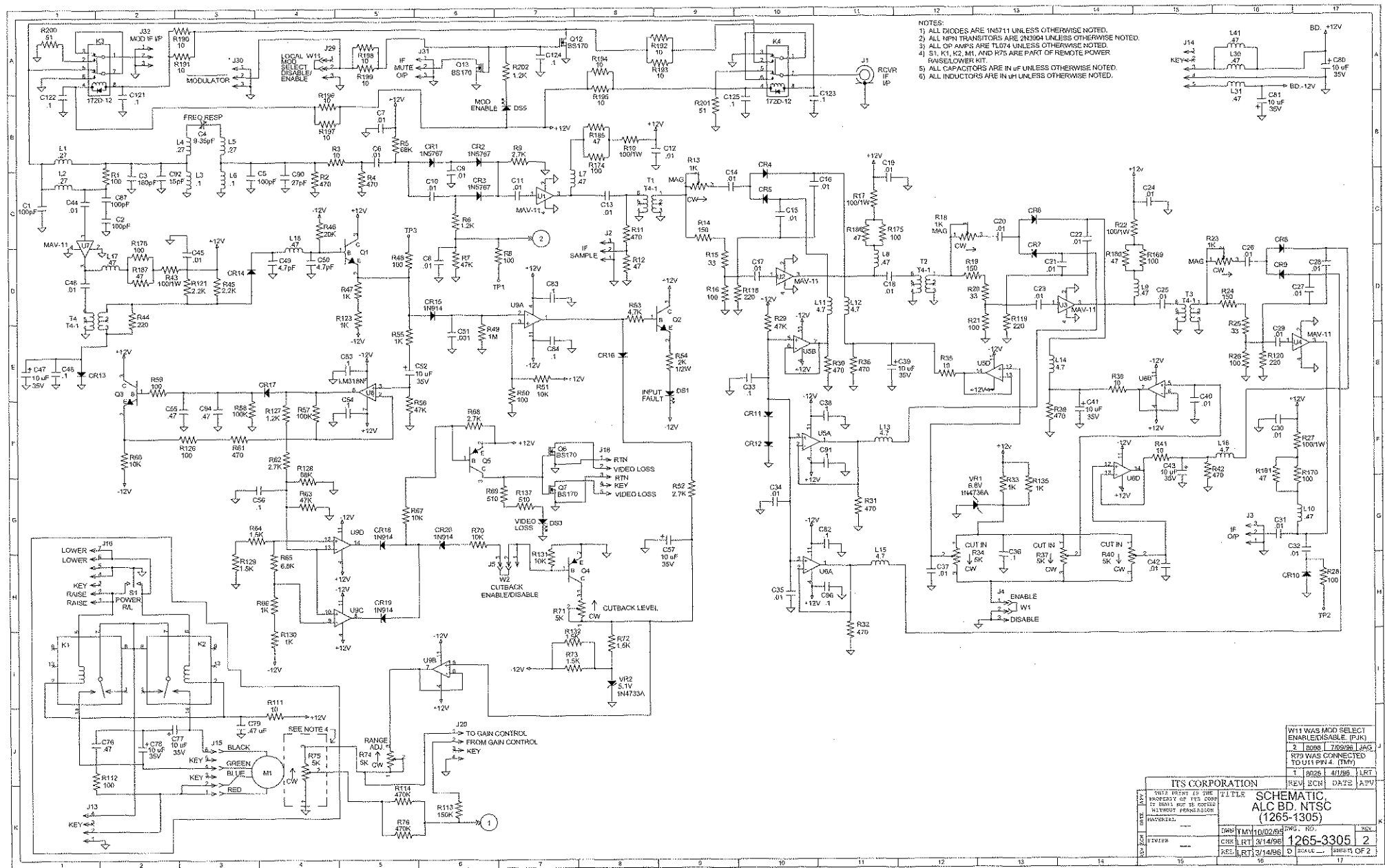
MATERIAL	DWN	RFB	10/27/93	DWG. NO.	REV
FINISH	CHK	DWB	12/27/93	1227-3204	6
	REL	DWB	12/27/93	C	SCALE - - SHEET 1 OF 1



NOTE: UNLESS OTHERWISE NOTED ALL RESISTORS ARE OHMS, ALL CAPACITORS ARE μF , ALL COILS ARE μH .

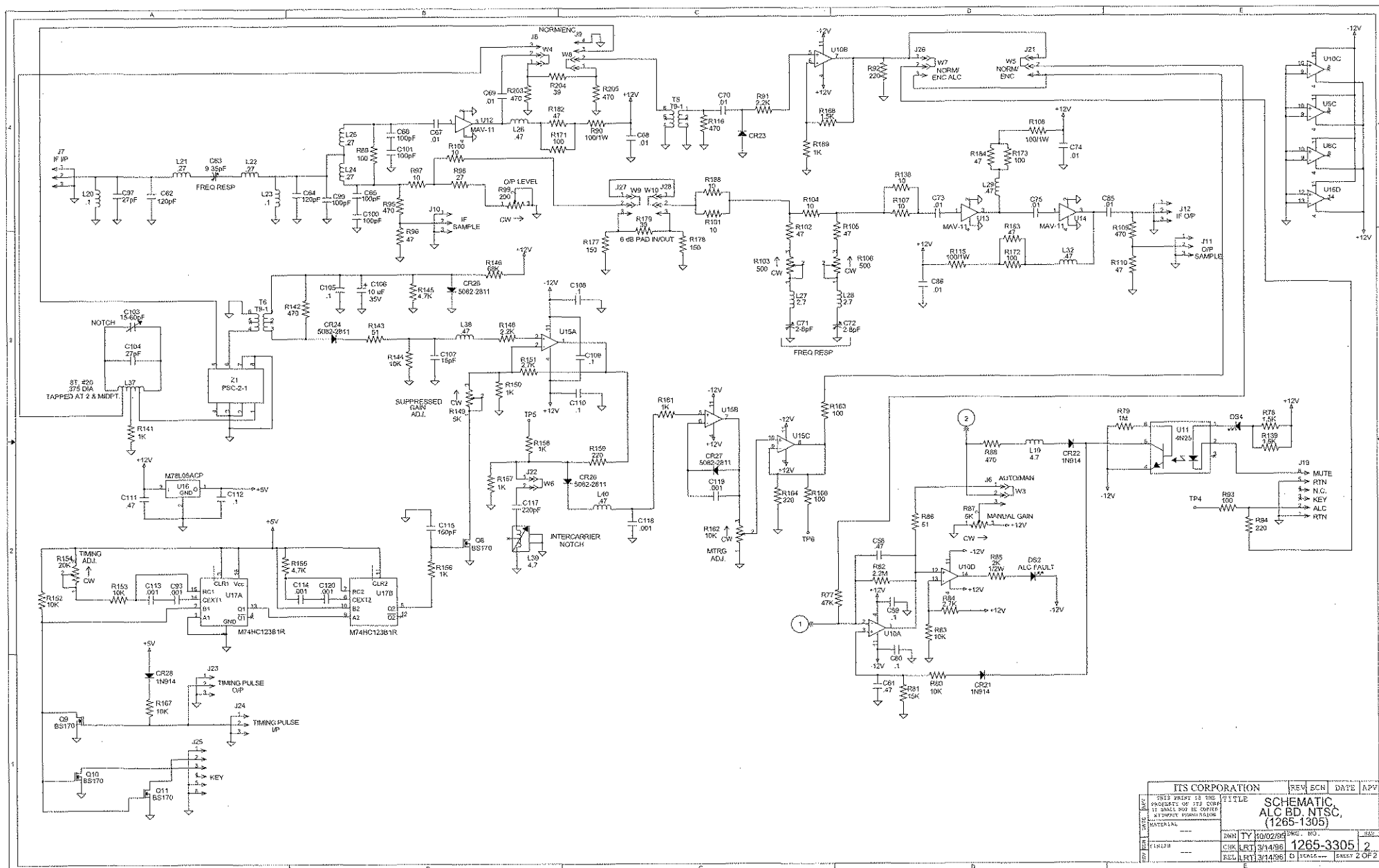
ETS CORPORATION THIS PRINT IS THE PROPERTY OF ETS CO. IT SHALL NOT BE COPIED WITHOUT ETS'S PERMISSION MATERIALS		TITLE SCHEMATIC, SYNC TIP CLAMP MODULATOR (1265-1302)		REV EGN DATE JANV
ORG PJK CHK LRT REL LRT	11/15/95 2/23/96 2/23/96	DATE NOV. 1265-3302 D BORGLE	3 1 OF 1	15 16 17

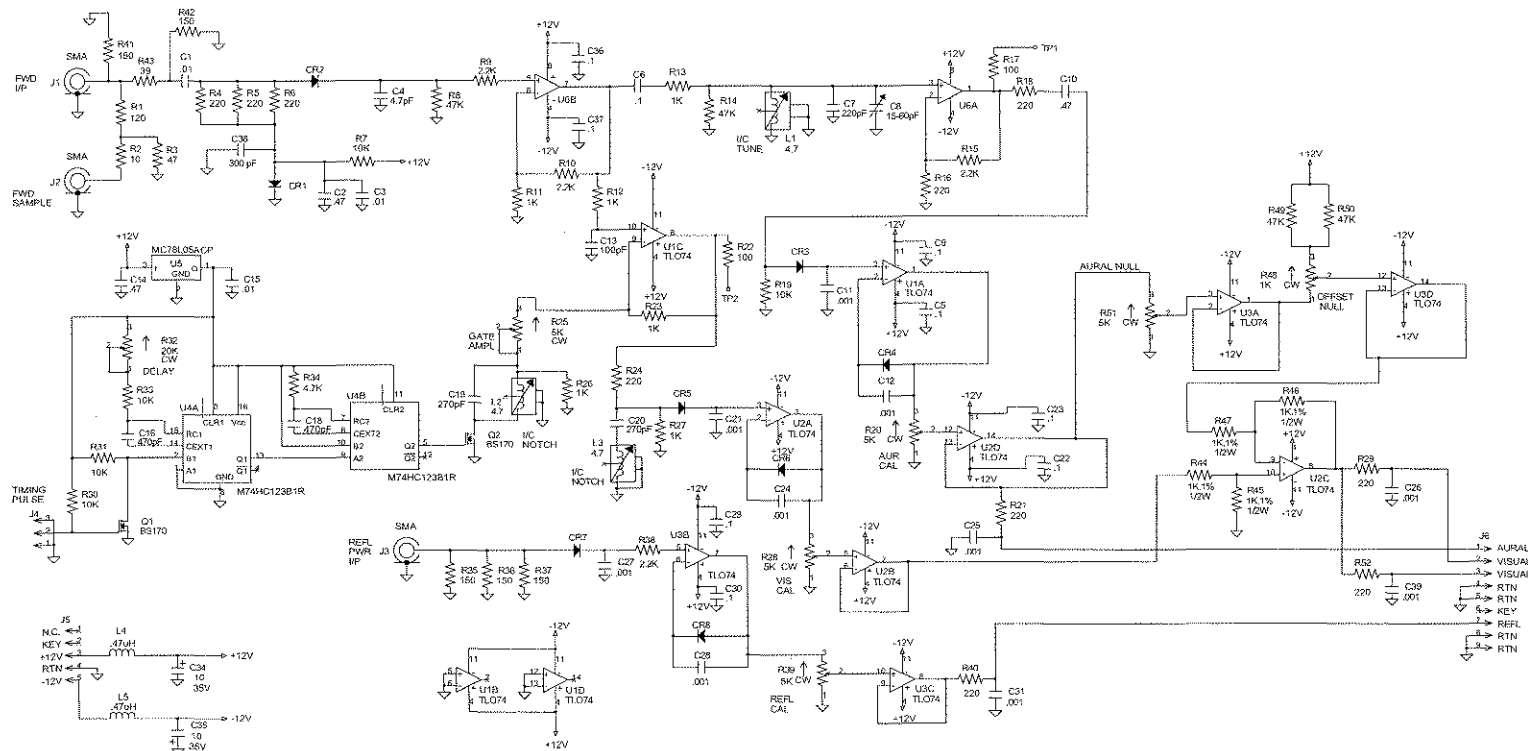




		W11 WAS MOD SELECT ENABLE/DISABLE (PJK)		2 1006 1 769/96 JAG	
		R75 WAS CONNECTED TO U11 PIN 4. (TMY)		1 0026 1 40/96 LRT	
ITS CORPORATION					
REV. ECH		DATE		REV. ECH	
TITLE		SCHEMATIC			
PROJECT NO.		ALC BD. NTSC			
DRAWN BY		1265-3305			
CHECKED BY		2			
REL. DATE		3/14/96			
SCALE		SHEET 1 OF 2			

15
16
17



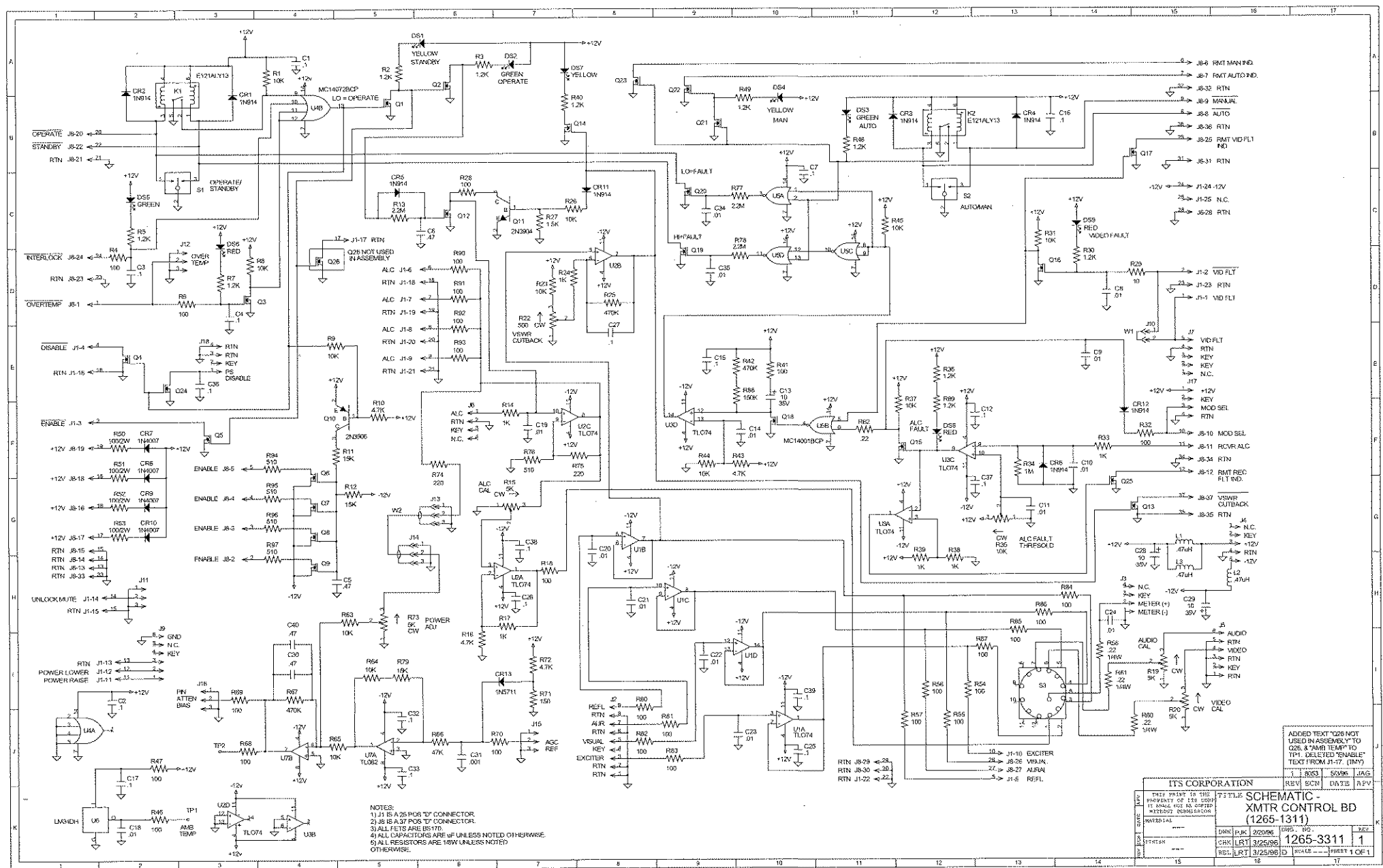


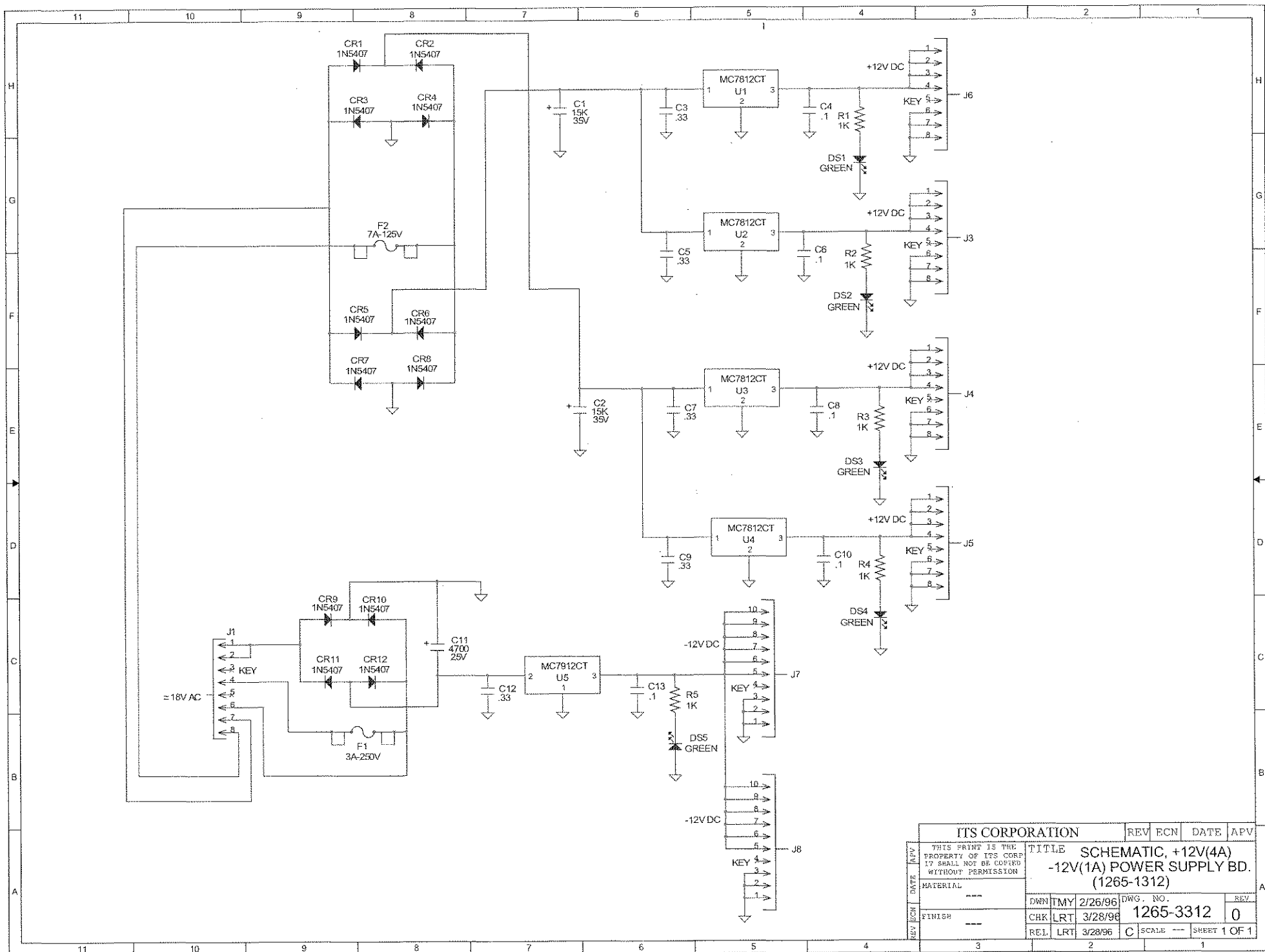
- NOTES:
 1) ALL RESISTORS ARE 1/8 WATT UNLESS NOTED OTHERWISE.
 2) ALL CAPACITORS ARE .01 UNLESS NOTED OTHERWISE.
 3) ALL INDUCTORS ARE .1H UNLESS NOTED OTHERWISE.
 4) ALL DIODES ARE 500V 2811 UNLESS NOTED OTHERWISE.
 5) ALL TRANSISTORS ARE 65170 UNLESS NOTED OTHERWISE.
 6) U6 IS 74V0231D OR LT1229CEA.

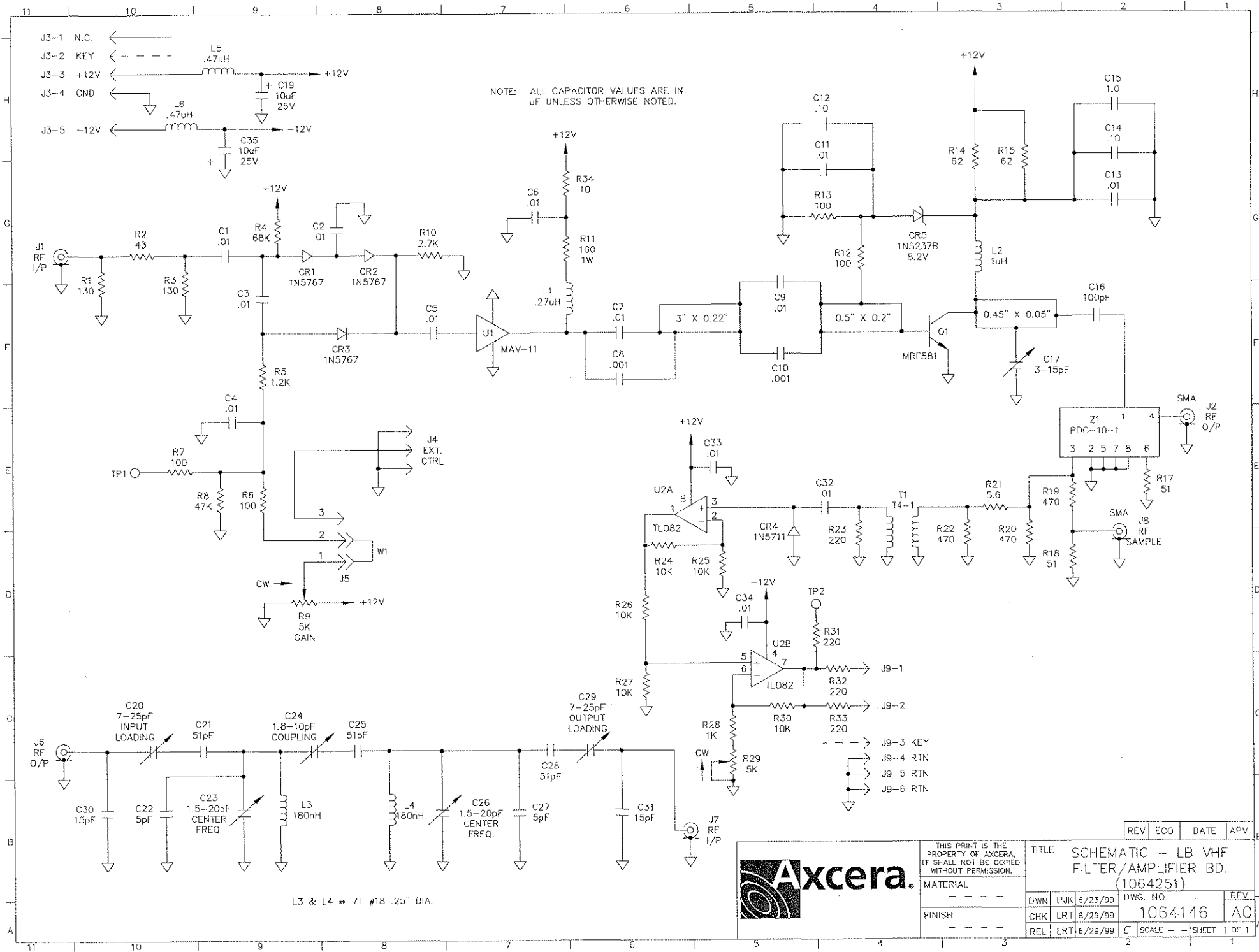
ADDED R52 AND C38 (P.W.)	
AD. 10/10/85	5/3/81
R52 WAS 47K (T.M.)	
5	5/3/81
R7 WAS 100K (P.W.)	
4	5/3/81
ADDED R41 - R51 & C38. R15 WAS 10K, R19 WAS 2.2K, R7 WAS 10K, C38 U1A TO U1B. C38 U1D TO U1A. ADDED CIRCUIT TO U1B & U1D. EN. ADDED TO 1" SIZE DWG. (T.M.)	
3	8/6/82
C7 WAS 180pF. J5 WAS A 7 PIN CONNECTOR (P.W.)	
2	8/6/82
ADDED U1K, C36, C37. ADDED NOTE: R6. U5 WAS MTR. LS6A. LS WAS CONNECTED TO J5-1. U5A WAS U1A. U5B WAS U1B. U5C WAS U1C. U5D WAS U1D. U5E WAS U1E. U5F WAS U1F. U5G WAS U1G. U5H WAS U1H. U5I WAS U1I. U5J WAS U1J. U5K WAS U1K. U5L WAS U1L. U5M WAS U1M. U5N WAS U1N. U5O WAS U1O. U5P WAS U1P. U5Q WAS U1Q. U5R WAS U1R. U5S WAS U1S. U5T WAS U1T. U5U WAS U1U. U5V WAS U1V. U5W WAS U1W. U5X WAS U1X. U5Y WAS U1Y. U5Z WAS U1Z.	
1	7/9/82

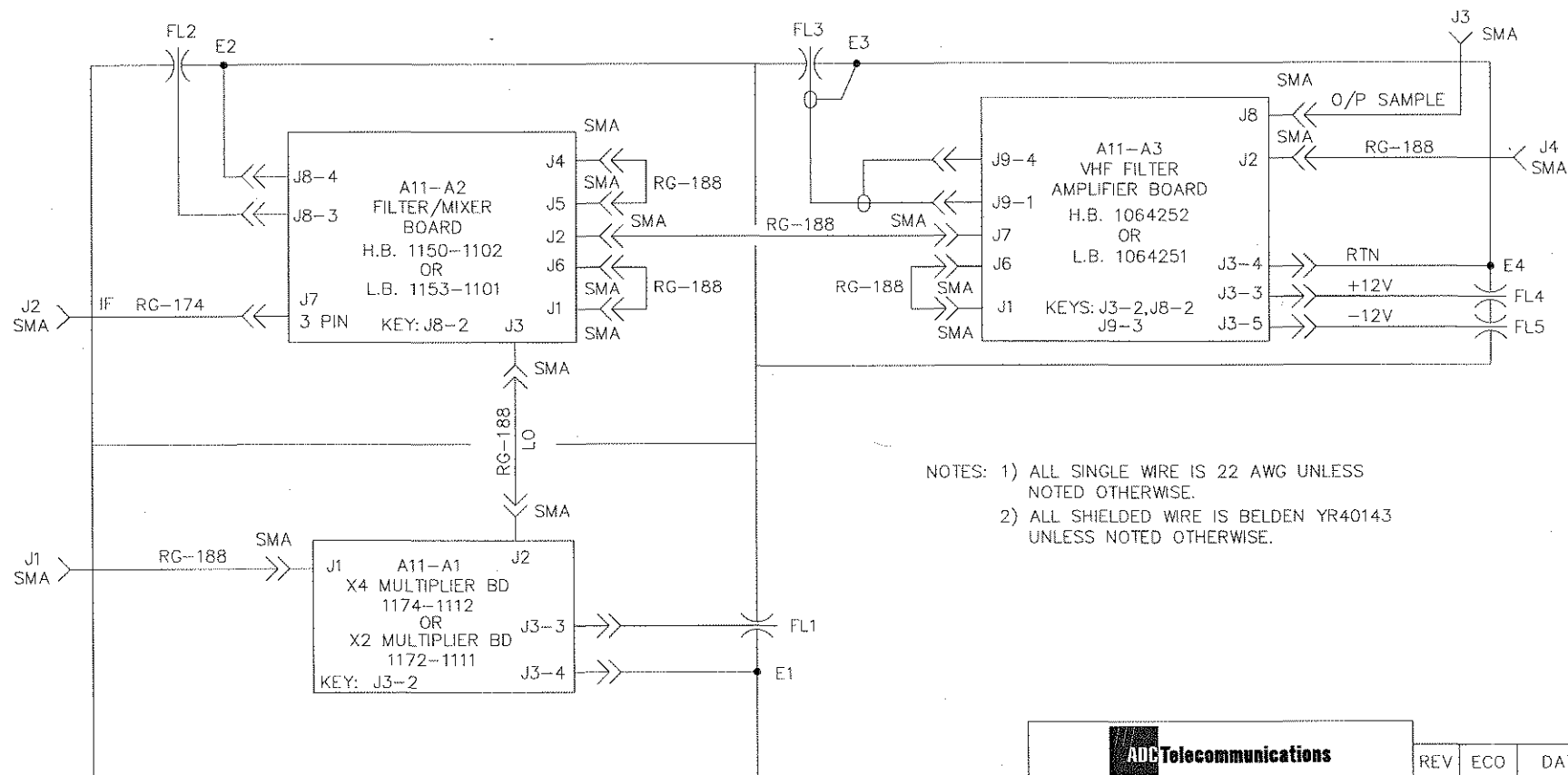
REV	ECO	DATE	APV
1	7999	34/90	1RT
2	8026	4/2/86	1RT
3	8062	7/15/86	1J5G
4	8208	9/3/86	1J5G
5	8470	9/30/86	1RT
6	8508	9/30/86	1J5G
7	8508	9/30/86	1J5G
8	8508	9/30/86	1J5G
9	8508	9/30/86	1J5G
10	8508	9/30/86	1J5G
11	8508	9/30/86	1J5G
12	8508	9/30/86	1J5G
13	8508	9/30/86	1J5G
14	8508	9/30/86	1J5G
15	8508	9/30/86	1J5G
16	8508	9/30/86	1J5G
17	8508	9/30/86	1J5G

ABC Telecommunications
 TITLE 91-00-00-50AC-61
 SCH. VIS/AURAL MTRNG BD
 (1265-1309)
 CHG P.W. 12/7/86 DWG. NO.
 CHG DWG. 12/86 1265-3309 [A0]
 CHG DWG. 10/86 D SCALE - SHEET 1 OF 1









- NOTES: 1) ALL SINGLE WIRE IS 22 AWG UNLESS NOTED OTHERWISE.
 2) ALL SHIELDED WIRE IS BELDEN YR40143 UNLESS NOTED OTHERWISE.

ADC Telecommunications

REV	ECO	DATE	APV
1		1/6/00	
2		1/6/00	
3		1/6/00	
4		1/6/00	
5		1/6/00	
6		1/6/00	
7		1/6/00	
8		1/6/00	
9		1/6/00	
10		1/6/00	

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MATERIAL

FINISH

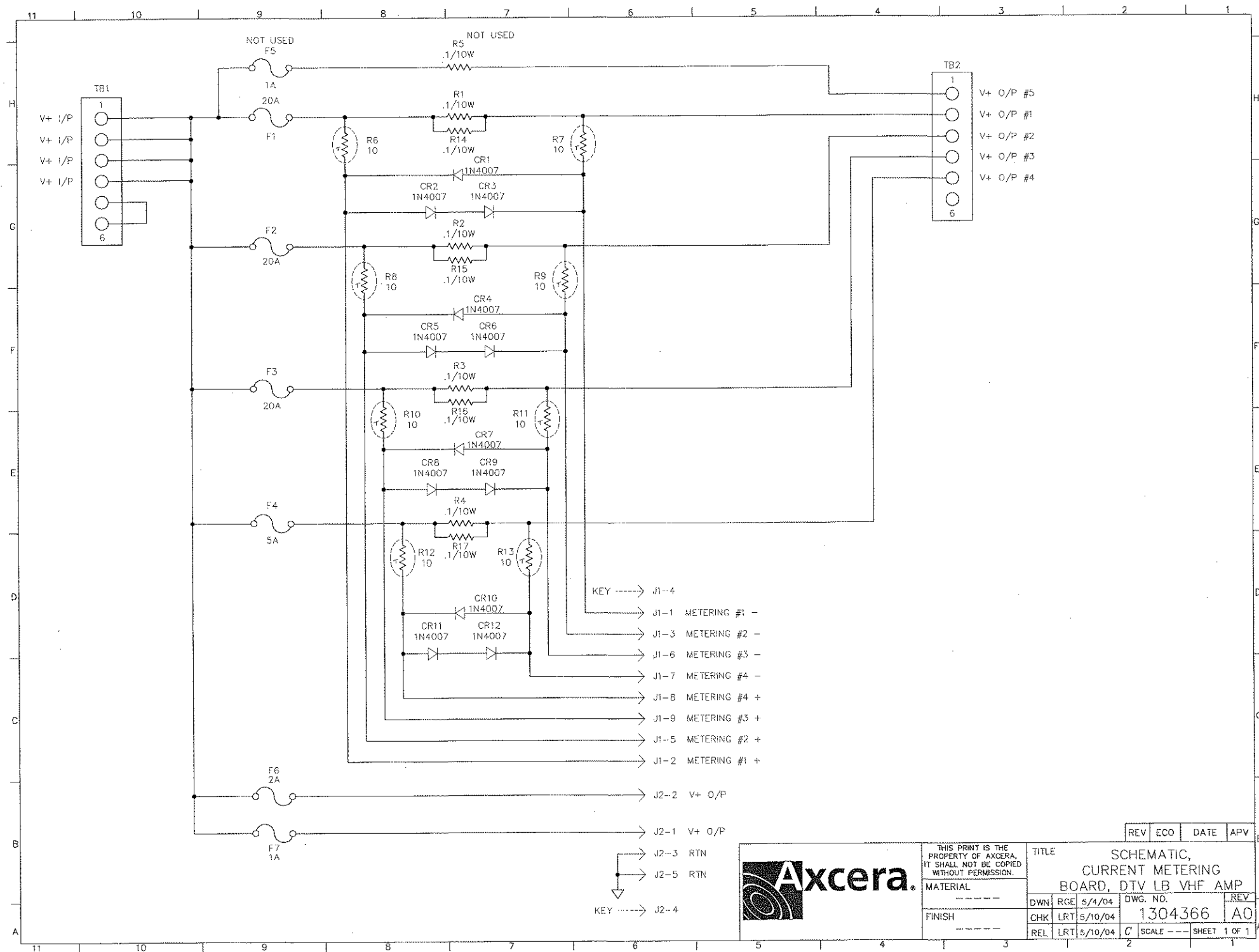
REL

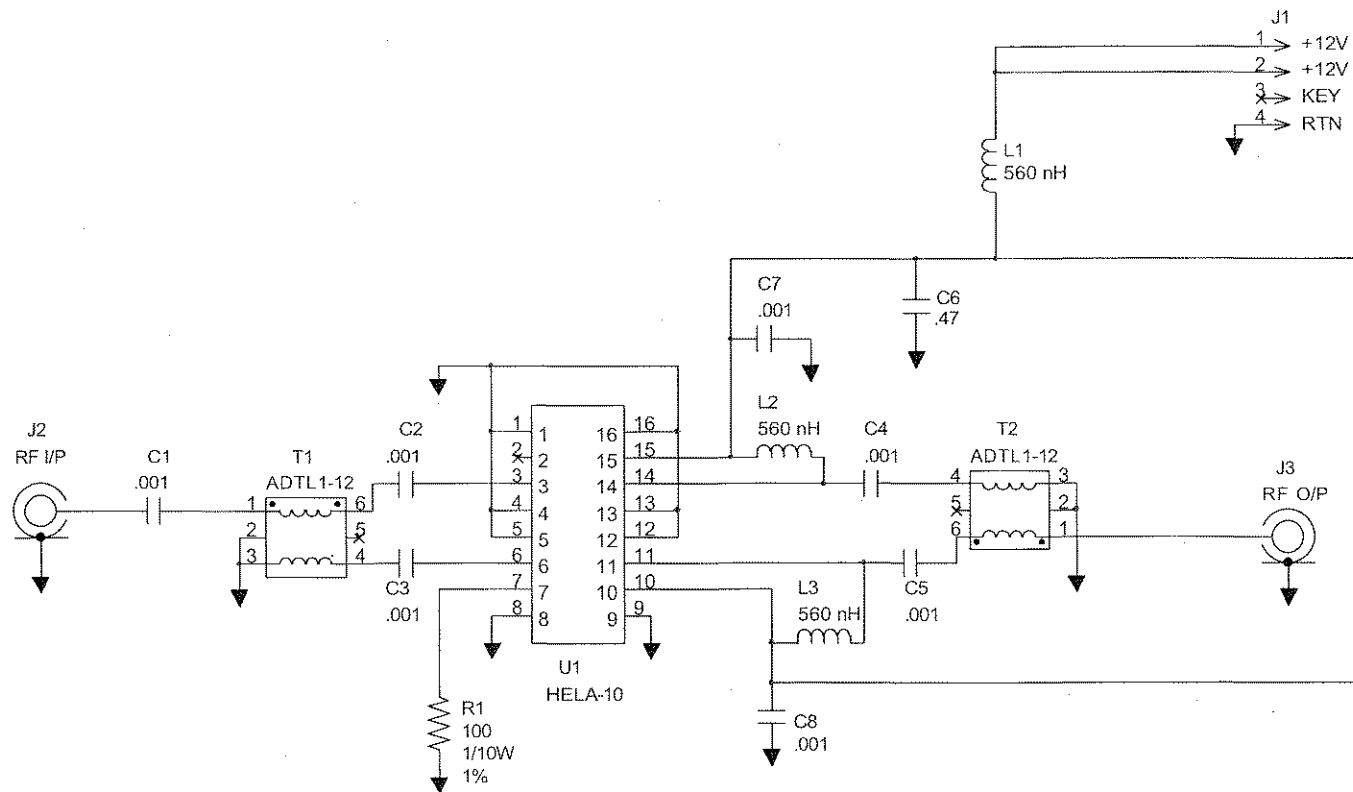
TITLE
 I/C - MIXER/
 AMPLIFIER ENCLOSURE
 ASSEMBLY

DWN CDD 1/6/00 DWG. NO. 1088069 REV A0

CHK RE 1/6/00

REL RE 1/6/00 B SCALE - - SHEET 1 OF 1





LAST USED REF.
C8
J3
L3
R1
T2
U1

- NOTE:
- 1) ALL RESISTORS ARE 1/8W UNLESS OTHERWISE NOTED.
 - 2) ALL CAPACITORS ARE IN μ F UNLESS OTHERWISE NOTED.
 - 3) ALL INDUCTORS ARE IN μ H UNLESS OTHERWISE NOTED.

Axcera				REV	ECO	DATE	APV
DATE	APV	THIS PRINT IS THE PROPERTY OF AXCERA. IT SHALL NOT BE COPIED WITHOUT PERMISSION					
		TITLE PCB, LOW LEVEL STAGE BD., HX LB (1304680)					
ECO	FINISH	MATERIAL		DWN		JWF	
		-----		8/16/04		8/17/04	
REV	REL	DWG. NO.		1304681		A0	
		8/17/04		B		SCALE --- SHEET 1 OF 1	

1806

Part Number

P400-VHF-L-18

Amplifier Name

Revision 1.c Release Date July 11 2007
Revision Notes

Technical Specifications Summary

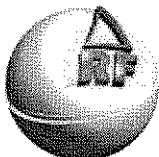
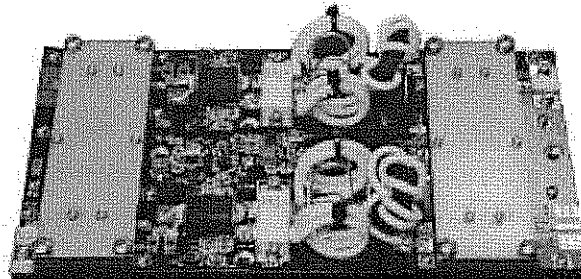
Frequency Range:	50 - 88 MHz	Gain:	19 dB
P1dB:	450 Watts CW	Efficiency:	40%
Class:	AB	Temperature Range:	0 to 70°C
Supply Voltage:	+32.0V	Max VSWR:	5:1

Amplifier General Description

The **P400-VHF-L** is an integrated TV linear amplifier designed for the television integrator. Providing a minimum of 350W Pk sync linear power, the P400-VHF-L is the perfect amplifier for any low band VHF transmitter. Featuring quadrature input and output combining, this unit is isolated from most external circuit problems. This amplifier is MOSFET based.

- ® No RF assembly or circuit tuning!
- ® 400 Watts of Output Power!
- ® Combined Video and Aural operation at full rated power!
- ® 18dB typical gain at Channel 6!
- ® Modular Construction for ease of Integration!

Amplifier Picture



Delta RF Technology, Inc.

High Power RF Amplifiers and Accessories

350 South Rock Boulevard • Reno • NV • 89502 • USA

Phone +1.775 DELTA RF [775 335 8273]

Fax +1.775 DELTA FX [775 335 8239]

website: <http://www.drft.com>

email: sales@drft.com

Parameter	Min	Typ	Max	Units	Notes
Frequency	50		88	MHz	
P1dB		450		W, CW	
Linear Power Out	350	400		W	
IMD3	-40			dBc	For 2 tones, 1MHz spacing, 350 W PEP
Power Input		6	10	W, CW	
Gain	18	19		dB	
Vsupply		32		V, DC	
Drain Current		20	23.5	A, DC	
Input VSWR		1.2:1	1.5:1		
Insertion Phase Variation		±5		°	Unit to unit
Gain Variation		±1		dB	Unit to unit
F2 Second Harmonic		-30		dBc	
F3 Third Harmonic		-10		dBc	
Baseplate Operating Temperature	0		+70	°C	

Physical Dimensions 7.5" x 4.0" x 1.5"

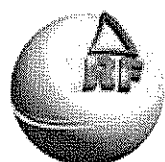
All specifications valid for 50 Ω output load, $V_{sup} = +32VDC$, $I_{dq} = 1.6A$

Absolute Maximum Ratings

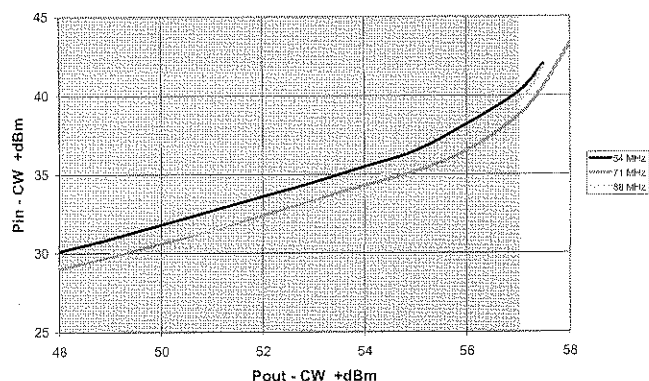
Parameter	Value	Units	Notes
Maximum Operating Voltage	+34.0	VDC	
Stable Operating Voltage	+26.0 to +32.0	VDC	
Maximum Bias Current	3.0	A	
Maximum Drain Current	28	A	
Load Mismatch Survival	5:1		
Storage Temperature	-40 to +105	°C	
Maximum Operating Baseplate Temp	+70	°C	

Features, Auxillary Functions

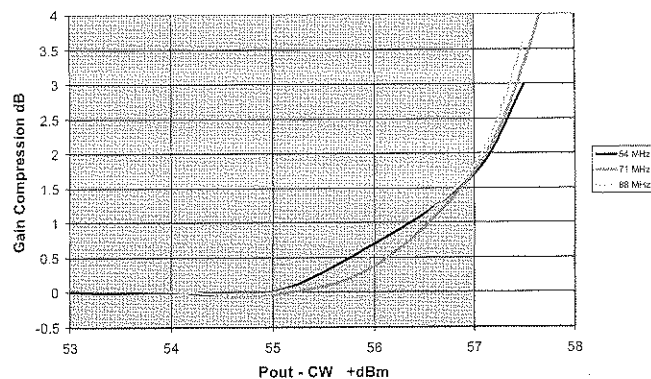
- ♦ Temperature Compensated Bias
- ♦ Temperature Controller - Analog Temperature Output
- ♦ High Temperature Alarm with Selectable Automatic PA Disable
- ♦ High Temperature Alarm Output
- ♦ Amplifier Disable
- ♦ Current Sense, Each Transistor
- ♦ Connectorized Power and I/O



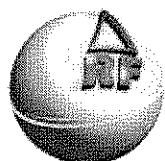
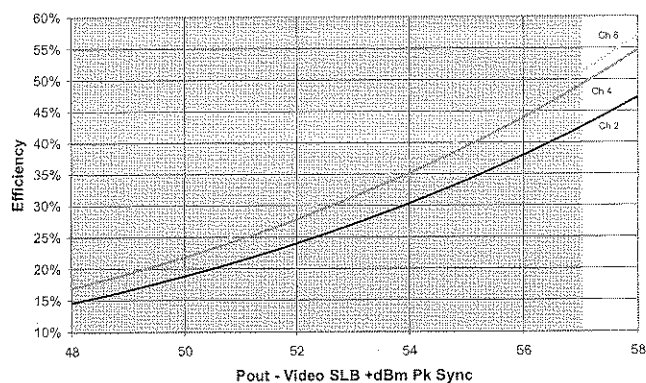
Pin vs. Pout



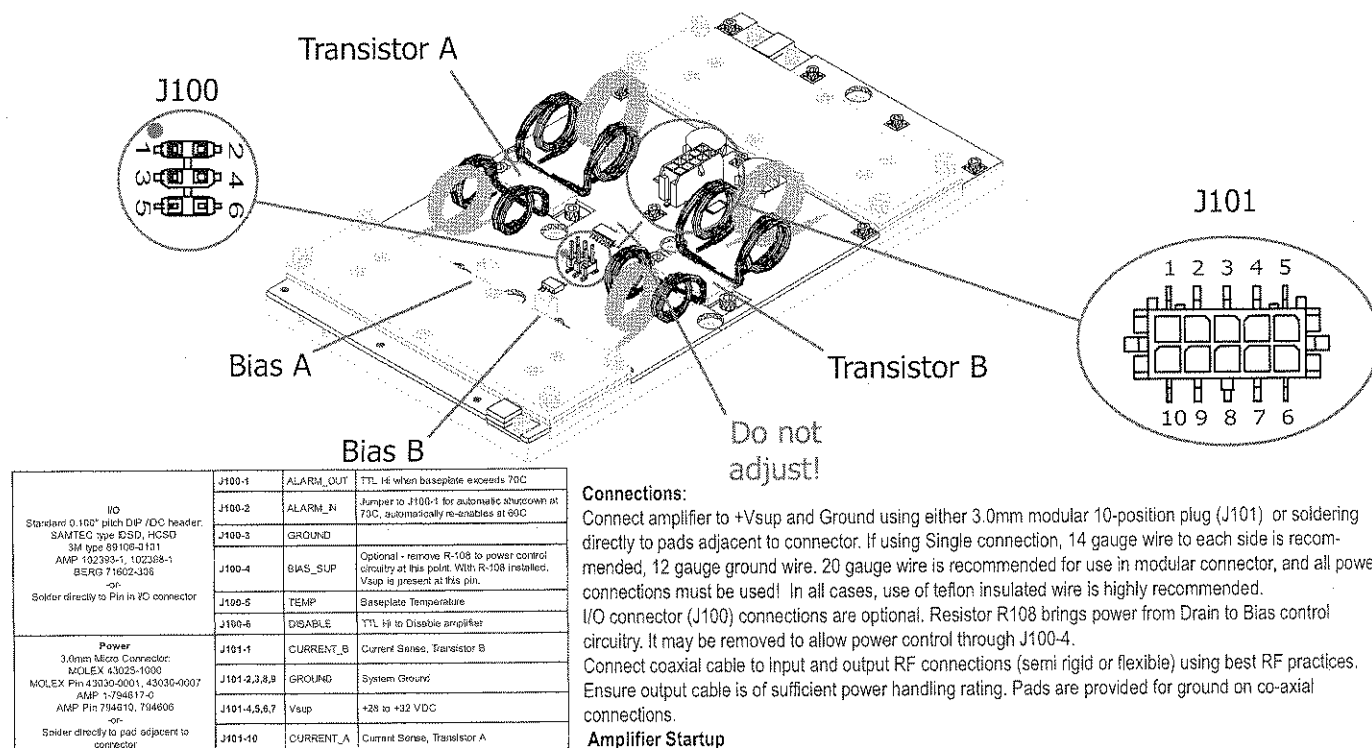
Gain Compression



Efficiency



Electrical Connections:



Connections:

Connect amplifier to +Vsup and Ground using either 3.0mm modular 10-position plug (J101) or soldering directly to pads adjacent to connector. If using Single connection, 14 gauge wire to each side is recommended, 12 gauge ground wire. 20 gauge wire is recommended for use in modular connector, and all power connections must be used! In all cases, use of teflon insulated wire is highly recommended.

I/O connector (J100) connections are optional. Resistor R108 brings power from Drain to Bias control circuitry. It may be removed to allow power control through J100-4.

Connect coaxial cable to input and output RF connections (semi rigid or flexible) using best RF practices. Ensure output cable is of sufficient power handling rating. Pads are provided for ground on co-axial connections.

Amplifier Startup

+Vsup should be applied to amplifier with no drive and with no bias applied. The system must allow drain voltage to reach +26V minimum before applying bias and drive or damage will result to the amplifier and void warranty. This typically takes between 2 - 10 seconds and should be verified by the system integrator. This can be accomplished in several ways:

- 1) Apply power to amp at J101, remove power from J100-4, and remove R108. After proper voltage has been reached, apply +28V - +32V to J100-4. Amplifier is ready for use.
- 2) Apply power to amp at J101. Place a TTL Hi (+5V) to J100-6 DISABLE. After proper voltage has been reached, remove TTL Hi from J100-6 DISABLE. Amplifier is ready for use.

Bias Current:

Bias current is controlled via temperature compensated bias system that uses a hermetically sealed glass thermistor as reference. If excessive air is directed above the amplifier such that the thermistor is cooled below the temperature of the baseplate, this circuitry may not perform properly. Bias has been pre-set at the factory to 0.8A each side at +32.0V DC. This bias point has been selected to offer the optimum balance between IMD performance, efficiency, and gain. If the bias point is changed, take great care to set the same bias point on each transistor, and not to exceed the bias maximum listed on page 1.

Fault Condition - Bad VSWR

Current sense J101-1, J101-10 should be monitored for excessive current. The voltage difference between J100-10 (transistor A) to J100-4,5,6,7 and J100-1 (transistor B) to J100-4,5,6,7 is scaled 1A per 0.010 V. If either transistor experiences currents in excess of normal operation, a fault condition exists, and the amplifier should be disabled through J100-6 DISABLE. If current on either transistor drops to below 0.5A indicated, a fault condition exists, and the amplifier should be disabled through J100-6 DISABLE.

Temperature Sense and Temperature Fault

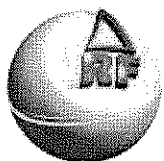
An on board temperature controller reports temperature on pin J100-5 TEMP. This is scaled to +395mV + (Temperature °C X +6.20mV/°C) and has an output impedance of 1.5kohm typical. An output alarm, J100-1 ALARM OUT, is TTL Low when the temperature exceeds approximately 70°C, and the alarm is cleared when the baseplate temperature drops below approximately 60°C. For automatic operation, jumper J100-1 ALARM OUT to J100-2 ALARM IN and the amplifier will automatically disable by removing bias when the temperature exceeds 70°C, and automatically re-enable when the temperature drops below 60°C.

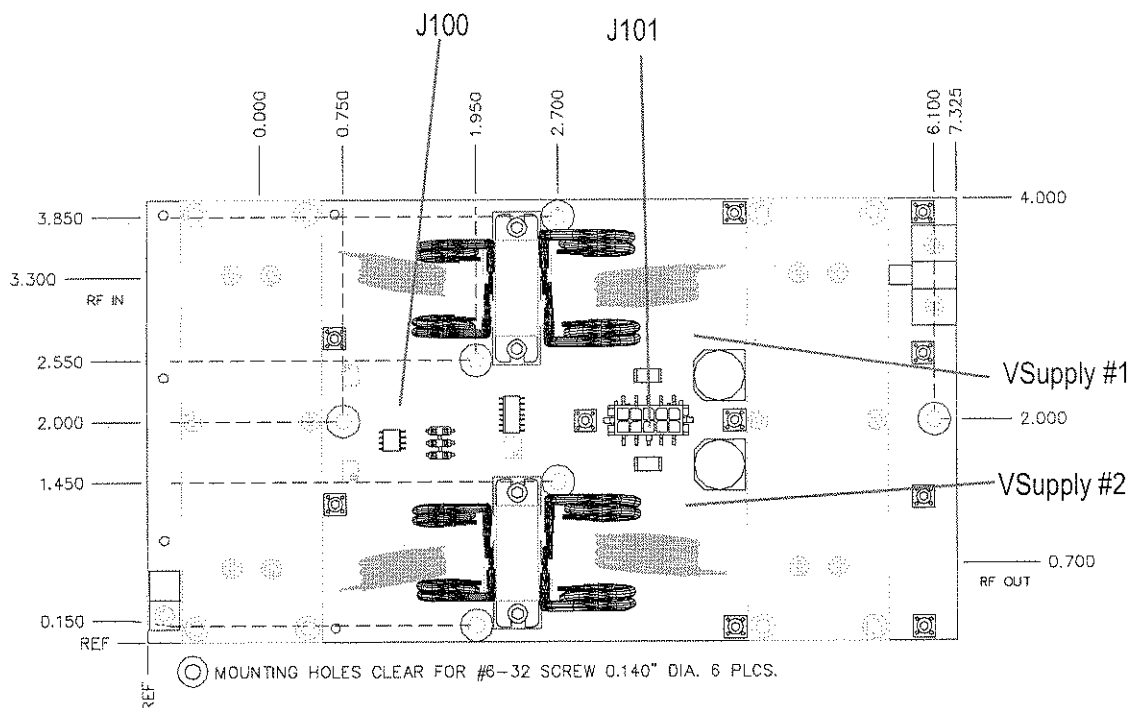
Amplifier Shutdown

To prevent damage to amplifier and surrounding systems, bias and drive should be removed prior to powering down PA. This can be accomplished by removing voltage from J100-4 with R-108 removed, or by applying TTL Hi (+5V) to J100-6 DISABLE. Power can safely be removed from PA.

Miscellaneous:

It is normal for the output quadrature and the output transformers (flexible coax on the output of transistors A and B) to get warm during operation. These components are rated for continuous operation in excess of 150°C. Placing noisy analog or digital systems, such as additional control circuitry, directly over the top of transistors or RF path can cause improper operation. Care should be taken to locate these components where they will not cause interference.



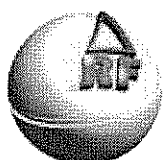


Tips for Mechanical Mounting:

- 1 All holes are clear for #6 Screw. Stainless Steel mounting hardware is recommended, grade 18-8 or better. A lock washer of same material should also be used.
- 2 Ensure mounting surface is flat to better than 0.003" / "
- 3 Use a thin layer of thermal compound on the backside of the PA - no more than 0.001" - 0.002" thickness!
- 4 Torque all screws to 10-12 in-lbs

Considerations for Mechanical Mounting:

- Considerations for proper thermal design include
- Total power dissipated = Total DC Power Consumed x (1-Efficiency)
- Ambient Airflow
- Thermal Resistance of Heat Sink
- VSupply #1 and #2 must both be connected.



Ordering Information:

Order Code	Description	DRFT Reference
P400-VHF-L	500W VHF Band I TV Linear Pallet Amplifier	1806
PAB400-VHF-L	Amplifier in Enclosure	TBA

Options

-A11	SMA Female Connectors In / Out	0201
-A12	Heat Sink Option	0202
-A13	Heat Sink Option with DC Fan, pre wired	0203
-A14	Ruggedized for vibration	0204
-A15	Wire harness, 1' length, 10 wires for pallet amplifier only (NON-FM)	0205
-A16	Wire harness, customer specified length for pallet amplifier only	0206
-T2	Extended Burn In	0271
-T3	Extended Data Collection	0272

Standard Pallet Options:

SMA Female Connectors, Input and Output. Stainless Body, Gold Center pin, 4-hole SMA bolted to pallet amplifier edge through bottom two holes located at amplifiers RF IN and RF OUT locations. All stainless steel hardware.

Enclosure- all aluminum machined enclosure available for most pallet amplifiers. Alodined aluminum, alloy 6061-T6. SMA Female input and output RF connectors. Supply voltage and ground through solder / feedthrough connections. Module must be bolted to appropriate heatsink.

Heat Sink - aluminum extruded heat sink, black anodized. Pallet amplifier or module will be bolted to heatsink. Customer will be required to provide adequate airflow.

Heat sink with fan - aluminum extruded heat sink as above, with included fan bolted to push air through the heat sink. Depending on heat requirements, a second fan may also be provided on the output of the unit.

Ruggedized - all screws have threadlocking compound applied, and all flying components are staked and attached to base. Designed to withstand MIL-STD-810E 514.4 Category 8.

Power Connector - a 10 pin molex connector is used on all standard pallet amplifiers to supply +Vsup and Ground connections, as well as hi-side current shunts for current monitoring. Delta RF offers the mating connector with 1' wires - Red (Vsup), Black (Ground), Yellow (Current monitor). All wires are 18 gauge teflon insulated wires. Customer may optionally specify wire length and wire color.

Testing Options:

Standard - includes power test and brief burn - in under laboratory conditions. Printed test report gives graph of Gain and Input Return Loss at rated P1dB and Voltage Conditions. Report shows pass/fail criteria. All amplifiers include this test.

Extended burn in - 8-hour burn in at P1dB with standard test run at completion. Unit is monitored during test and any discrepancy reported. Standard test data is included.

Extended data collection - Standard data is run and included. Detailed data is taken point by point giving the customer 25 - 70 frequency points, depending on the amplifier model. For each frequency point, data is generated to include gain, input power, input return loss, current, second harmonic, third harmonic, efficiency, audio distortion.

Other tests available - Vibration, Temp cycling, Shock. Please inquire.

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