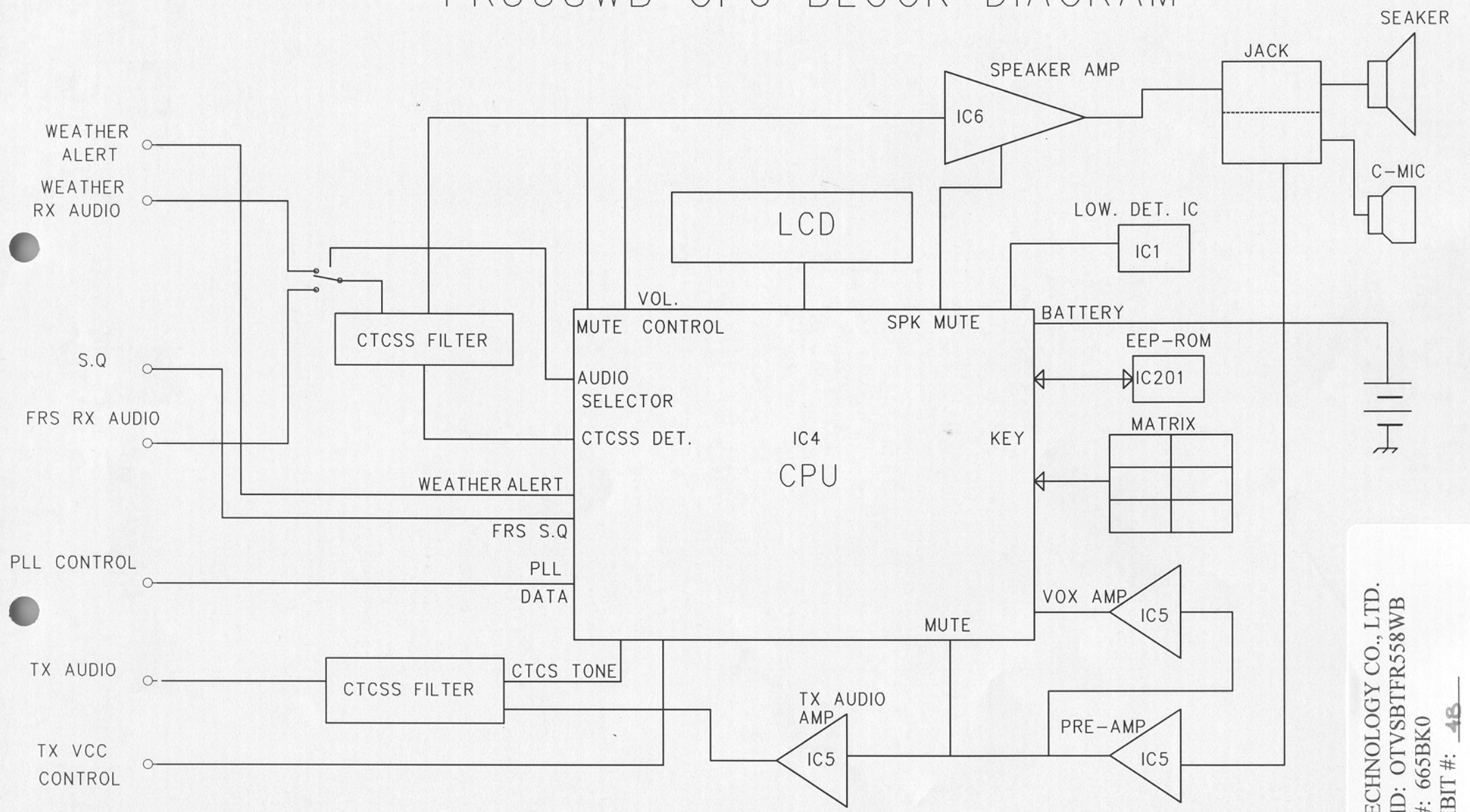


FR558WB CPU BLOCK DIAGRAM

REV.	ECO-NO	DATE



SB TECHNOLOGY CO., LTD.
 FCC ID: OTVSBTFR558WB
 JOB #: 665BK0
 EXHIBIT #: 48

DESIGN	CHECKED	APPROVED	S.B TECHNOLOGY SCHEMATIC DIAGRAM		
			Title FR558 WB CPU BLOCK DIAGRAM		
Size	Document Number	REV			
	CPU BLOCK.SCH	0			
Date :	2000.12.4	Sheet	2	of	2