

Model:56812 (Receiver portion)
Technical Description of Receiver

The receiver operates at 49.860 MHz, and controls by the corresponding transmitter to move forward, backward, left and right.

The circuit of the receiver comprises of,

1. Super-regenerative circuit

Consisting: L2, C2, C1, L1, Q1, L3, C3

2. Decoder

Consisting: IC1 (PT8A978BLW)

3. Drive Motor Circuit

Consisting: Q2,Q3,Q4,Q5,Q6,Q7,Q8,Q9,Q10,Q11,Q12,Q13

4. A Power Supply

Consisting: 4*1.5V AA batteries

5. Clock Frequency Generator

Consisting: R6 (220kohm) for Internal clock frequency oscillation 128KHz

6. Regulator

Consisting: D2 (3.3V)

The receiver uses an internal, integral and non-detachable antenna.