

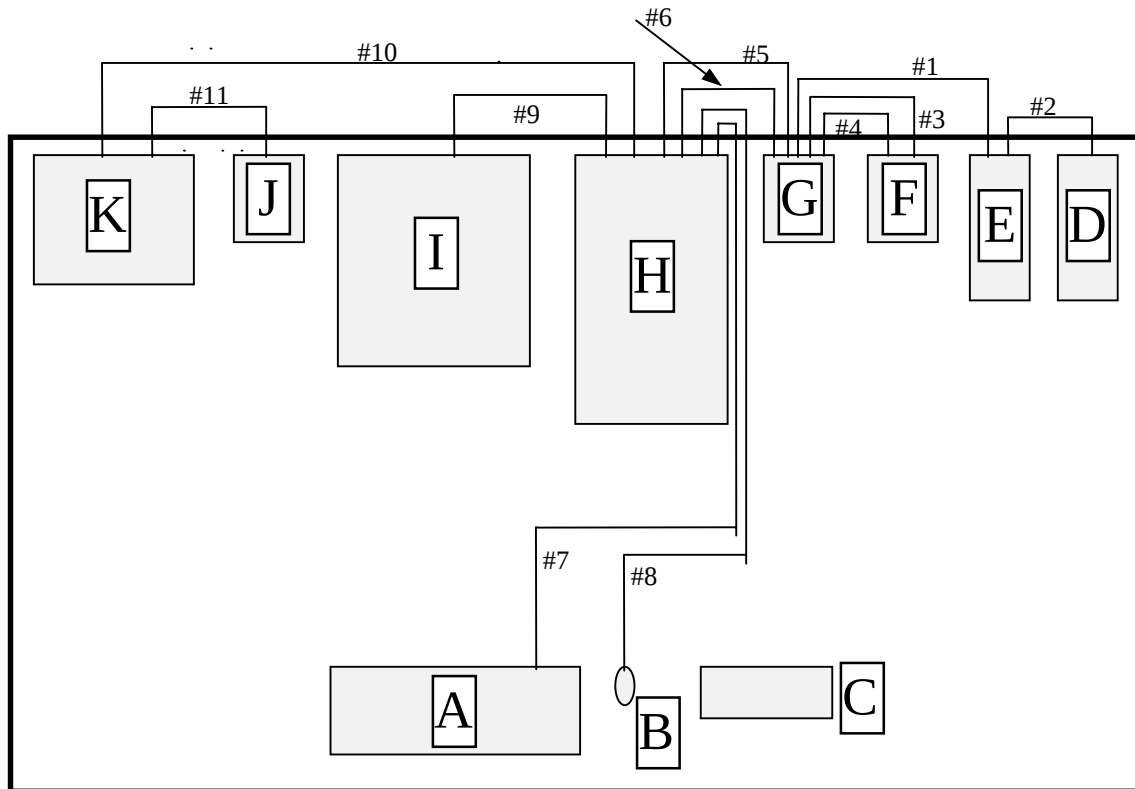
FCC ID: OT7418

EMT

ELECTRO MAGNETIC TEST, INC.

1547 Plymouth Street, Mountain View, CA 94043 Tel: (650) 965-4000 Fax: (650) 965-3000

BLOCK DIAGRAMS

EQUIPMENT CONFIGURATION BLOCK DIAGRAM

Wooden Test Table

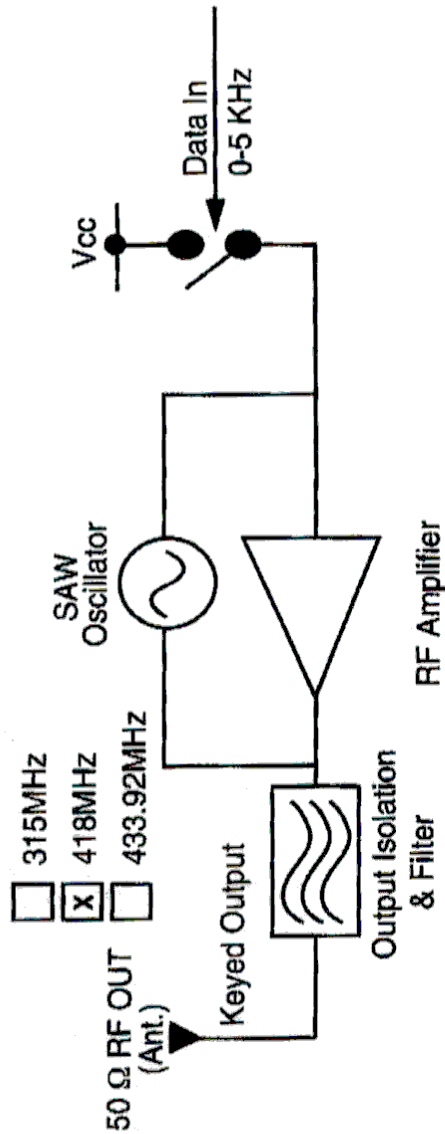
A. Keyboard	G. EUT (Receiver)
B. Mouse	H. Computer
C. EUT (Transmitter)	I. Monitor
D. Speaker B	J. AC Adapter
E. Speaker A	K. Printer
F. Sound Feeder	

E.M.T.

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Linx RF Module Block/Signal Diagram



Linx TECHNOLOGIES
HYBRID RF MODULES

575 SE ASHLEY PLACE • GRANTS PASS, OR 97526

Module Series	LC TXM	Architecture	Saw Stabilized AM Transmitter
Modulation Method	AM Keyed Carrier	Duty Cycle	Determined by User Data
Frequency	☐ 315 ☒ 418 ☐ 433.92	Overall Accuracy	± 50 KHz
Occupied Bandwidth	<50 KHz		

This product utilizes a modular hybrid RF stage manufactured by Linx Technologies Inc. The module contains all RF components excepting antennas. While each module is inherently designed to meet or exceed all FCC requirements, external factors such as antenna selection, transmission content, and intended application may affect its use. A block diagram of the module's internal architecture and signal path is shown above. Additional information regarding the use, construction or testing of Linx modules may be obtained by calling (541) 471-6256, from 8-4 PST or addressing a written e-mail request to info@linxtechnologies.com.

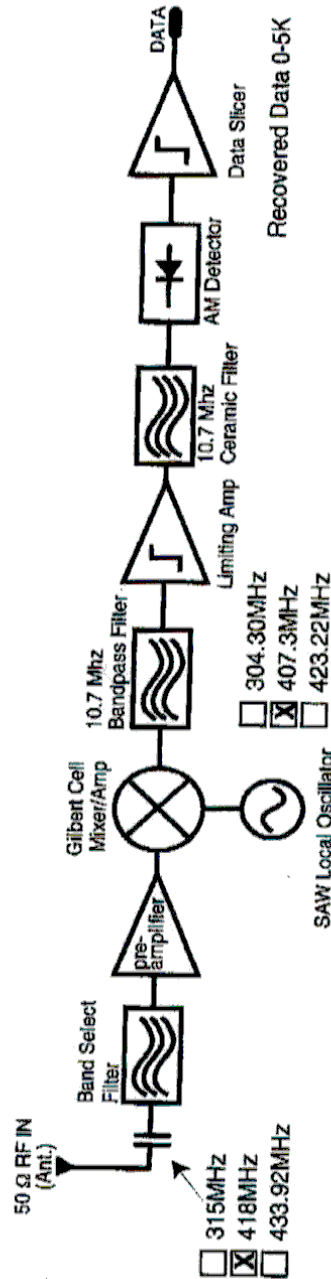
BLOCK DIAGRAM FOR TRANSMITTER UNIT (U4)

E.M.T.

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Linx RF Module Block/Signal Diagram



Linx TECHNOLOGIES
575 SE ASHLEY PLACE • GRANTS PASS, OR 97526
PHONE: (800) 736-6677 • FAX: (541) 471-6251

Module Series	LC RXM	Architecture	Single Conversion Superhet
Modulation Method	AM Keyed Carrier	Duty Cycle	NA
Frequency	☐ 315 ☒ 418 ☐ 433.92	Overall Accuracy	± 90 KHz
Occupied Bandwidth	NA		

This product utilizes a modular hybrid RF stage manufactured by Linx Technologies Inc. The module contains all RF components excepting antennas. While each module is inherently designed to meet or exceed all FCC requirements, external factors such as antenna selection, transmission content, and intended application may affect its use. A block diagram of the module's internal architecture and signal path is shown above. Additional information regarding the use, construction or testing of Linx modules may be obtained by calling (541) 471-6256, from 8-4 PST or addressing a written e-mail request to info@linxtechnologies.com.

BLOCK DIAGRAM FOR RECEIVER UNIT (U7)

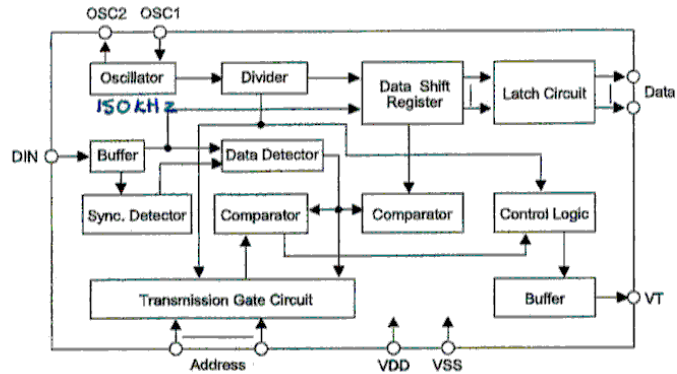
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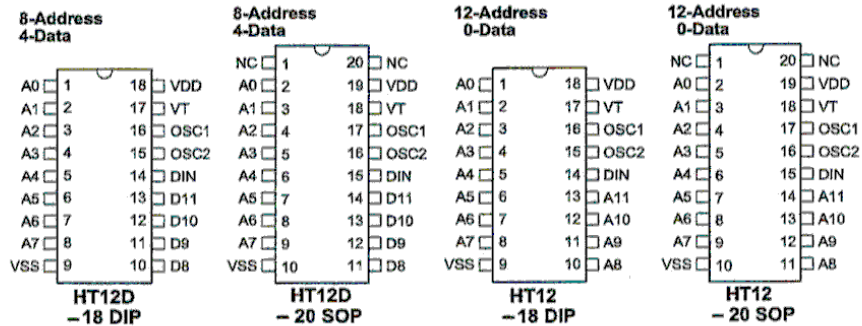
U3

HOLTEK**2¹² Series of Decoders**

HT12D Block Diagram

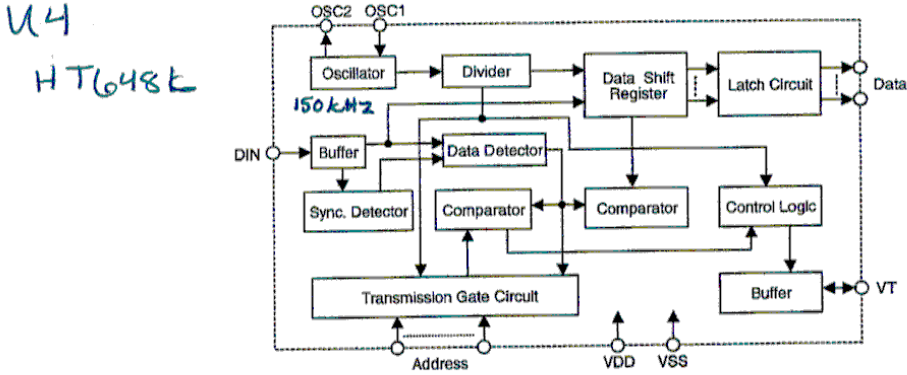


Note: The address/data pins are available in various combinations (see the address/data table).

Pin Assignment

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HULTER**3rd Series of Decoders****Block Diagram**

Note: The address/data pins are available in various combinations (refer to the address/data table).

Pin Description

Pin Name	I/O	Internal Connection	Description
A0~A17	I	TRANSMISSION GATE	Input pins for address A0~A17 setting They can be externally set to VDD, VSS, or left open.
D10~D17	O	CMOS OUT	Output data pins
DIN	I	CMOS IN	Serial data input pin
VT	O	CMOS OUT	Valid transmission, active high
OSC1	I	OSCILLATOR	Oscillator input pin
OSC2	O	OSCILLATOR	Oscillator output pin
VSS	I	—	Negative power supply (GND)
VDD	I	—	Positive power supply