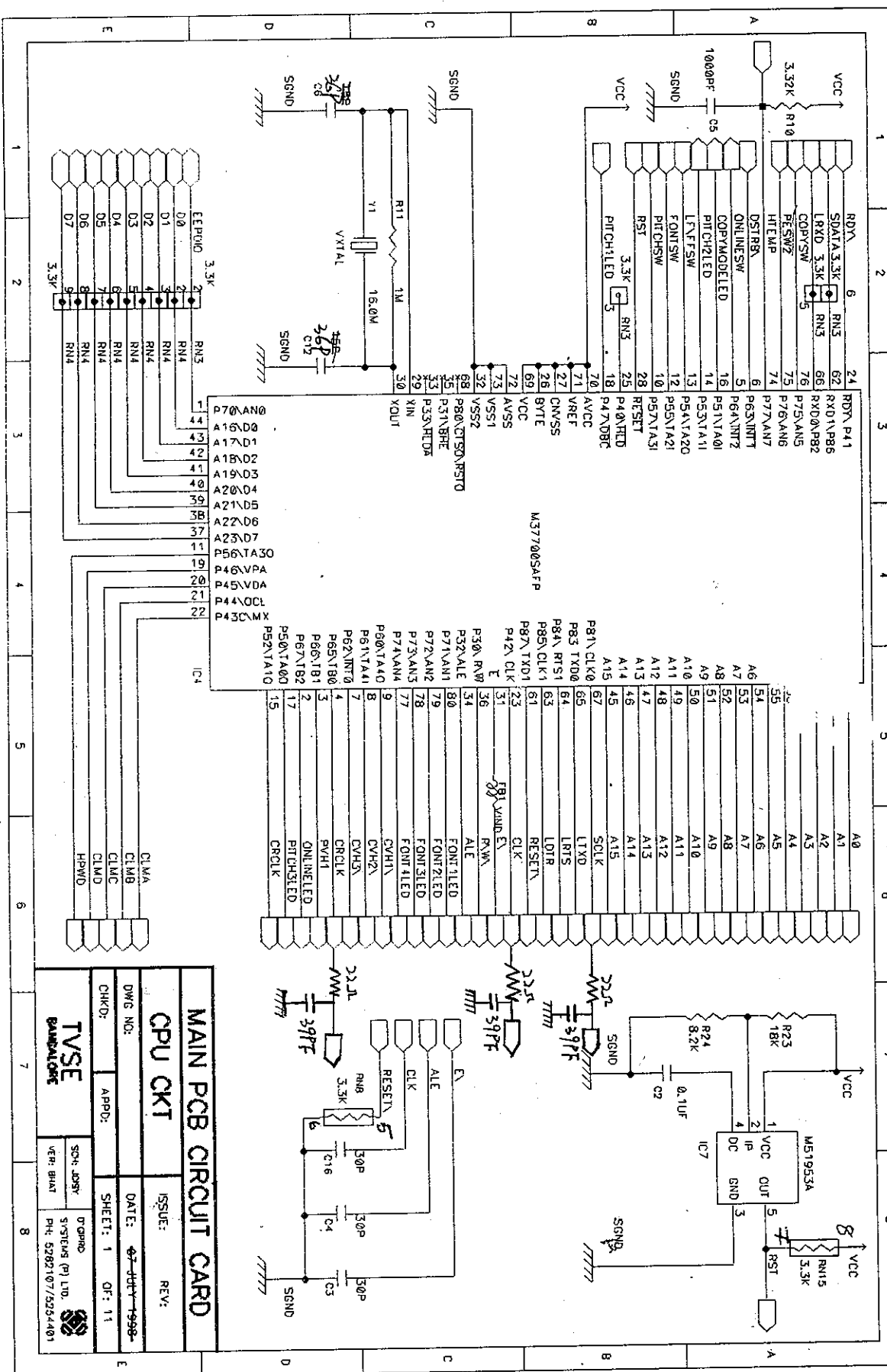
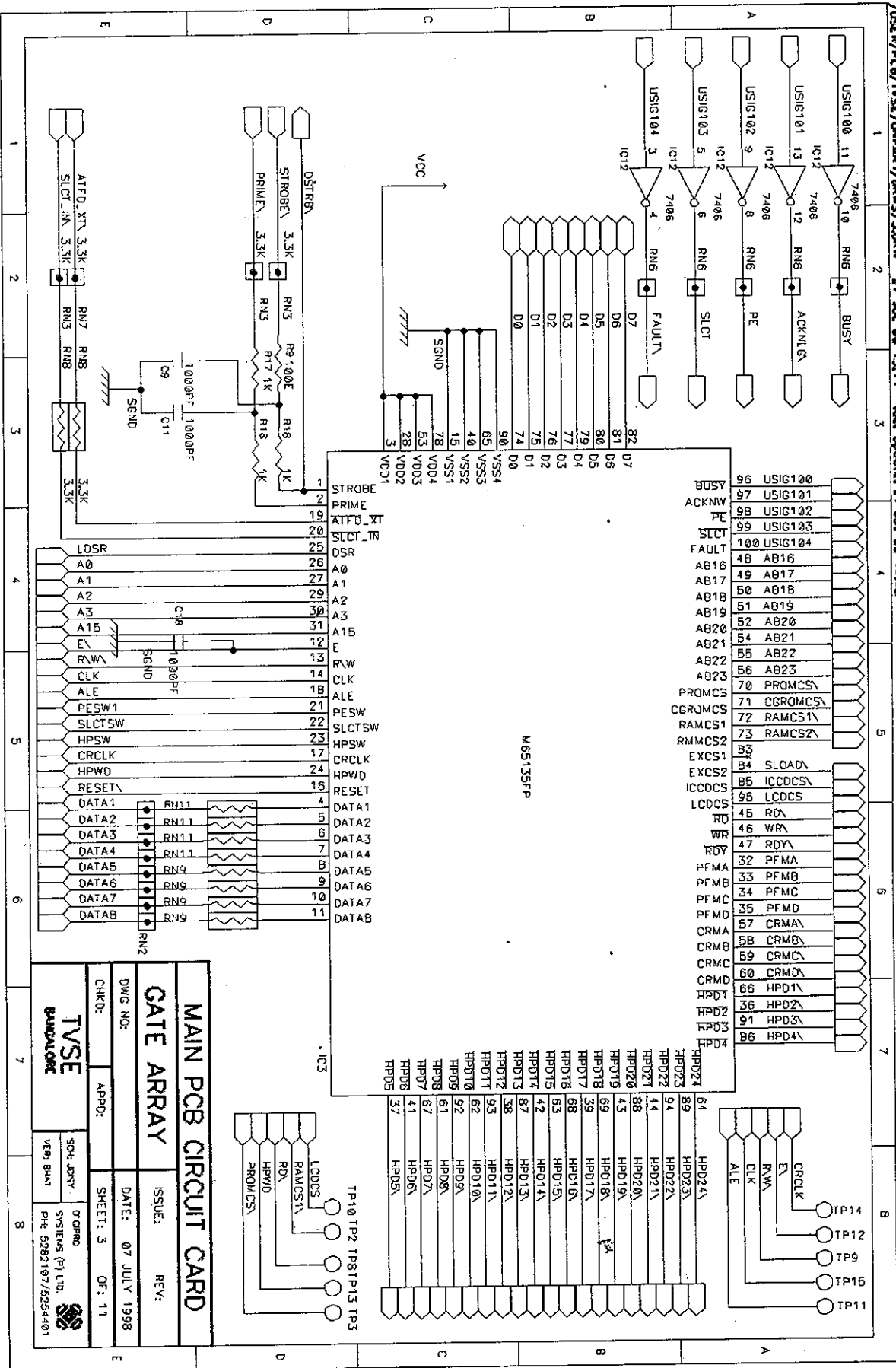


EXHIBIT D

Circuit Diagram

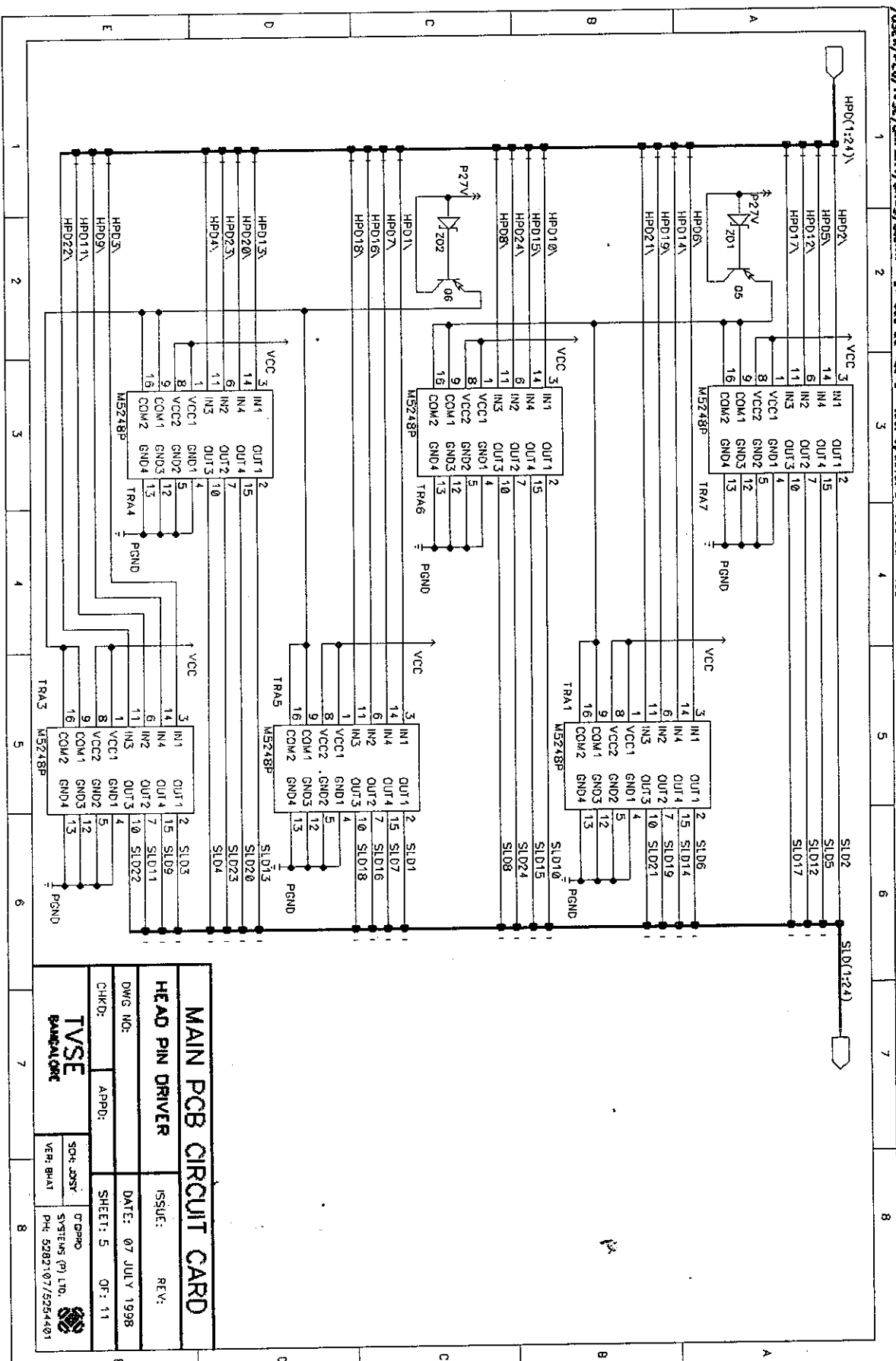




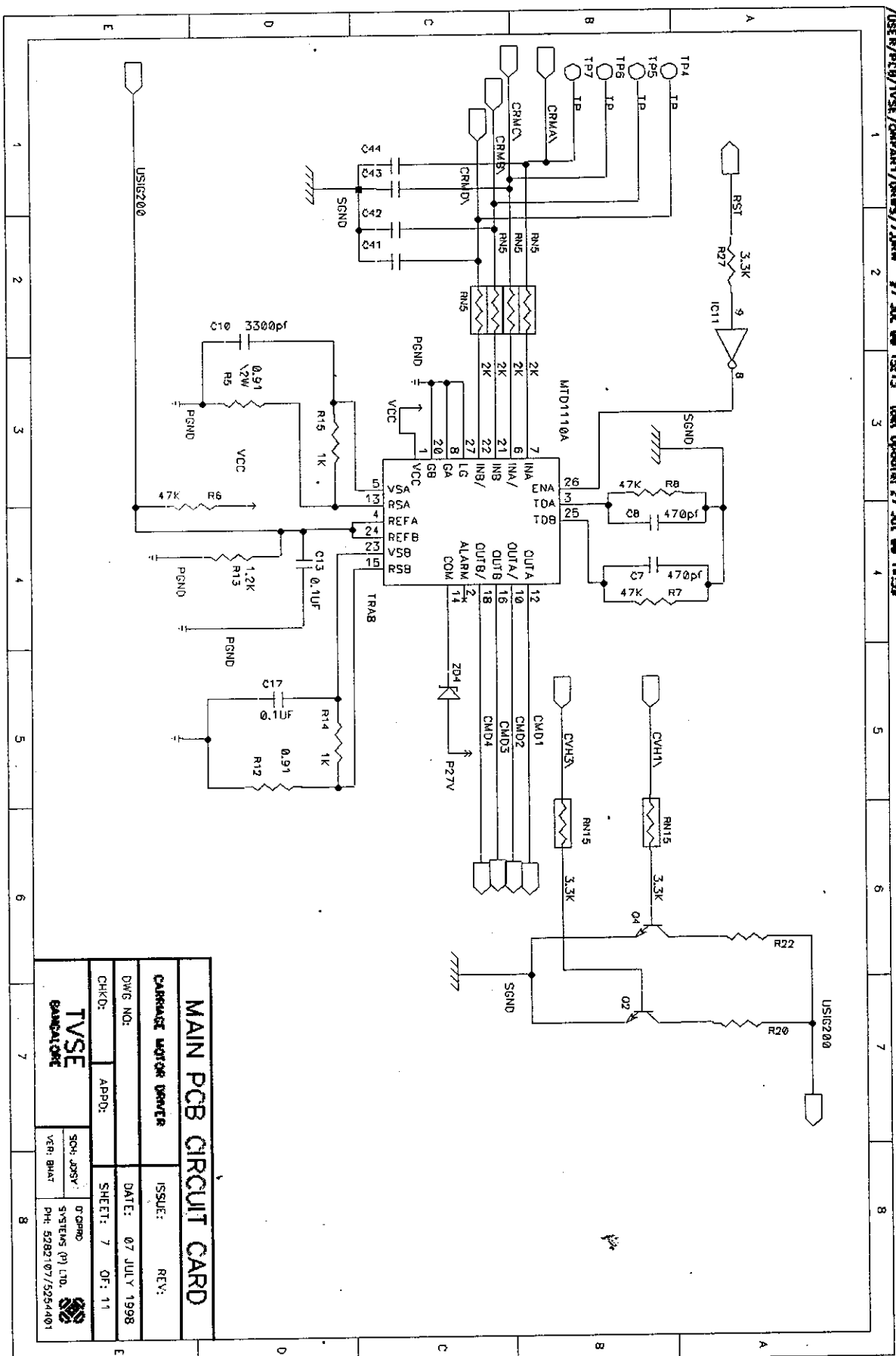
MAIN PCB CIRCUIT CARD

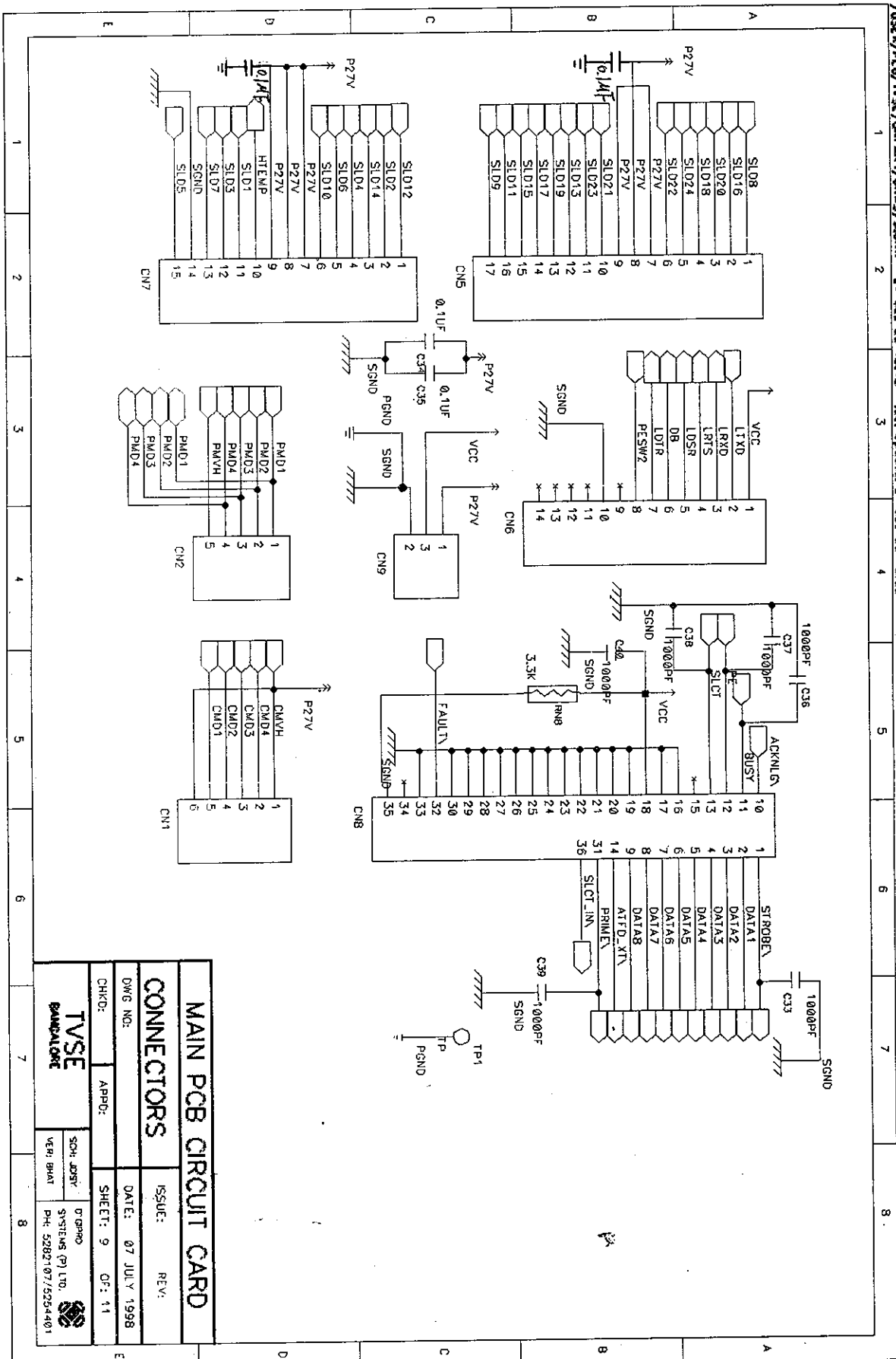
GATE ARRAY

DWG NO:	ISSUE:	REV:
CHKO:	DATE: 07 JULY 1998	
APPR:	SHEET: 3 OF: 11	
SCH: JSTY	SYSTEMS (P) LTD.	
VER: B41	PH: 5282107/5254401	

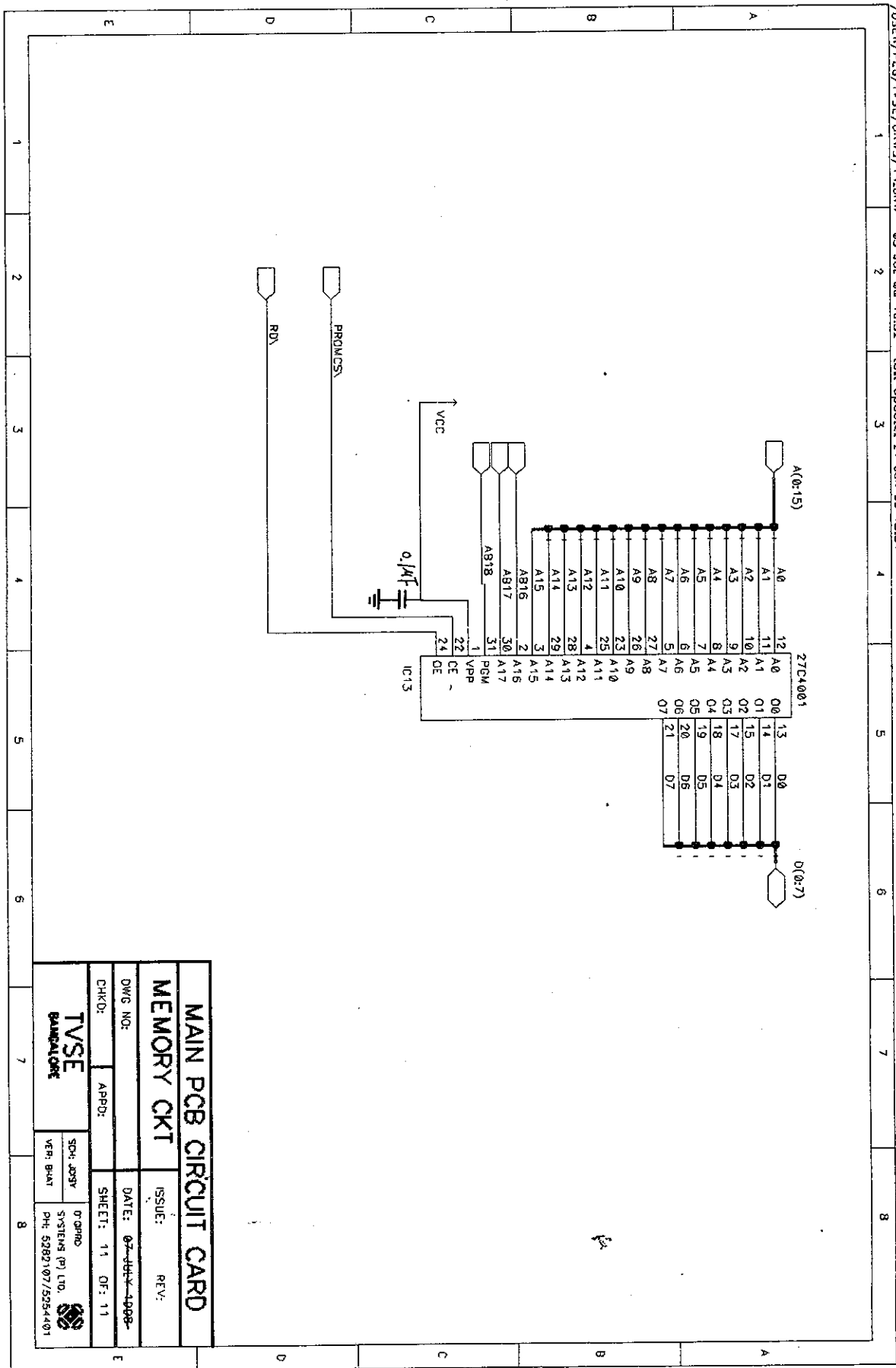


MAIN PCB CIRCUIT CARD			
HEAD PIN DRIVER		ISSUE:	REV:
DWG NO:		DATE: 07 JULY 1998	
CHKD:	APPD:	SHEET: 5	OF: 11
TISE		SOA: 2057	Q: 0000
BANKALONE		VER: BHAT	PH: 5282107/5254401





MAIN PCB CIRCUIT CARD			
CONNECTORS		ISSUE:	REV:
DWG NO:		DATE: 07 JULY 1998	
CHKD:	APPD:	SHEET: 9	OF: 11
TVSE		D'OPPO	
BANDALONE		SYSTEMS (P) LTD.	
		PHE: 5282107/5254401	



MAIN PCB CIRCUIT CARD			
MEMORY CKT		ISSUE:	REV:
DWG NO:	DATE: 07-000-4008		
CHKD:	APPD:	SHEET: 11	OF: 11
TVSE		SCH: JDSY	OF DRWD
BANGALORE		VER: BANT	SYSTEMS (P) LTD.
		PH: 5282107/52554401	