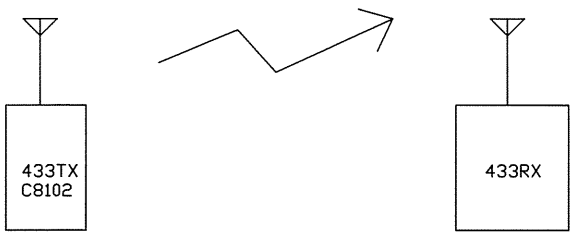
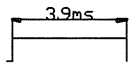
Chungs electronic co., ltd

2006-7-3 Chan Kai Hung

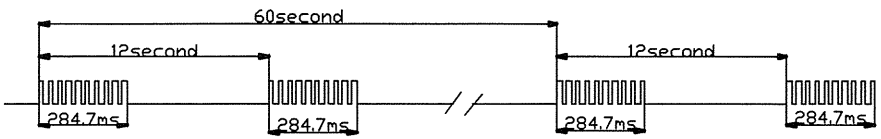
C8201 TIMING DIAGRAM





EACH PULSE

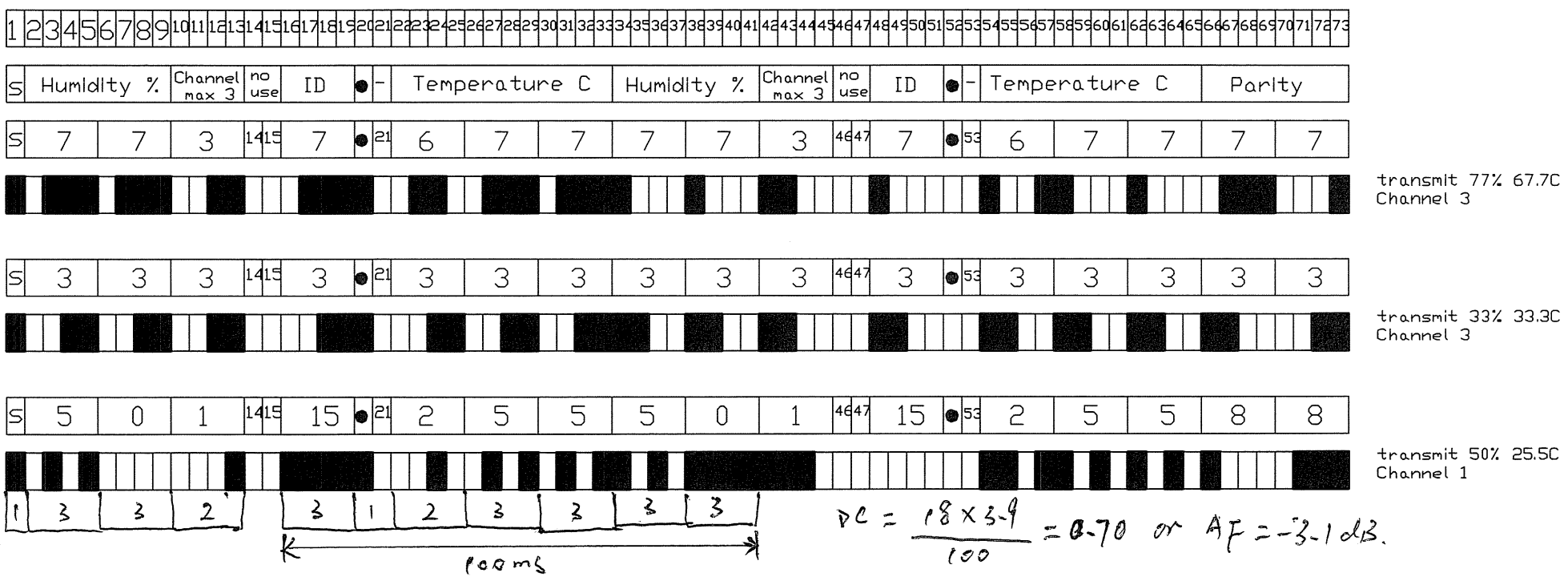
EACH DATA=3.9x73=284.7ms



Transmission Patten

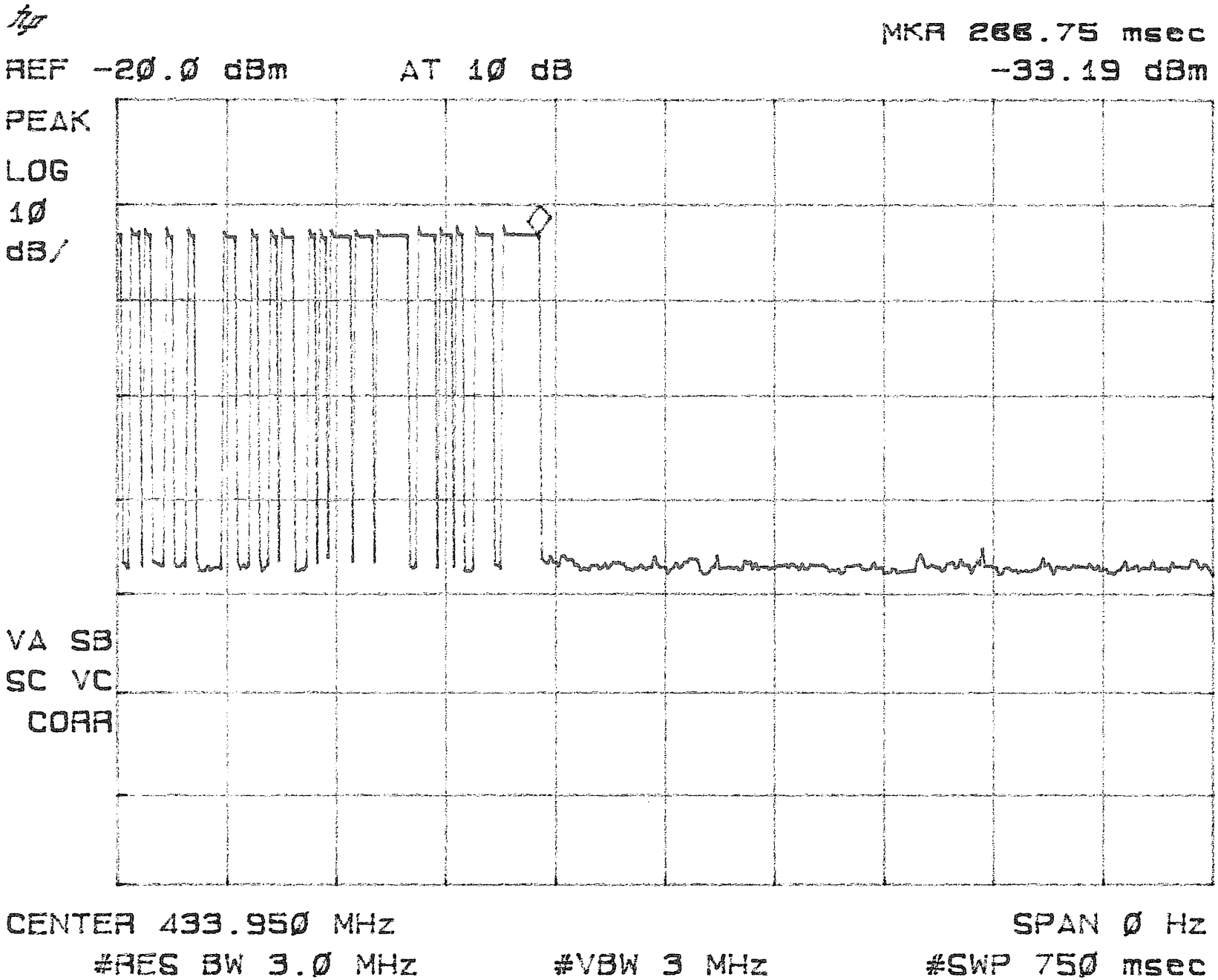
For reference transmit patten
BCD CODE

■=High □=Low [S]=start



$DC = \frac{18 \times 3.9}{100} = 0.70$ or $AF = -3.1\,dB$.

0609656 (CH2)


MKR 266.75 msec
REF -20.0 dBm
AT 10 dB
-33.19 dBm
PEAK
LOG
10
dB/
VA SB
SC VC
CORR
CENTER 433.950 MHz
SPAN 0 Hz
#RES BW 3.0 MHz
#VBW 3 MHz
#SWP 750 msec