

The following are the block diagrams for the RF and digital sections of the EUM3003. The blocks shown are as follows:

Top level
RF section
Ethernet MAC/PHY
Microprocessor memory
Microprocessor
MAC interface

1. Block Diagram

1.1 Top Level Block Diagram

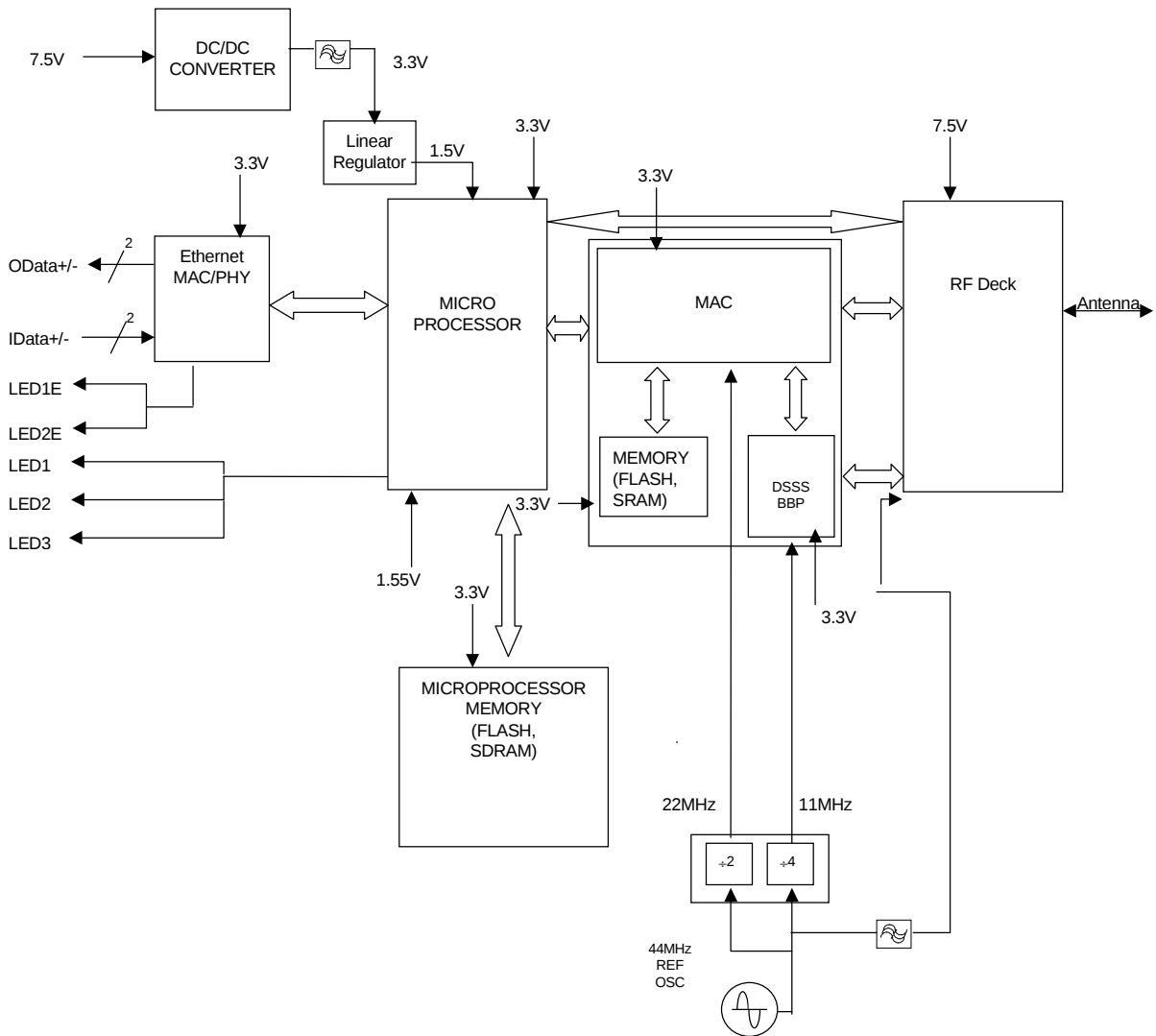


Figure 1.1 – Top Level Block Diagram

1.2 RF Section

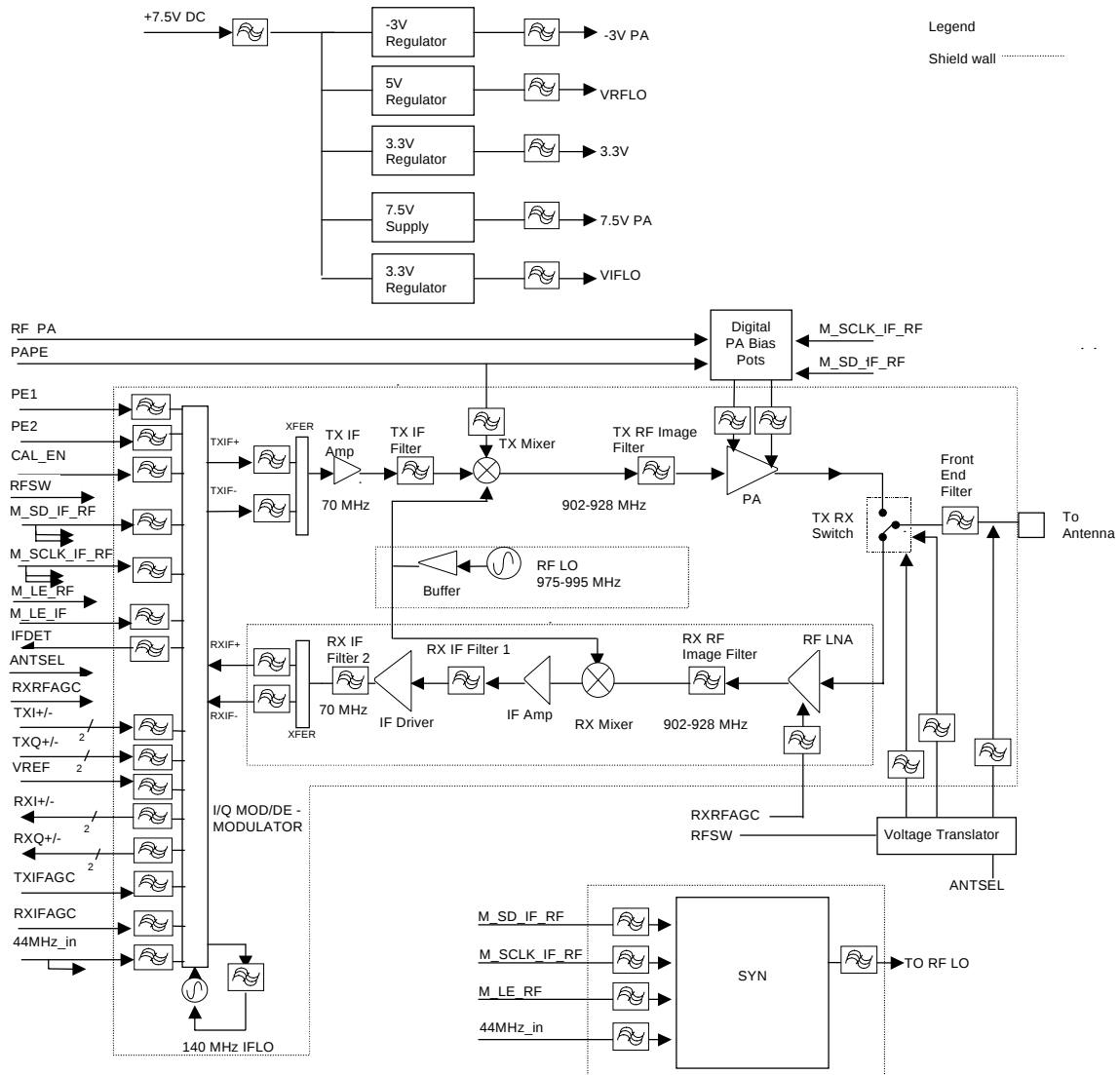
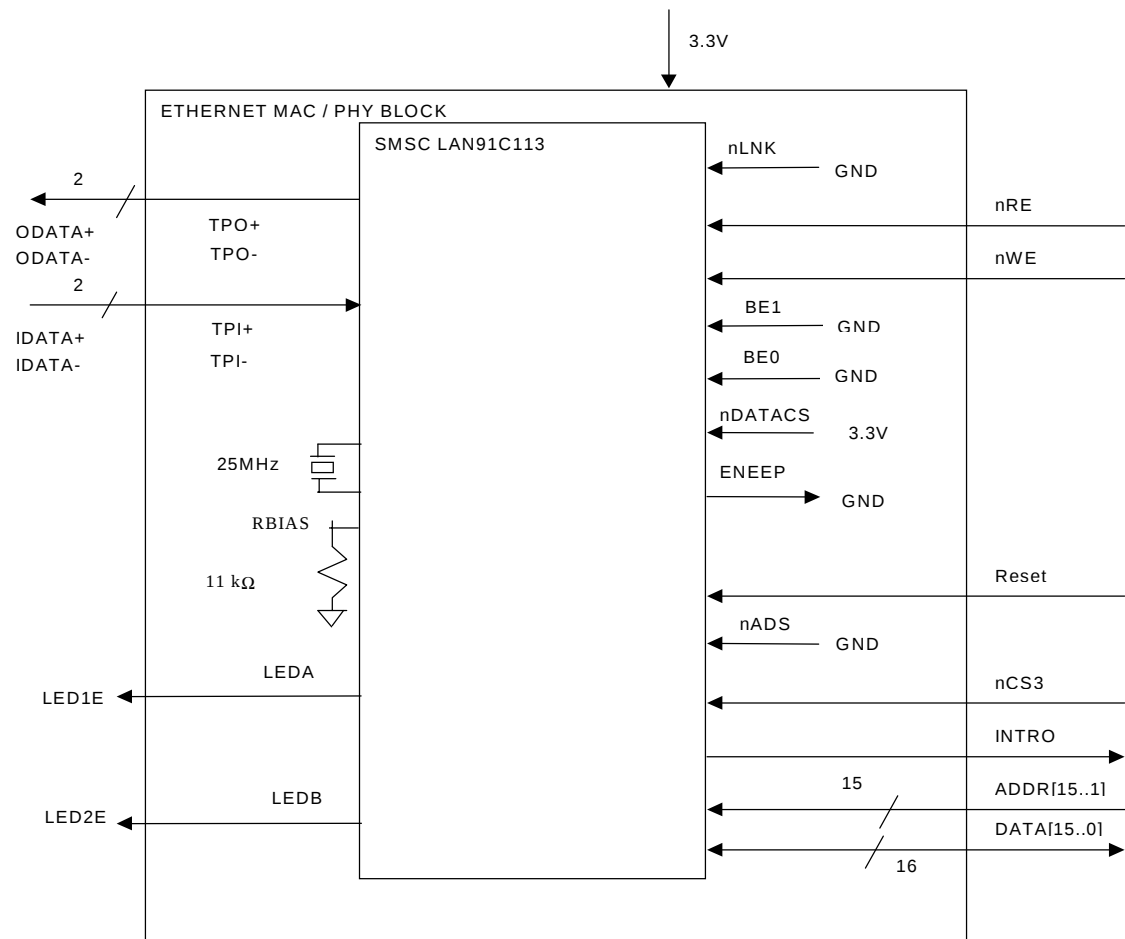


Figure 1.2 – RF Section Block Diagram

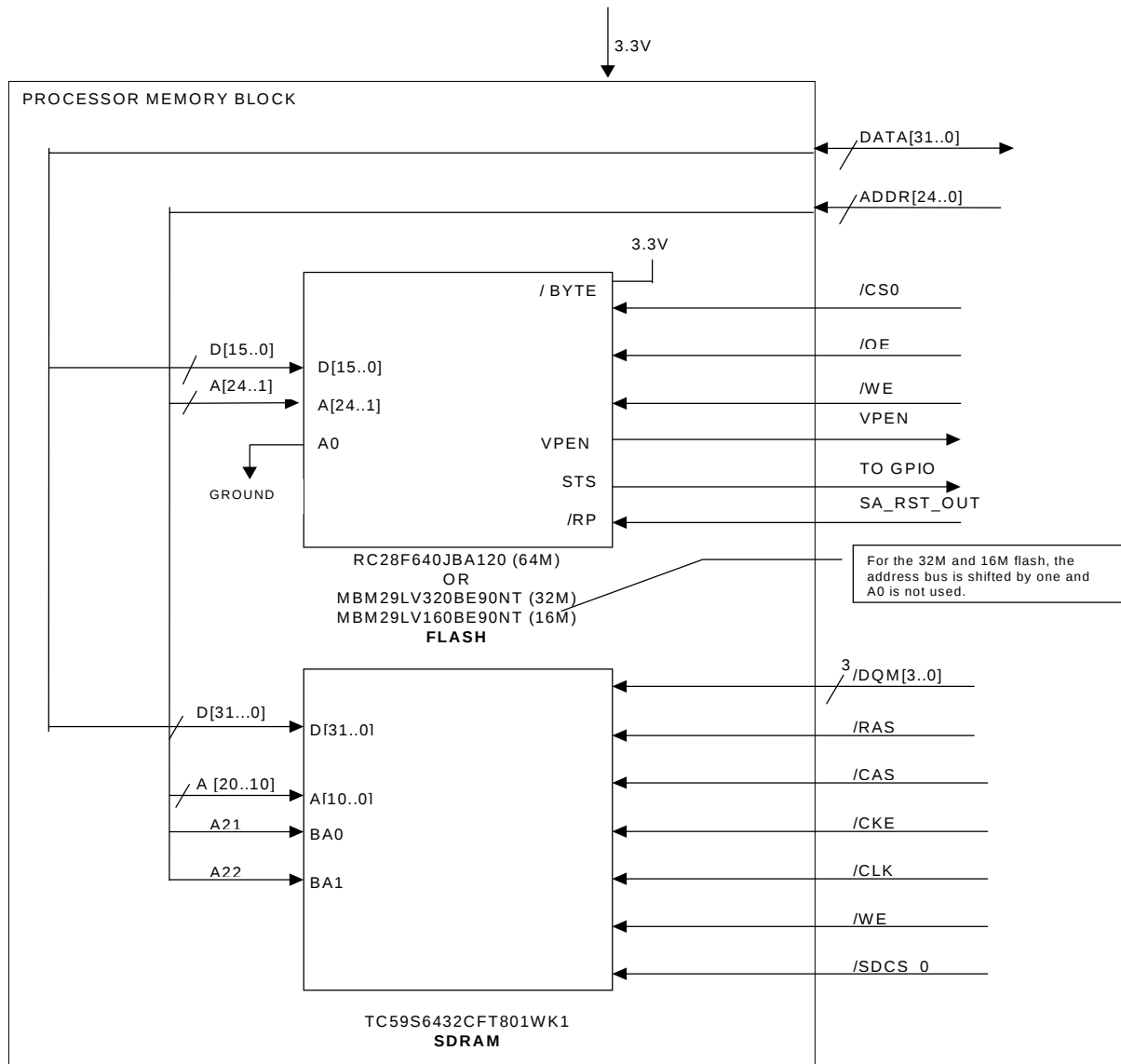
1.3 Digital Section

1.3.1 Ethernet MAC/PHY



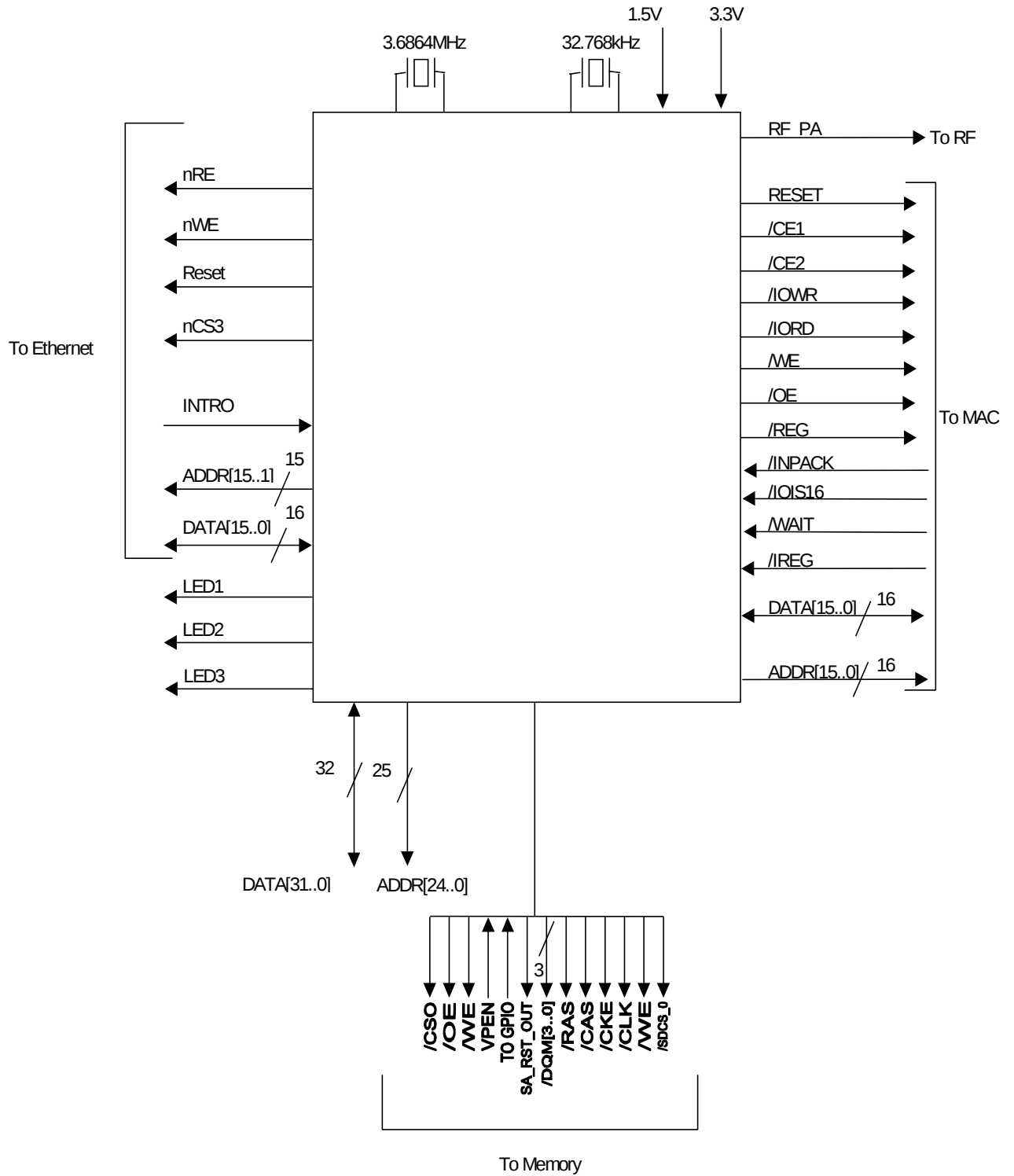
1.3.1 Ethernet MAC/PHY Block Diagram

1.3.2 Microprocessor Memory (FLASH,SDRAM)



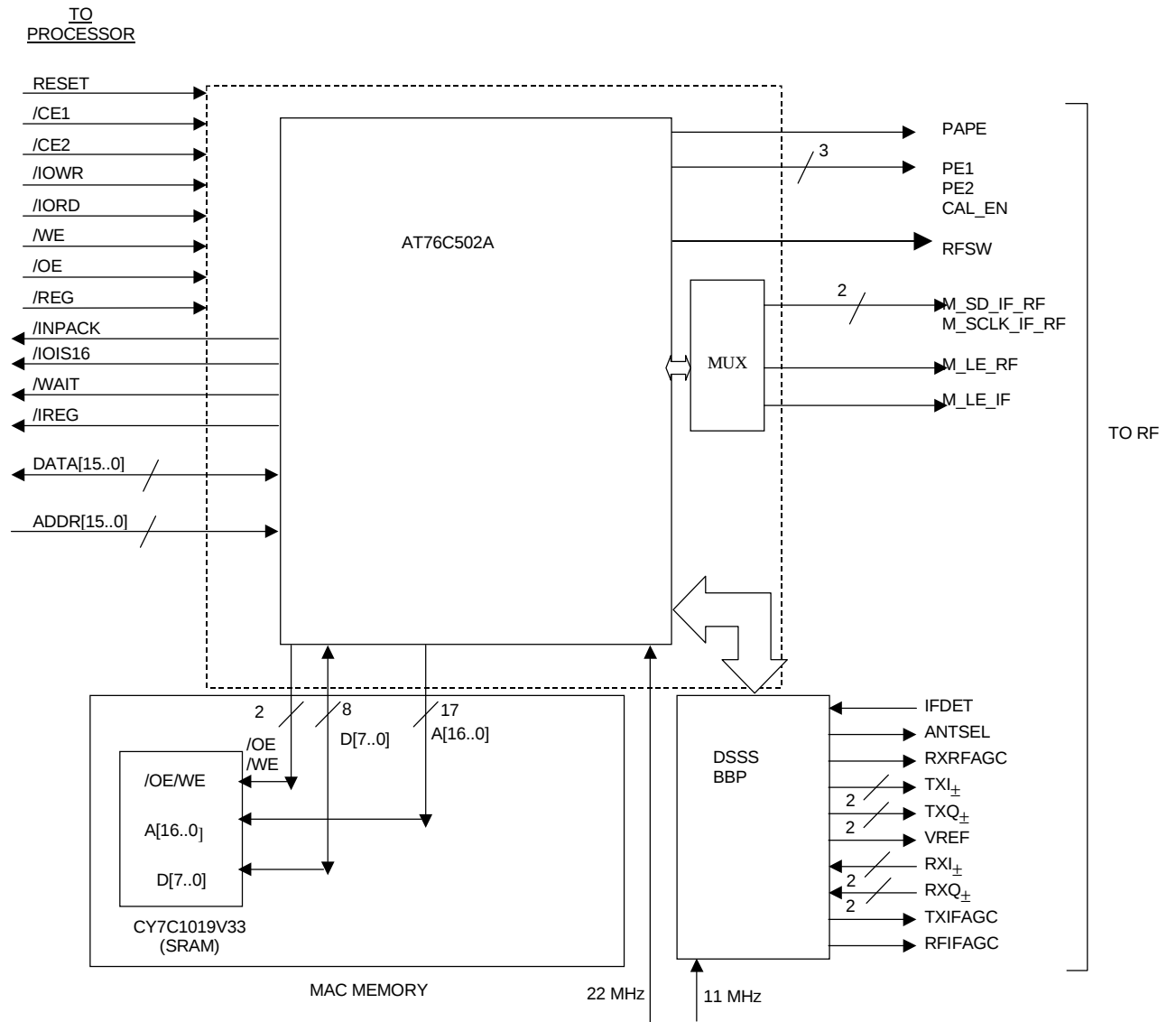
1.3.2 Microprocessor Memory Block Diagram

1.3.3 Microprocessor



1.3.4 MAC Interface

MAC BLOCK - PCMCIA SOCKET 1



1.3.3 MAC Interface Block Diagram