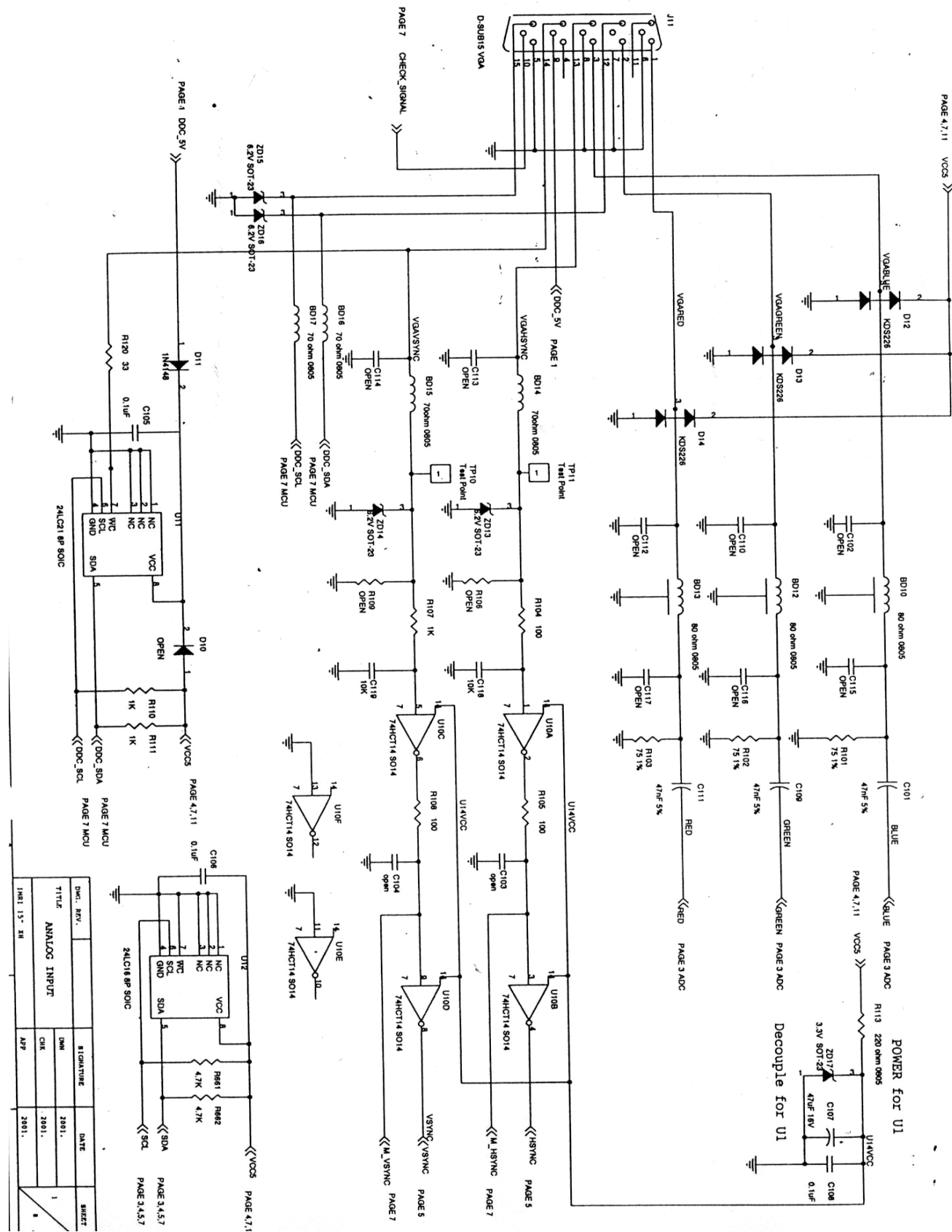
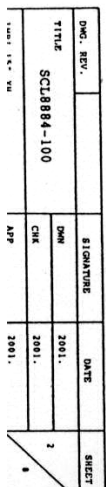
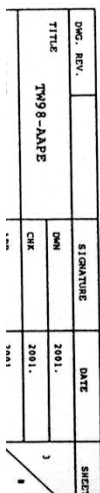
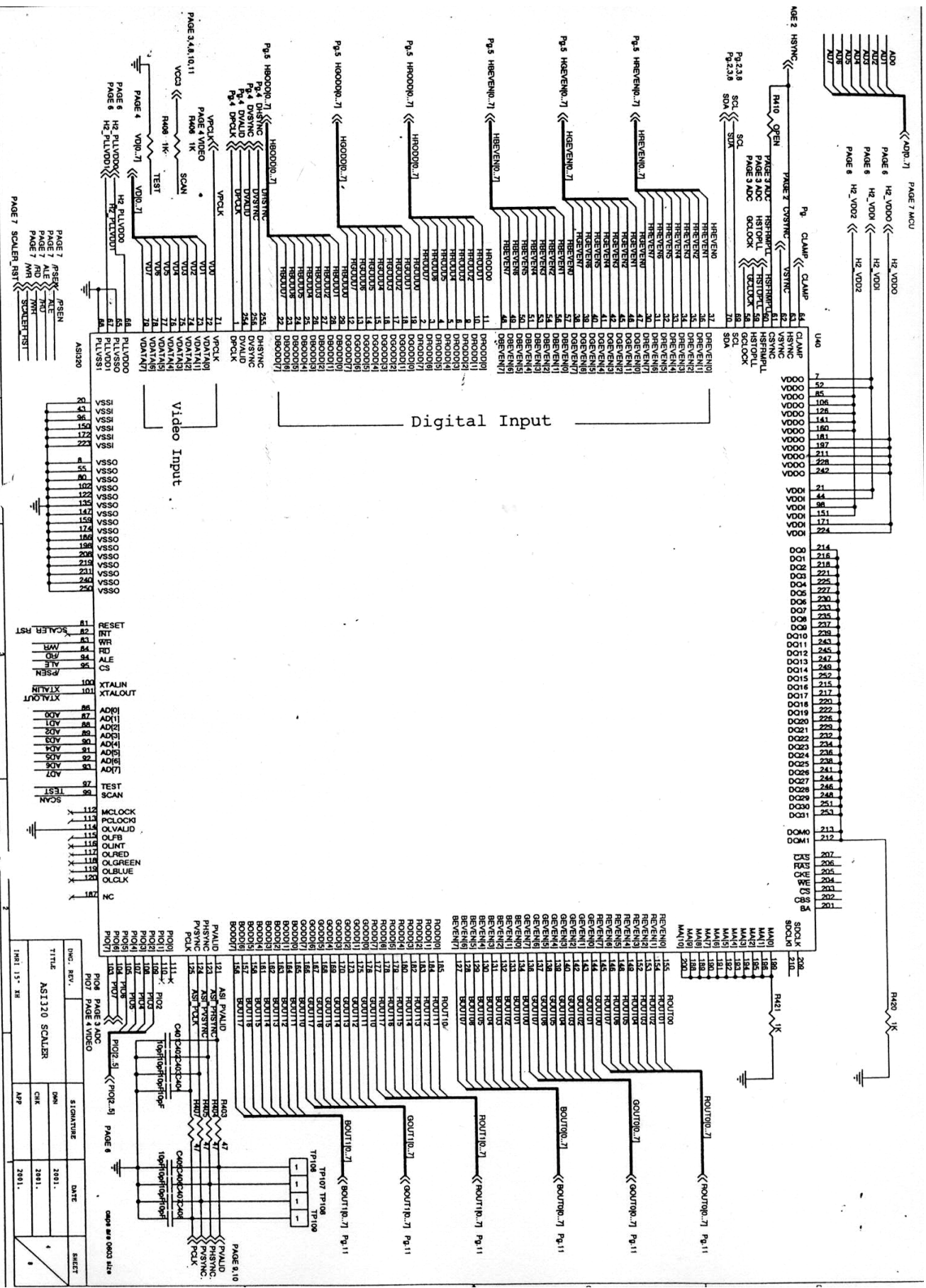




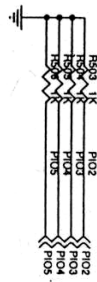
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INR1 15' - 48	APP	2001.





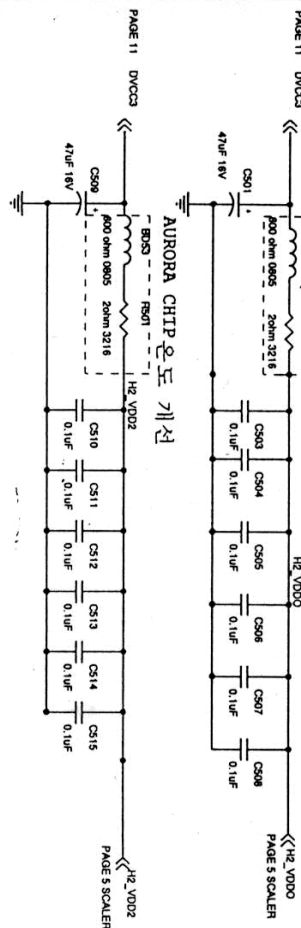


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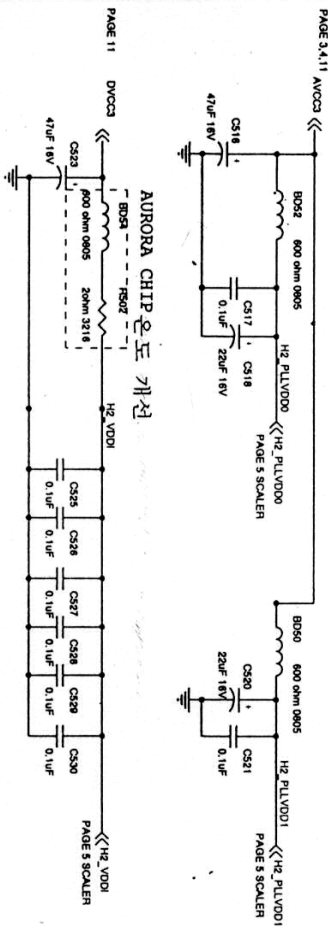


PAGE 9 SCALER

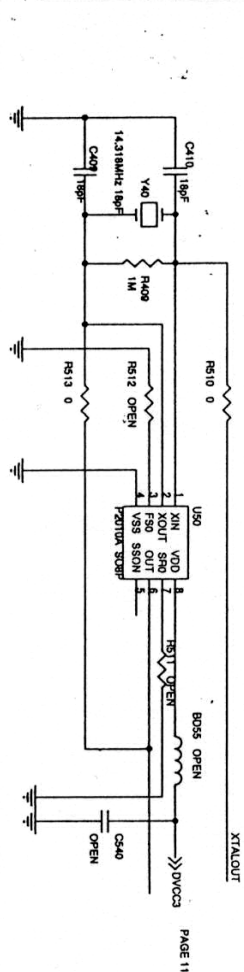
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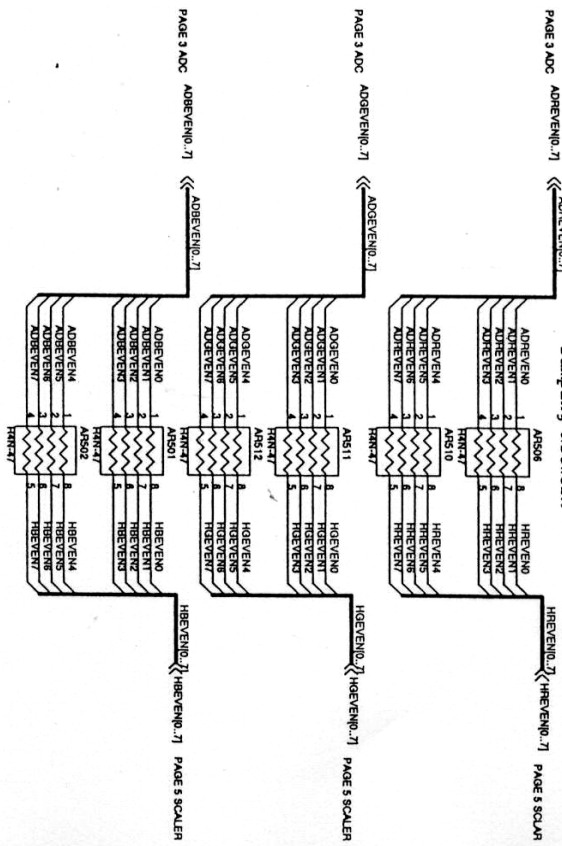
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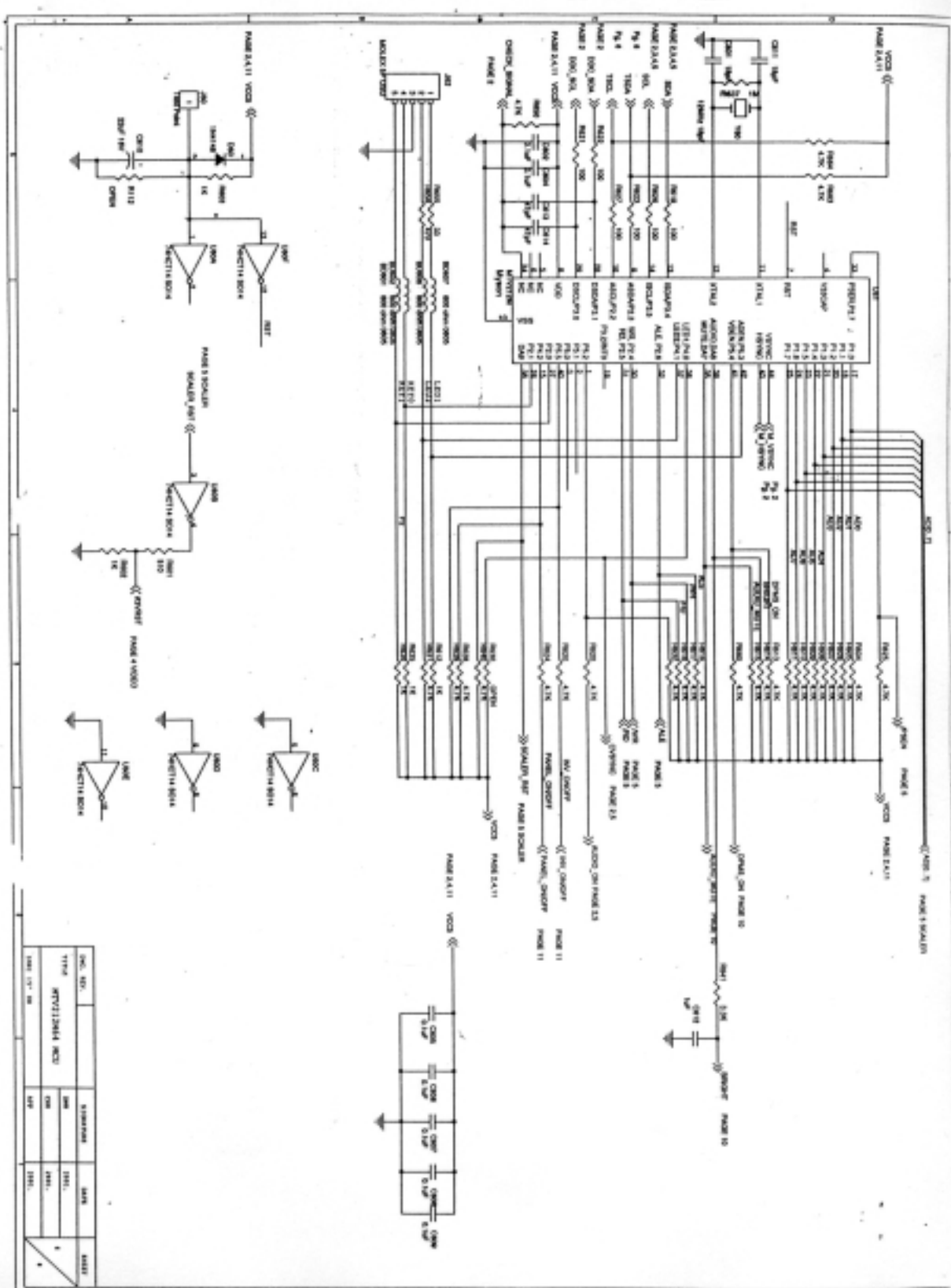
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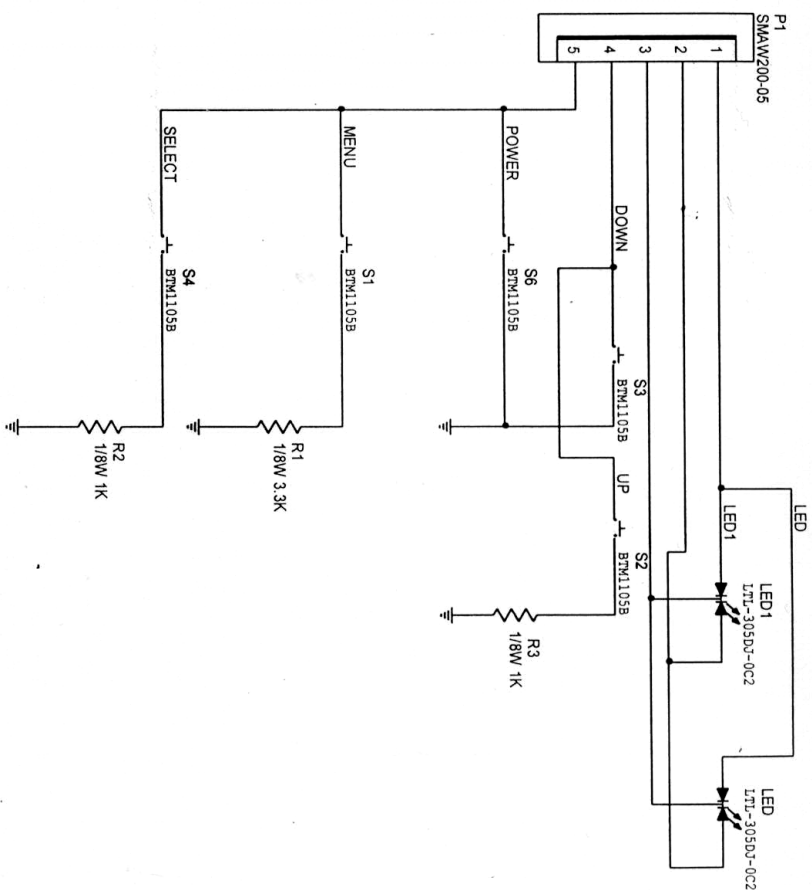
Analog Interface Damping Network



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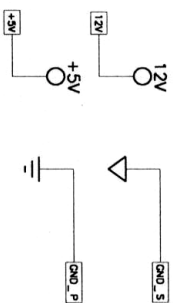


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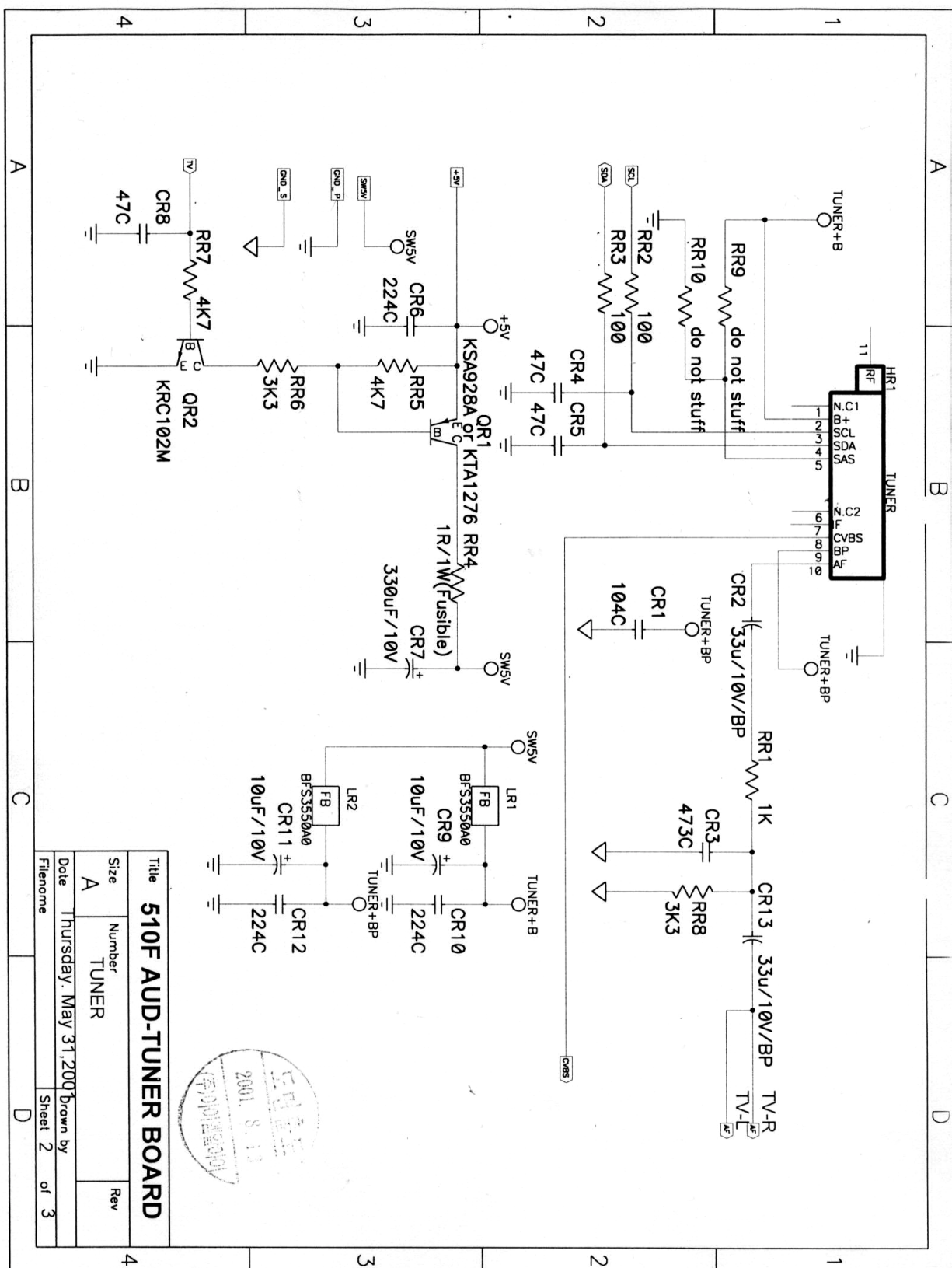
NOTE: 1. RESISTANCE IS SHOWN IN OHM K=1,000 M=1,000,000 RATED POWER OF RESISTOR NOT NOTED IN SCHEMATIC DIAGRAM IS 1/8W SIZE CHIP RESISTOR.
2. CAPACITANCE IS SHOWN μ F AND NOTED CAPACITANCE IS SHOWN μ F, μ F=1,000,000PF RATED VOLTAGE OF CONDENSER NOT NOTED IN SCHEMATIC DIAGRAM IS 50V.
3. THIS SCHEMATIC DIAGRAM IS SUBJECTED TO CHANGE WITHOUT NOTICE FOR FURTHER IMPROVEMENT.

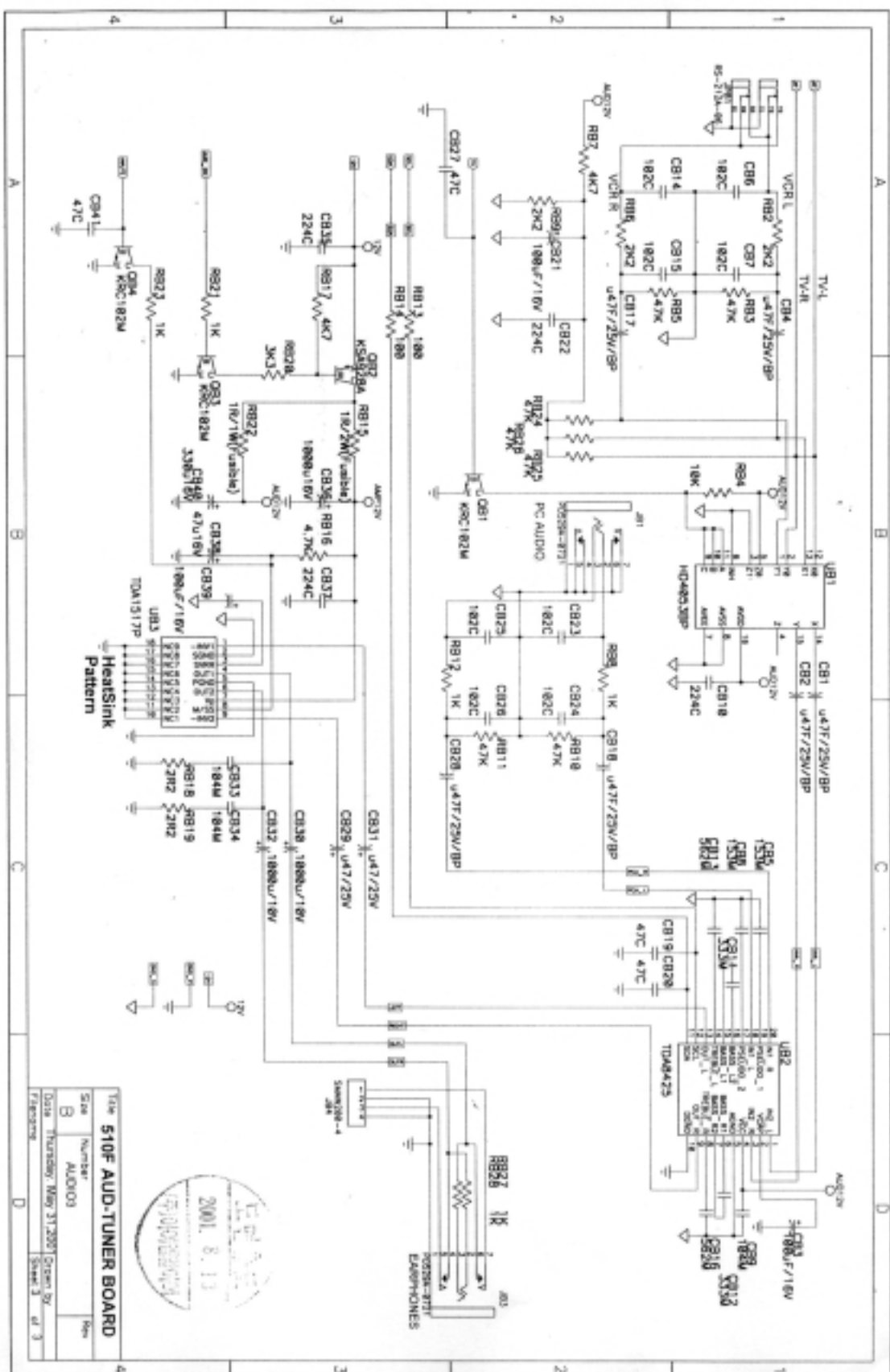
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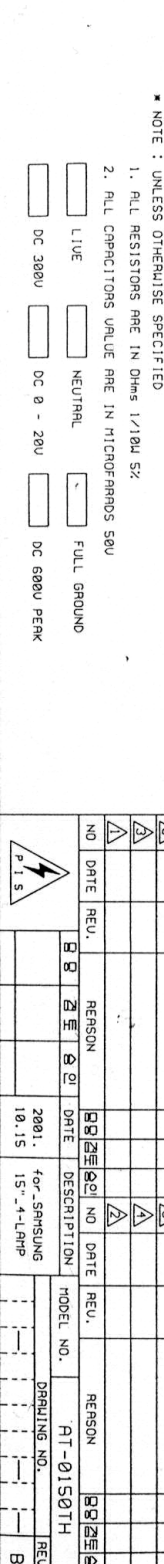


Title 510F AUD-TUNER BOARD		
Size	Number	Rev
A	POWER	
Date Thursday, May 31, 2001		
Filename	Drawn by	
	Sheet 1 of 3	

2001. 8. 13
(주)이디스이디





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