

DRAWING LIST
ETM: FARM ALERT

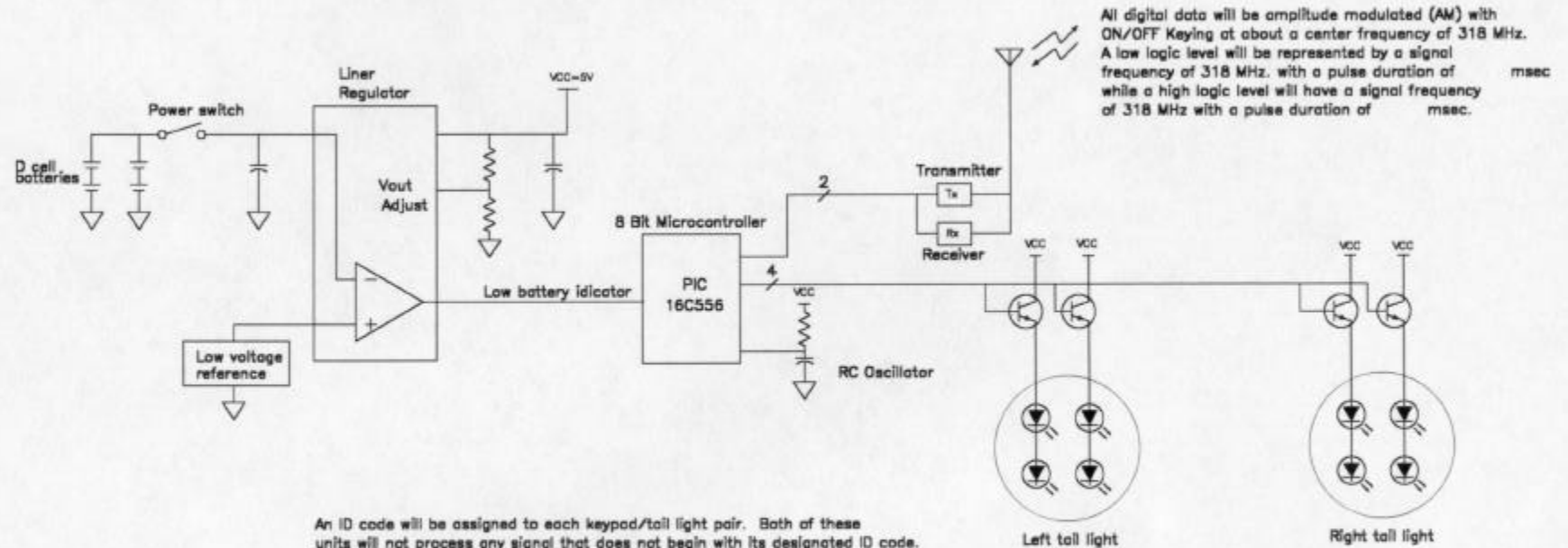
100-1	DRAWING LIST
100-2	REVISION NOTES
100-3	MEMORY MAP
100-4	TAIL LIGHT BLOCK DIAGRAM
100-5	KEYPAD BLOCK DIAGRAM
100-6	POWER SUPPLY
100-7	MICRO CONTROLLER
100-8	LED CIRCUITS
100-9	RF TRANSMITTER
100-10	RF RECEIVER
100-11	RECEIVER INTERFACE
100-12	KEYPAD LAYOUT
100-13	PCB PANEL LAYOUT



TRANSFORMIX ENGINEERING INC.

DESIGNED BY 1. M. Smith 2. R. Zolrzenski 3. 4.	PROJECT Farm Alert
	DRAWING LIST
	DRAWING NUMBER: 100-1
	REV: 2
APPROVAL: R. Zolrzenski	DATE: Wed Apr 14, 1999
	SHEET: 1 OF 13

Tail light Block Diagram

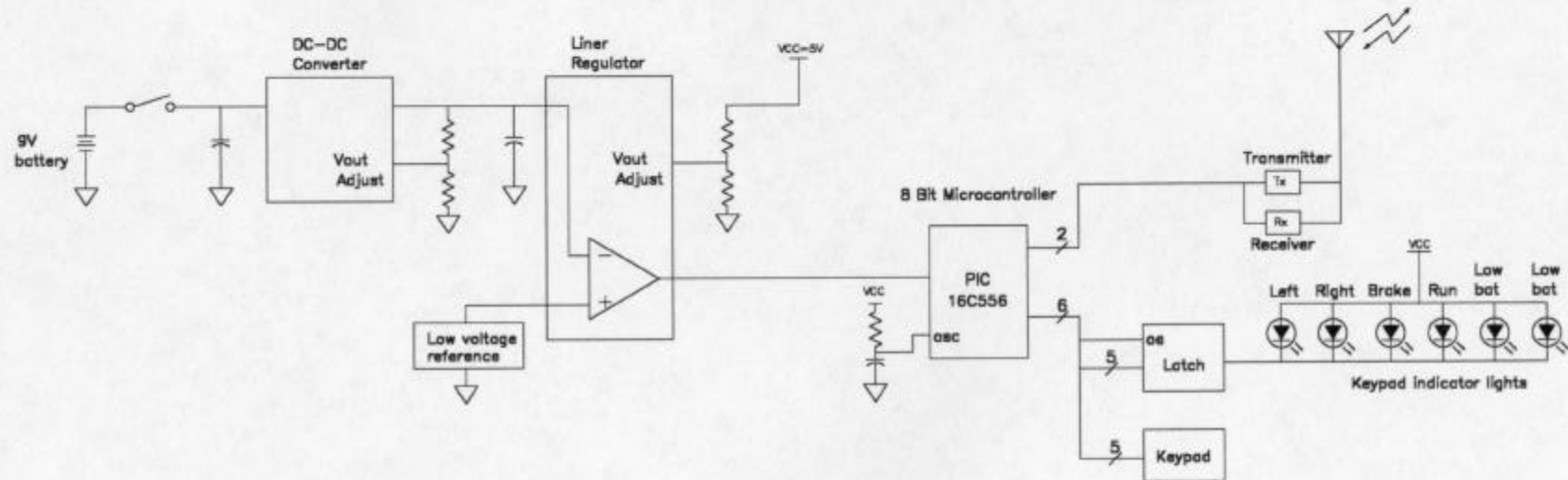


An ID code will be assigned to each keypad/tail light pair. Both of these units will not process any signal that does not begin with its designated ID code. The ID code will be represented by a serial bit stream of 4 bits. This will eliminate the need to transmit at unique frequencies. There will be a total of 5 possible ID codes.

There is a five pulse pre-ambble and a five pulse post-ambble to add energy to the beginning and end of the message.

TRANSFORMIX ENGINEERING INC.			
DESIGNED BY: M.Smith	PROJECT: Farm Alert		
REVIEWED BY: 1. M.Smith	DATE: 100-4	REV: 2	
2. R.Zakrzewski	FILE NAME: Farm Alert R3.sch	REV: B	
3.	DATE: Wed Apr 14, 1999	pages: 4 OF 13	
4.			

Keypad Block Diagram



All digital data will be amplitude modulated (AM) with ON/OFF Keying at about a center frequency of 318 MHz. A low logic level will be represented by a signal frequency of 318 MHz, with a pulse duration of msec while a high logic level will have a signal frequency of 318 MHz with a pulse duration of msec.

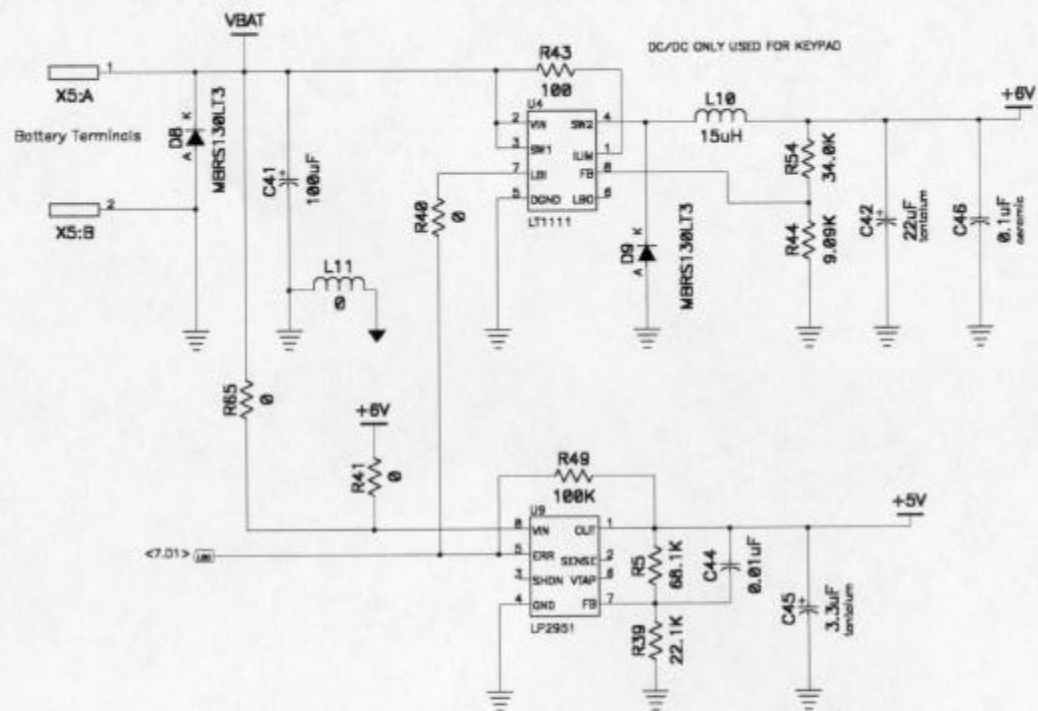
An ID code will be assigned to each keypad/tail light pair. Both of these units will not process any signal that does not begin with its designated ID code. The ID code will be represented by a serial bit stream of 4 bits. This will eliminate the need to transmit at unique frequencies. There will be a total of 5 possible ID codes.

There is a five pulse pre-ambble and a five pulse post-ambble to add energy to the beginning and end of the message.



TRANSFORMIX ENGINEERING INC.

DESIGNED BY	1. M.Smith	PROJECT	Form Alert
2. R.Zakrzewski		DATE	199-5
3.		REV	2
4.		FILE NAME	Form Alert R3.sch
DATE	Wed Apr 14, 1999	SHEET	5 OF 13



Bypass Capacitors

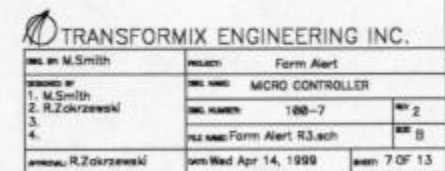


THE OUTPUT VOLTAGE FOR THE REGULATOR IS SET BY THE FOLLOWING FORMULA: $V_{out} = \left(\frac{R5}{R39} + 1 \right) \times 1.235$
 THE OUTPUT VOLTAGE FOR THE DC-DC CONVERTER IS SET BY THE FOLLOWING FORMULA: $V_{out} = \left(\frac{R54}{R44} + 1 \right) \times 1.28$
 NOTES: 1) ALL RESISTANCE VALUES ARE IN OHMS, ALL TOLERANCES ARE 1%, 1/10 WATT (8085 PACKAGES), UNLESS OTHERWISE INDICATED.
 2) ALL CAPACITANCE VALUES ARE IN MICROFARADS, ALL TOLERANCES ARE 10%, (8085 PACKAGES) UNLESS OTHERWISE INDICATED.
 3) ALL LEADS ARE RED UNLESS OTHERWISE INDICATED.

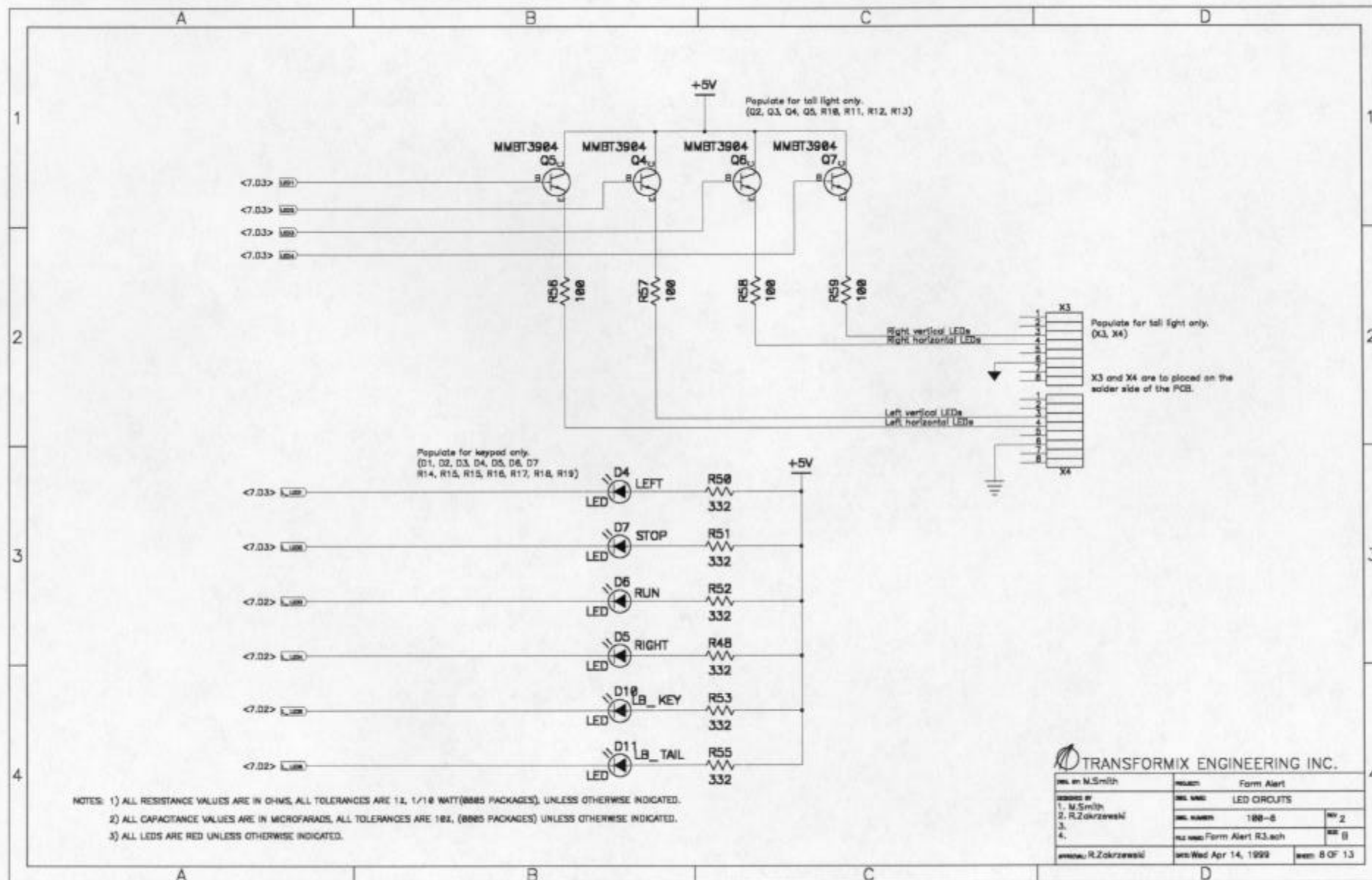


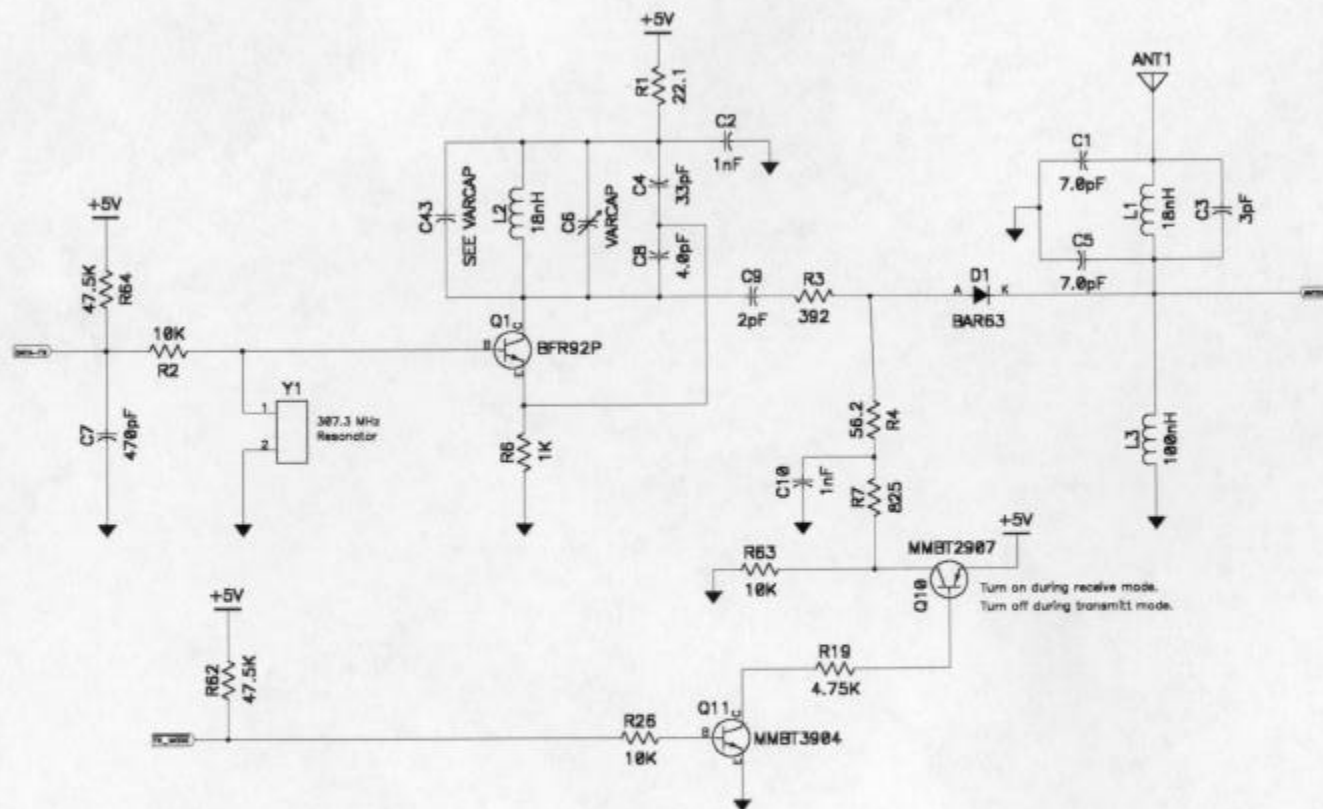
TRANSFORMIX ENGINEERING INC.

DESIGNED BY	DESIGNED BY	FORM ALERT
1. M. Smith	DESIGNED BY	POWER SUPPLY
2. R. Zdzienicki	DESIGNED BY	100-B
3.	DESIGNED BY	Form Alert R3.ach
4.	DESIGNED BY	Form Alert R3.ach
APPROVED: R. Zdzienicki	DATE: Wed Apr 14, 1999	PAGE: 6 OF 13



NOTES: 1) ALL RESISTANCE VALUES ARE IN OHMS, ALL TOLERANCES ARE 1%, 1/10 WATT (8085 PACKAGES), UNLESS OTHERWISE INDICATED.
2) ALL CAPACITANCE VALUES ARE IN MICROFARADS, ALL TOLERANCES ARE 10%, (8085 PACKAGES) UNLESS OTHERWISE INDICATED.
3) ALL LEADS ARE RED UNLESS OTHERWISE INDICATED.



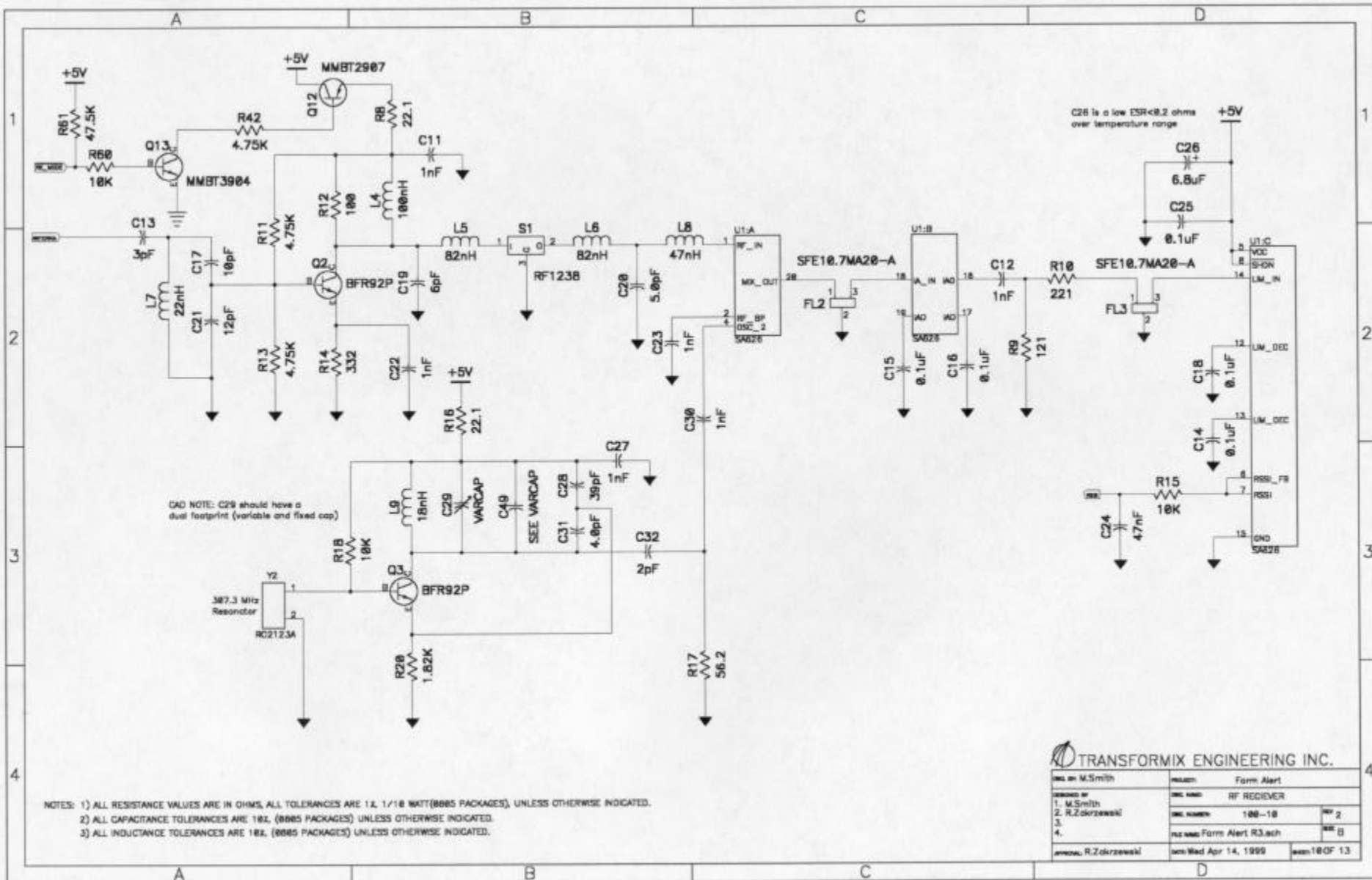


NOTES: 1) ALL RESISTANCE VALUES ARE IN OHMS, ALL TOLERANCES ARE 1%, 1/10 WATT (8885 PACKAGES), UNLESS OTHERWISE INDICATED.
 2) ALL CAPACITANCE TOLERANCES ARE 10%, (8885 PACKAGES) UNLESS OTHERWISE INDICATED.
 3) ALL INDUCTANCE TOLERANCES ARE 10%, (8885 PACKAGES) UNLESS OTHERWISE INDICATED.



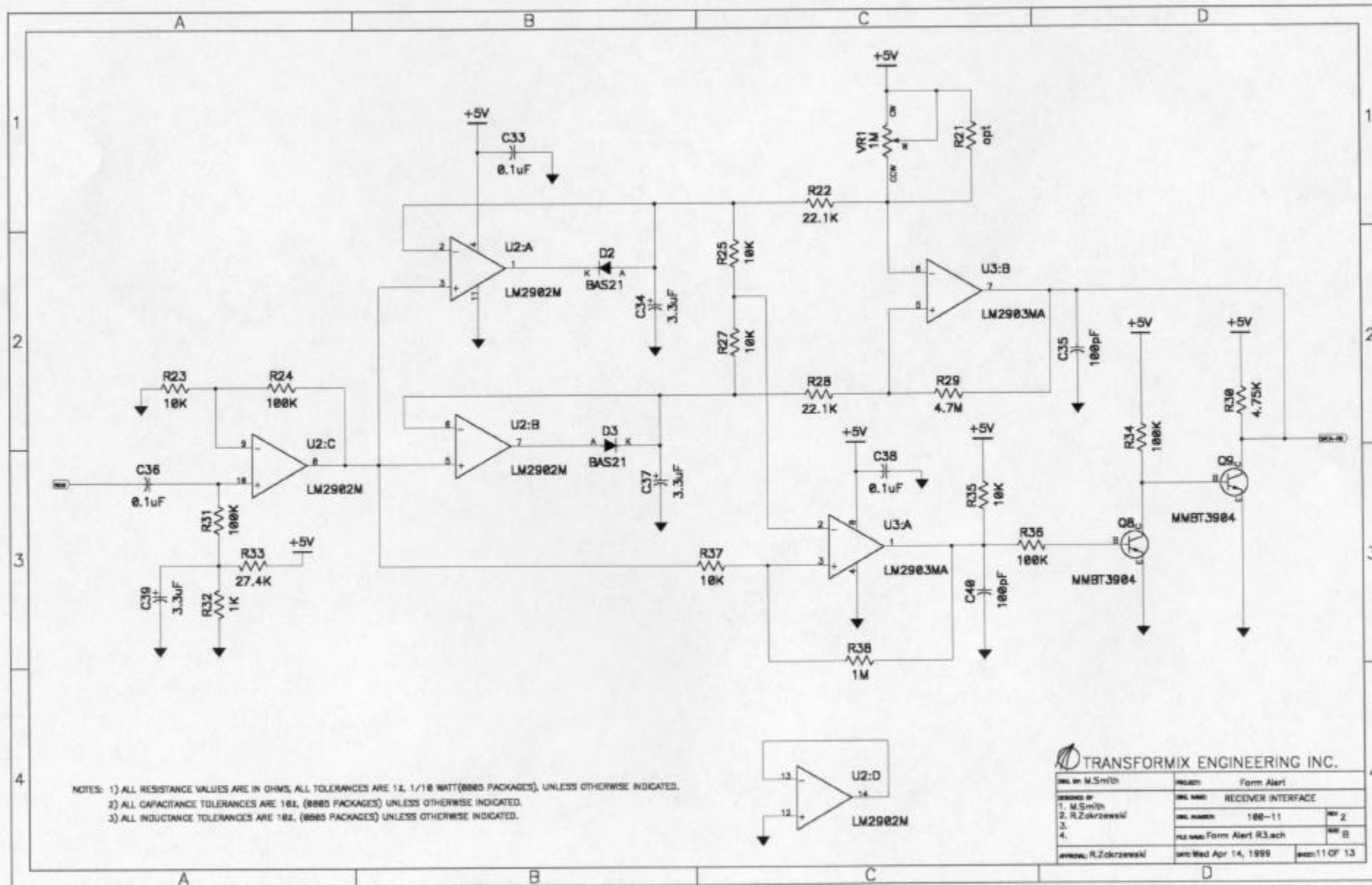
TRANSFORMIX ENGINEERING INC.

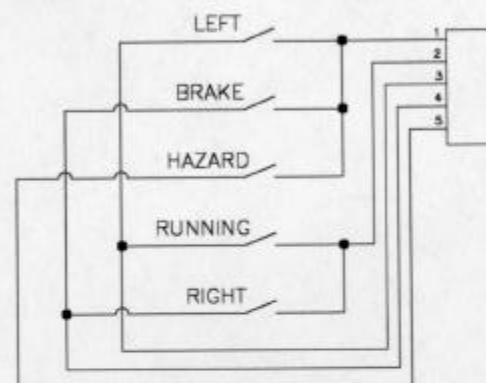
DESIGNED BY 1. M.Smith	REVISION Form Alert
DRAWN BY 2. R.Zakrzewski	RF TRANSMITTER
CHECKED BY 3.	100-9
DATE 4.	Form Alert R3.sch
DATE Wed Apr 14, 1999	9 OF 13



TRANSFORMIX ENGINEERING INC.

DESIGNED BY 1. M.Smith 2. R.Zakrzewski 3. 4.	PROJECT Form Alert RF RECEIVER 100-10 Form Alert R3.ach REV 2 REV B	APPROVAL R.Zakrzewski DATE Wed Apr 14, 1999 PAGE 18 OF 13
--	---	--





Notes: 1) Use 200g Trip Force Domes, 12.2mm Round, P/N F12200
 2) Supplier: Continental Decal

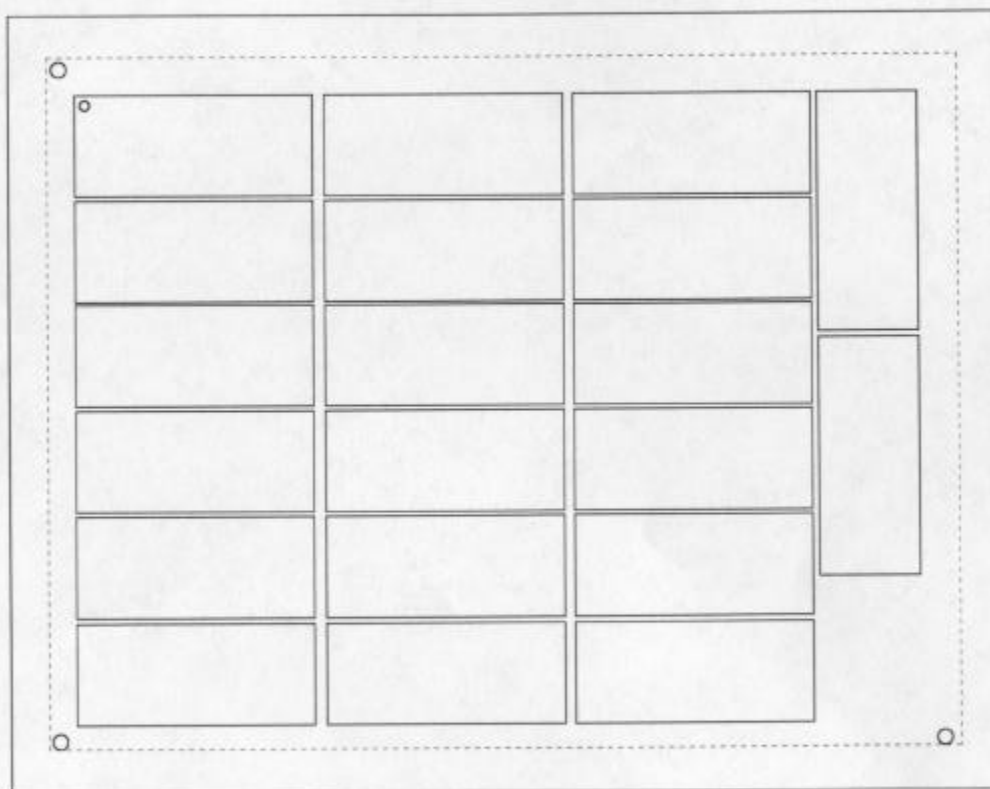


TRANSFORMIX ENGINEERING INC.

DESIGNED BY	PROJECT	Form Alert
1. M.Smith	KEYPAD LAYOUT	
2. R.Zakrzewski	100-12	REV 2
3.	Form Alert R3.ach	REV B
4.		
APPROVAL: R.Zakrzewski	DATE: Wed Apr 14, 1999	Sheet: 12 OF 13

PCB PANEL LAYOUT

FOR A PANEL SIZE OF 24" X 18"



LEGEND

- O Fudicial
- Clearance

NOTES: 1) THERE IS ONE FUDICIAL PER PCB
 2) THERE ARE THREE FUDICIALS ON THE OUTSIDE OF THE PANEL ITSELF.
 3) THERE SHOULD BE A 1" CLEARANCE AROUND THE PANEL.

SCALE = 3 : 1

 TRANSFORMIX ENGINEERING INC.

DESIGNED BY 1. M. Smith	PROJECT Farm Alert
DRAWN BY 2. R. Zakrzewski	DRAWN BY 100-13
CHECKED BY 3.	FILE NAME Farm Alert R3.sch
DATE 4.	DATE Wed Apr 14, 1999
APPROVED BY R. Zakrzewski	DATE Wed Apr 14, 1999

Page 13 OF 13