

FCC ID: OJJ75210

Technical Description :

The brief circuit description is listed as follows :

- U1 SLA201 and associated circuit act as MCU.
- Q2 and associated circuit act as Motor Controller for M1.
- Q3 and associated circuit act as Motor Controller for M2.
- LY, LX, RX, RY, S1 and associated circuit act as Axis Controllers.
- L1, L2, L3, DU, DL, DR, DD, R1, R2, R3, BU, BL, BR, BD, Start, Select and Mode and associated circuit act as Control Keys.

MASK ROM Based 8-bit CMOS Microcontroller

1. GENERAL DESCRIPTION

The SLA 201 is an 8-bit microprocessor designed and developed with low-power and high speed CMOS technology. Its operation kernel is implemented with RISC-like architecture. And it is available in the mask ROM version. This device is equipped with the Serial Peripheral Interface (SPI) function and an easy-implemented RS-232. The SLA 201 is very suitable for the wired communication. Only 58 easy-to-learn instructions are needed.

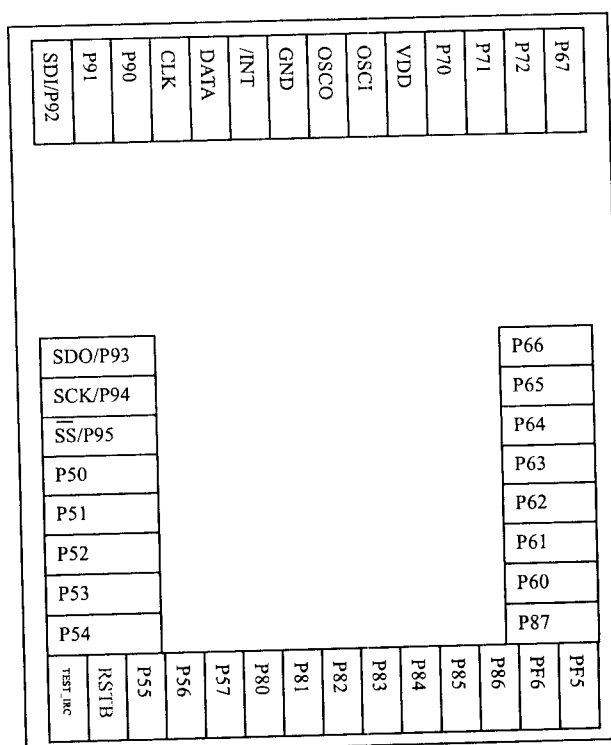
2. FEATURES

- Operating voltage range: 2.3V~5.5V.
- Operating temperature range: 0°C~70°C.
- Operating frequency rang (base on 2 clocks):
- Crystal mode: DC~20MHz at 5V, DC~10MHz at 3.3V.
 - Built-in RC oscillator with external serial resistor(R=43Kohm suggested), $\pm 10\%$ variation. (f = 10MHz at 3.3V)
- Low power consumption:
 - Less than 3 mA at 5V/4MHz
 - Typically 10 uA during sleep mode
- Serial Peripheral Interface (SPI) available.
- 4K $\times$ 14 bits on chip ROM.
- 11 special function registers.
- 140 $\times$ 8 bits are on chip general-purposed registers.
- 5 bi-directional I/O ports (35 I/O pins).
- 2 LED direct sinking pins with internal serial resistors.
- Built-in power-on reset.
 - Also it supports external power-on reset.
- Five stacks for subroutine nesting.
- 8-bit real time clock/counter (TCC) with overflow interrupt.
- Two machine clocks or four machine clocks per instruction cycle.
- Power down mode.
- Programmable wake up from sleep circuit on I/O ports.
- Programmable free running on-chip watchdog timer.
- 12 wake-up pins.
- 2 open-drain pins.
- 2 R-option pins.
- 32 programmable pull-high input pins.
- Package types:
 - 44 pin QFP.

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- Four types of interrupts.
 - External interrupt (/INT).
 - SPI transmission completed interrupt.
 - TCC overflow interrupt.
 - Timer1 comparator matches interrupt.

3. PIN ASSIGNMENT



Pad number/ Pad name/Coordinate

1. P92	X=83.5,	Y=1904.575	12. P55	X=285.5,	Y=78
2. P93	X=78,	Y=994.85	13. P56	X=386.5,	Y=78
3. P94	X=78,	Y=893.85	14. P57	X=487.5,	Y=78
4. P95	X=78,	Y=792.85	15. P80	X=588.5,	Y=78
5. P50	X=78,	Y=691.85	16. P81	X=689.5,	Y=78
6. P51	X=78,	Y=590.85	17. P82	X=790.5,	Y=78
7. P52	X=78,	Y=489.85	18. P83	X=891.5,	Y=78
8. P53	X=78,	Y=388.85	19. P84	X=992.5,	Y=78
9. P54	X=78,	Y=287.85	20. P85	X=1093.5,	Y=78
10. TEST_IRC	X=83.5,	Y=78	21. P86	X=1194.5,	Y=78
11. PF7	X=184.5,	Y=78	22. PF6	X=1295.5,	Y=78