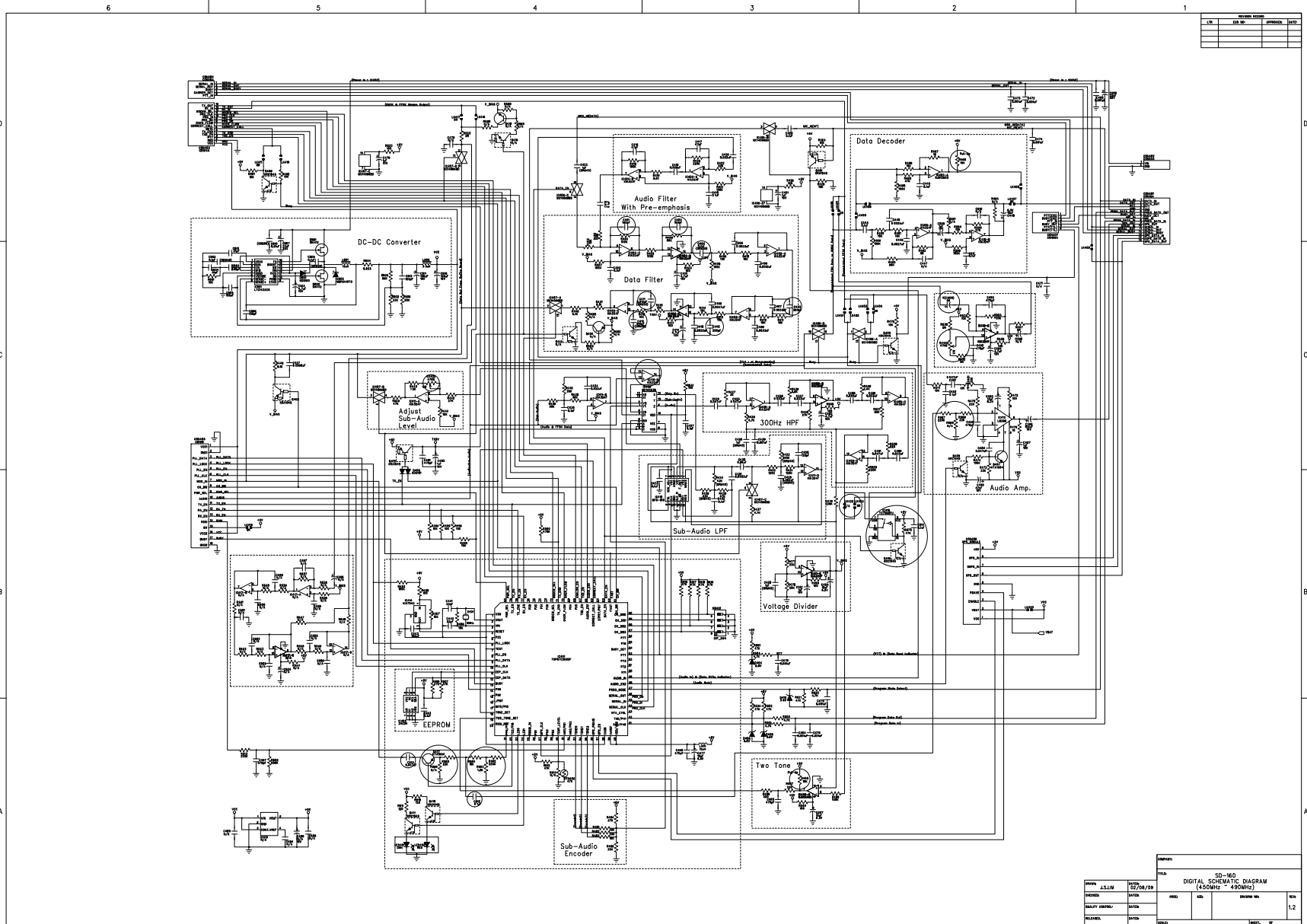




REVISION HISTORY			
NO.	REV.	DATE	REASON
1	1.0	10/08/99	Initial

Title: SD-MO DIGITAL SCHEMATIC DIAGRAM (450MHz ~ 490MHz)			
Date: 10/08/99	Author: JAL/JW	Checked: JAL/JW	Drawn: JAL/JW
Rev: 1.2	Rev: 1.2	Rev: 1.2	Rev: 1.2

P.A MODULE

