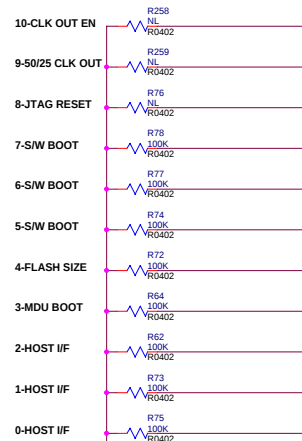


RESET CONFIGURATION BITS[10:0]



Flash size - 16 bits
MDU boot - from Flash
Host I/F 2.0
100 -> UART, NO HOST INTF

OPTIONAL pulldown resistors for embedded applications

CRITICAL: PLACE R7 CLOSE TO PIN

This R-C time constant provides about 200 ms holdoff for start of access of ARM instructions from external FLASH. Allows time for FLASH to initialize.

Make TXI_P, TXI_N, TXQ_P and TXQ_N all the same length

Make RXI_P, RXI_N, RXQ_P and RXQ_N all the same length

OPTIONAL JTAG interface

Signal Pullup Resistors are 10 kOhm
Signal Pulldown resistor is 100 kOhm
Isolation resistor R88 is 1 kOhm

88W8515

OPTIONAL Software Configuration

GPIO10----AP MODE
GPIO11----CLIENT MODE
GPIO12----RT MODE

Source Series Termination Resistors
Keep these resistors as close to 88W8515 as possible

CRITICAL: PLACE R59 CLOSE TO PIN

CRITICAL: Route Ethernet Signals Away From Clock Lines and 88W8000G control lines

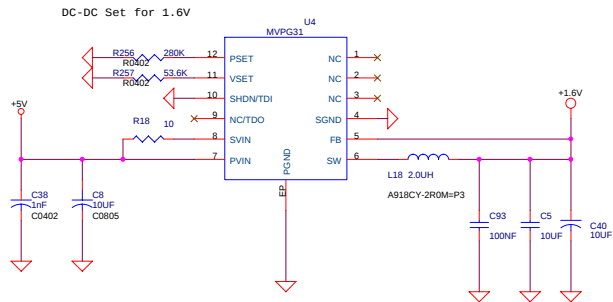
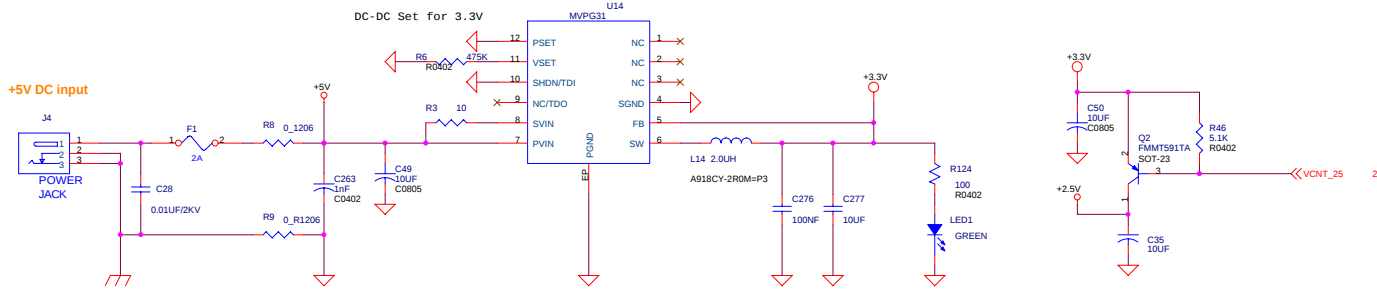
Make FERX_P and FERX_N the same routed length
Make FETX_P and FETX_N the same routed length

CRITICAL: ROUTE 88W8000G CTRL SIGNALS AWAY FROM CLOCK LINES AND OTHER NOISE SOURCES



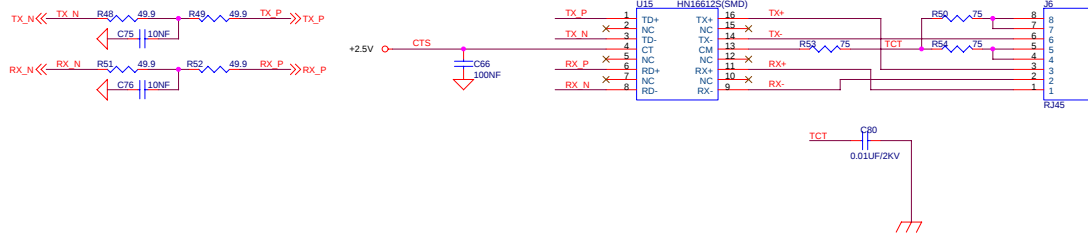
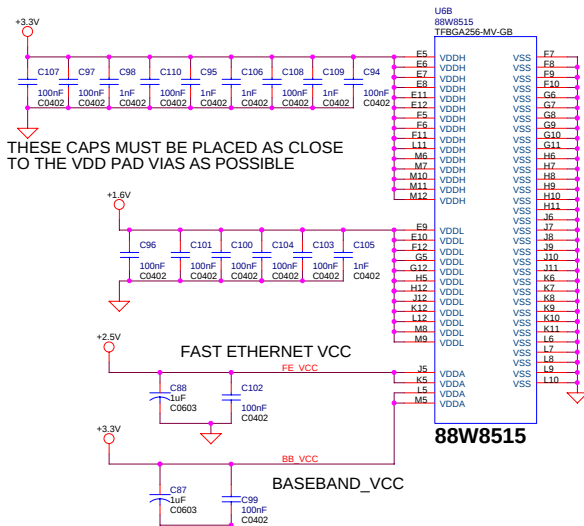
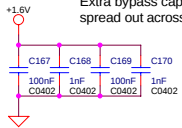
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Power Regulator



BYPASS CAPACITORS

Extra bypass caps for EMI control spread out across the board



8500 BGA FIDUCIALS

BOTTOM SIDE
FIDUCIALS

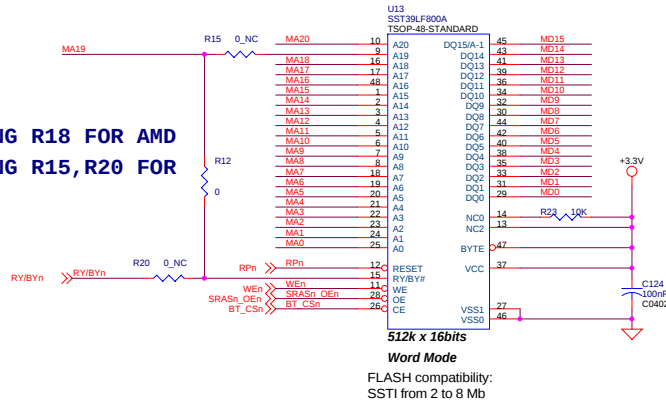
Mounting Holes

Tooling Holes
0.126 inch, non-plated

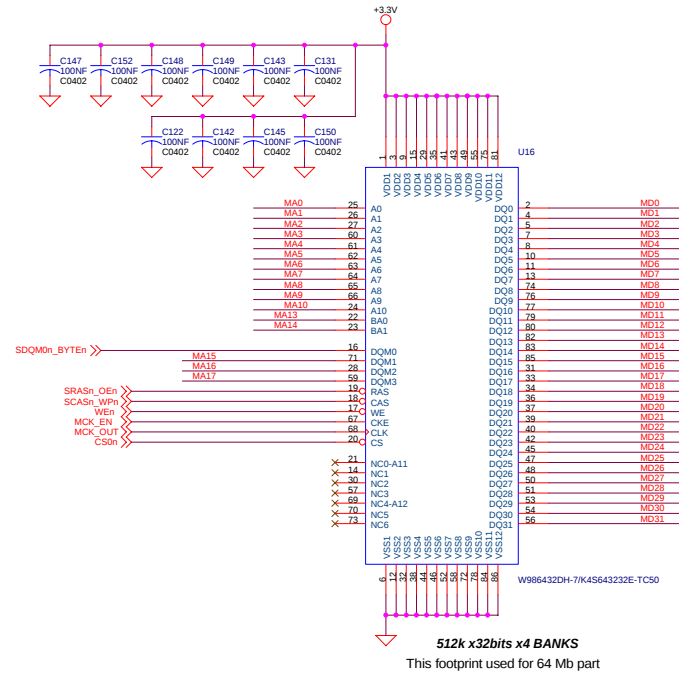


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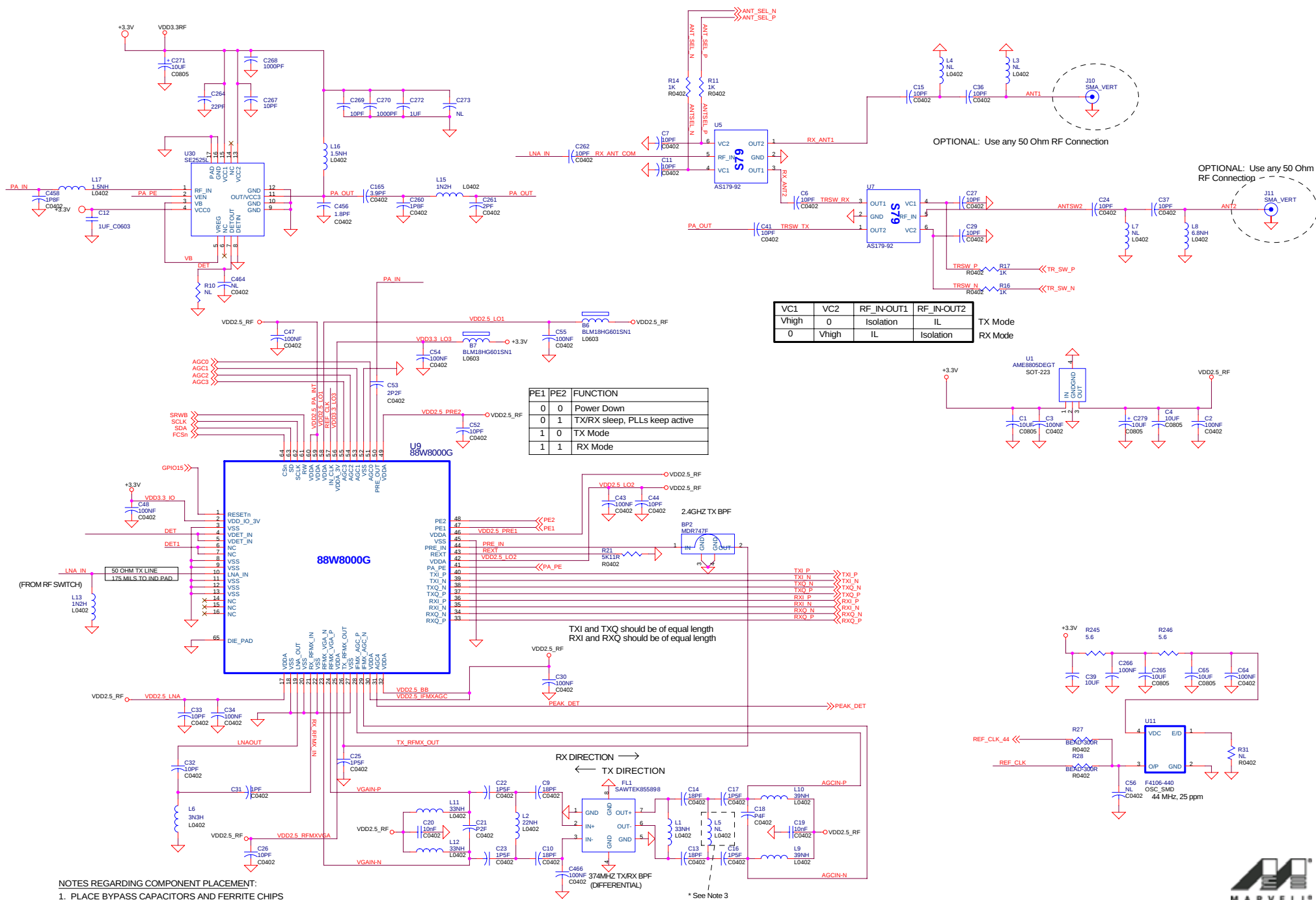
NON-MOUNTING R18 FOR AMD
NON-MOUNTING R15,R20 FOR INTEL



SDRAM MEMORY



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NOTES REGARDING COMPONENT PLACEMENT:

1. PLACE BYPASS CAPACITORS AND FERRITE CHIPS AS CLOSE TO THE POWER PINS AS POSSIBLE.

2. PLACE THE REXT CHIP RESISTOR AS CLOSE TO PIN 43 AS POSSIBLE.

3. L5 USED WITH ALTERNATIVE (LG-INNOTEK) SAW FILTER ONLY.
(See alternate SAW filter datasheet for information on alternate component values for L2; L9; C10; L1; C13; C14).

* See Note 3



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