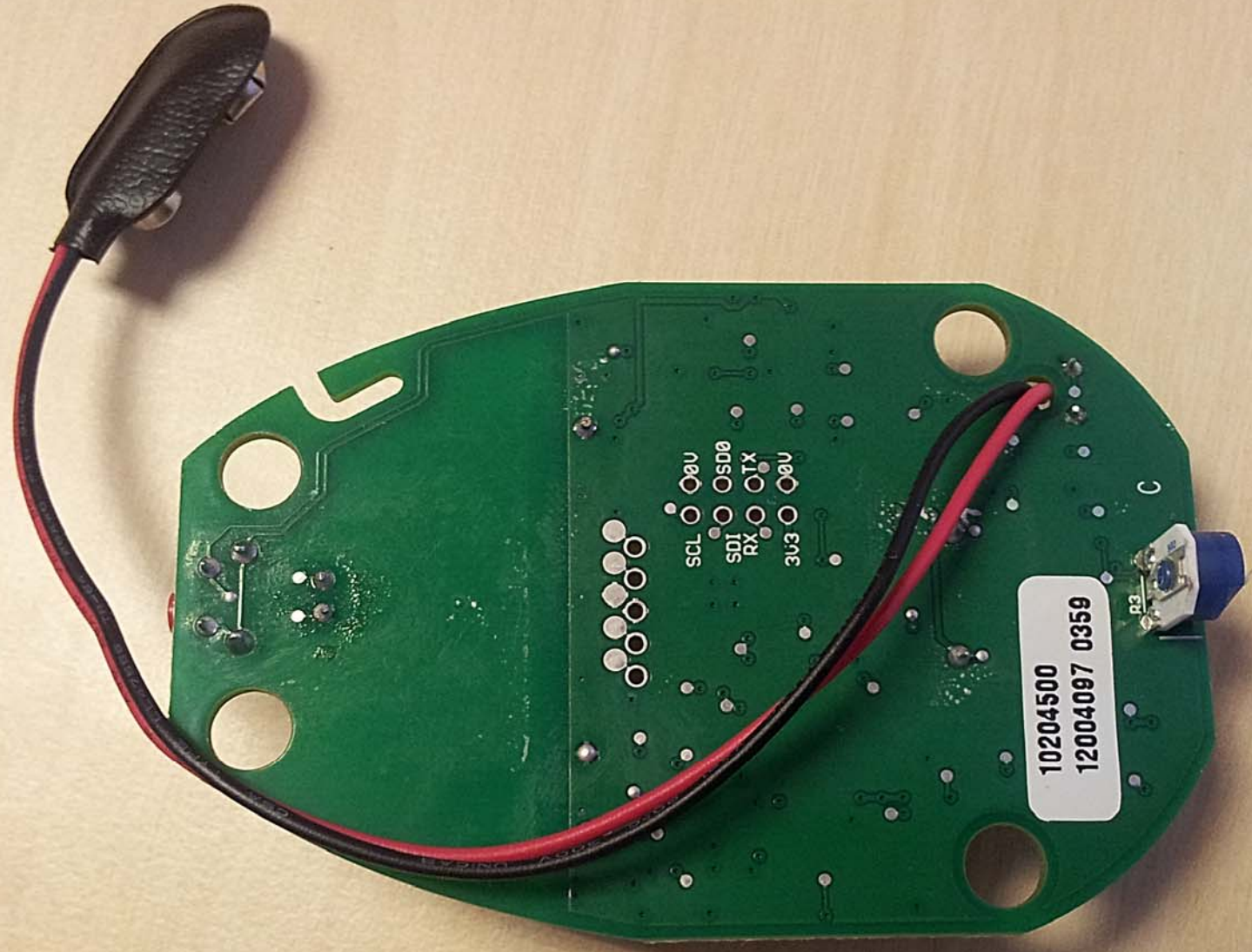




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SCL ○ ○00U
SDI ○ ○SD0
RX ○ ○TX
3V3 ○ ○00U