



N78(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N78(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N78(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N78(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

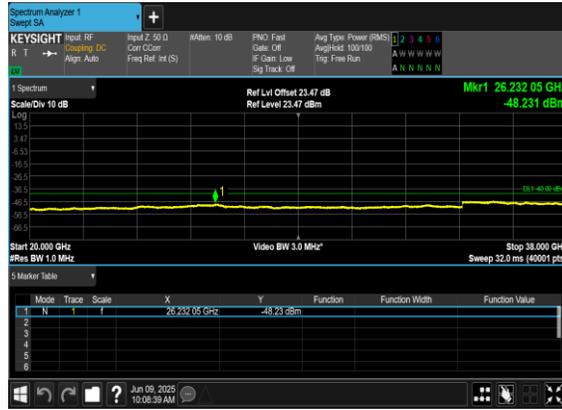




N78(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

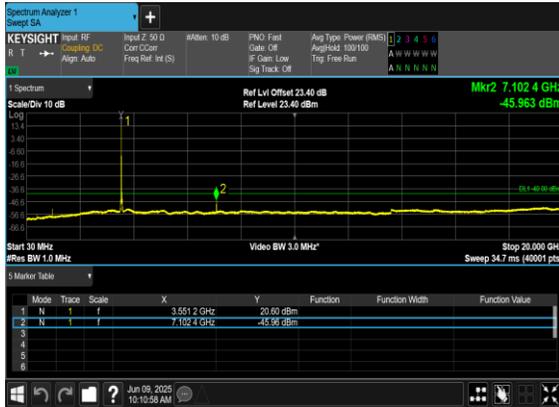


N78(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N78(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

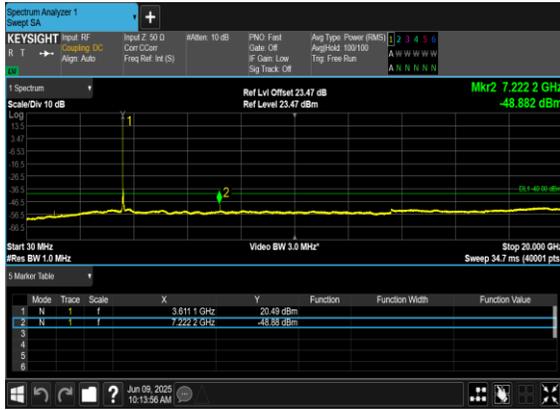


N78(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

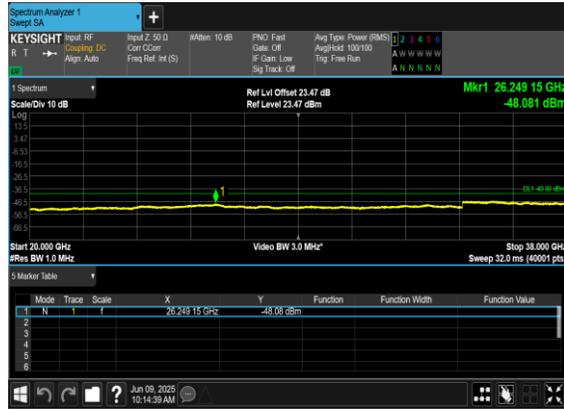




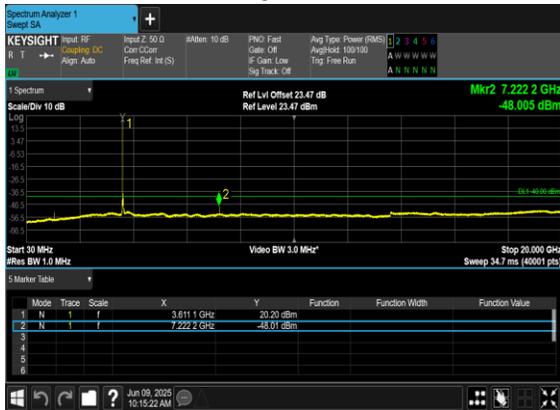
N78(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N78(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





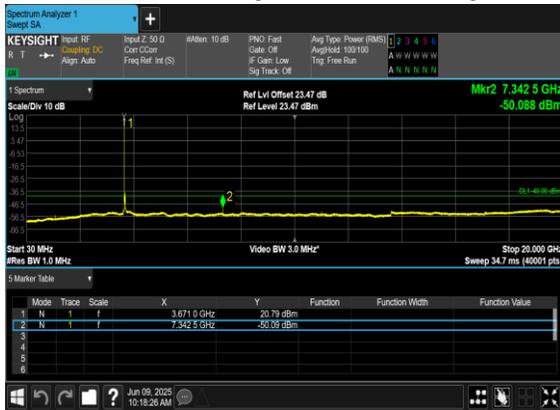
N78(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N78(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N78(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

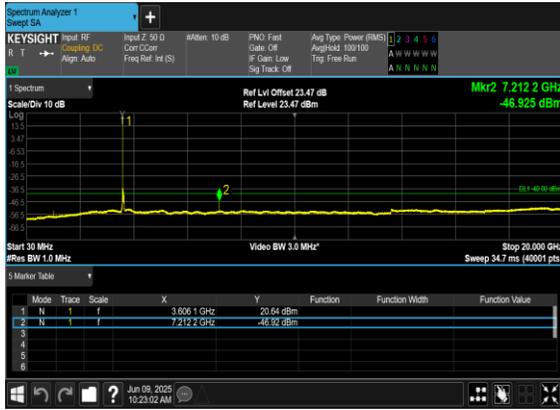


N78(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

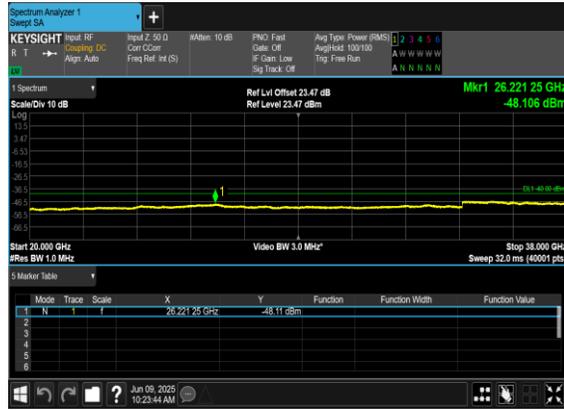




N78(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

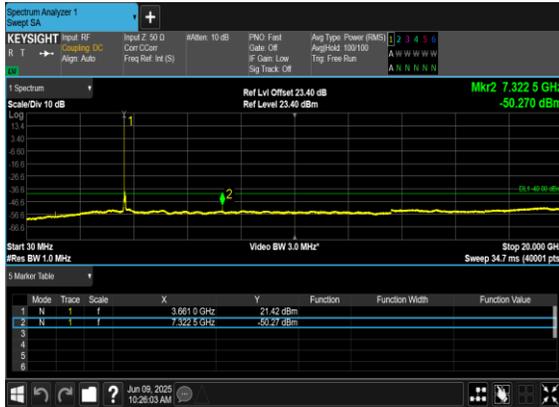


N78(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





N78(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N78(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N78(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N78(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N78(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



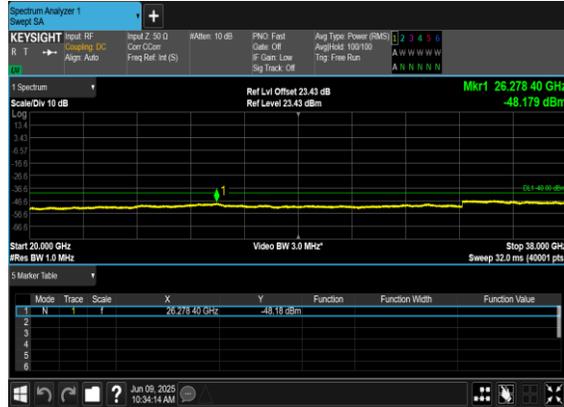
N78(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N78(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

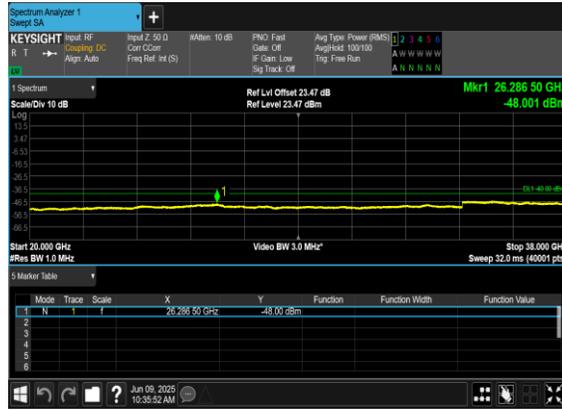




N78(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(50M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

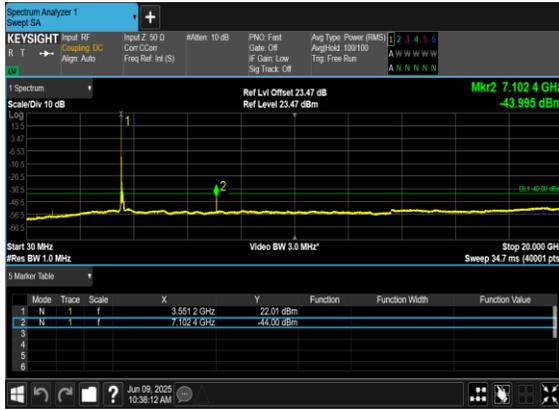


N78(50M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

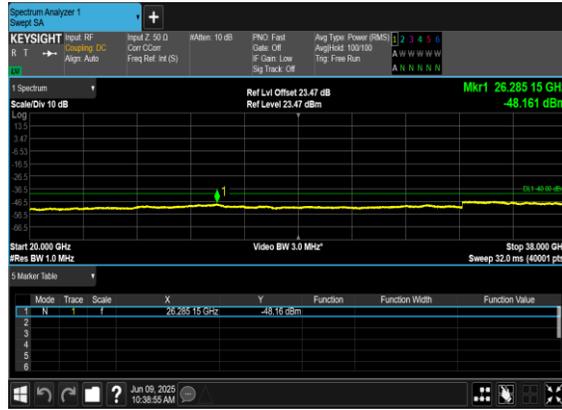




N78(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

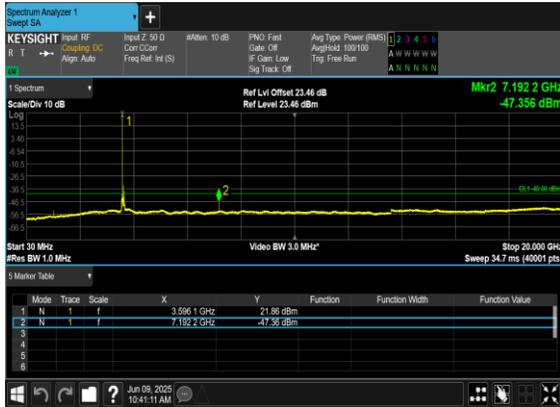


N78(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N78(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



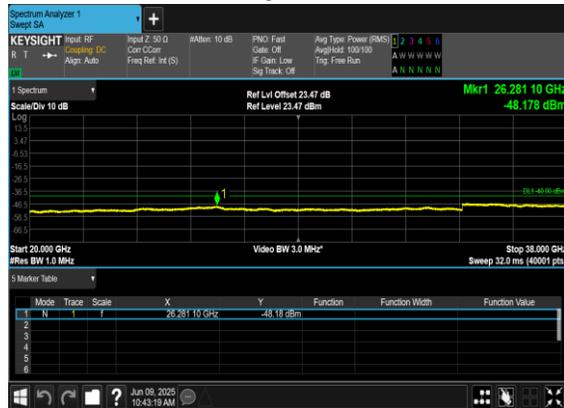
N78(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

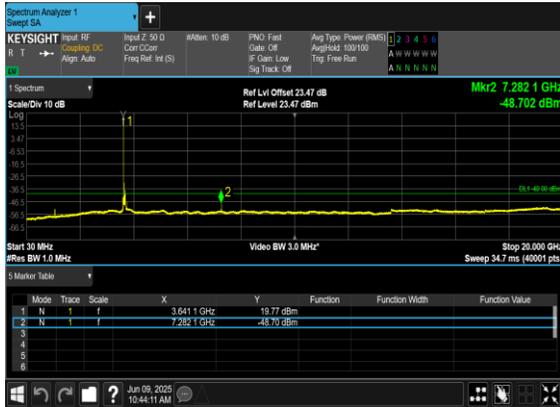


N78(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





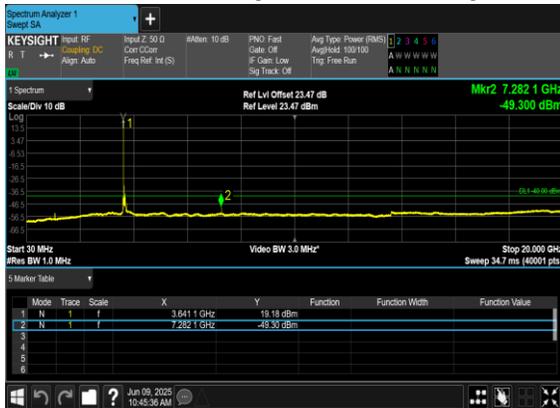
N78(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



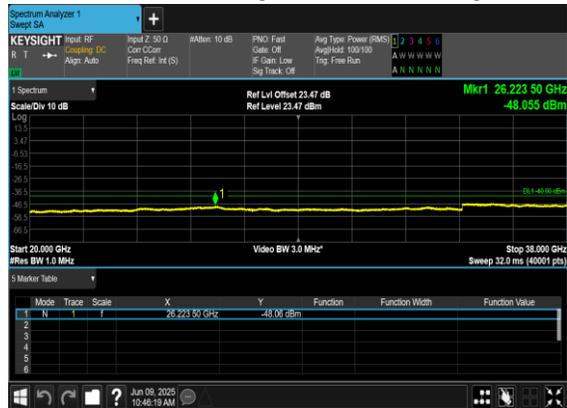
N78(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

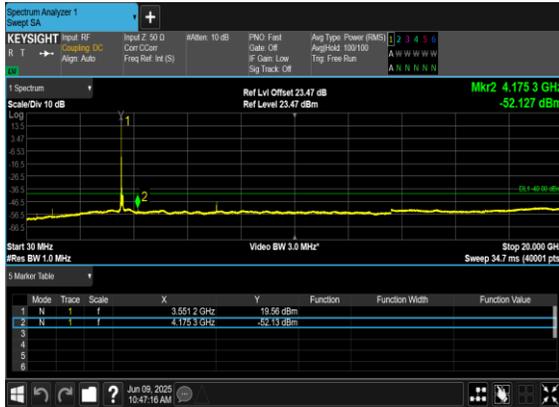


N78(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N78(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

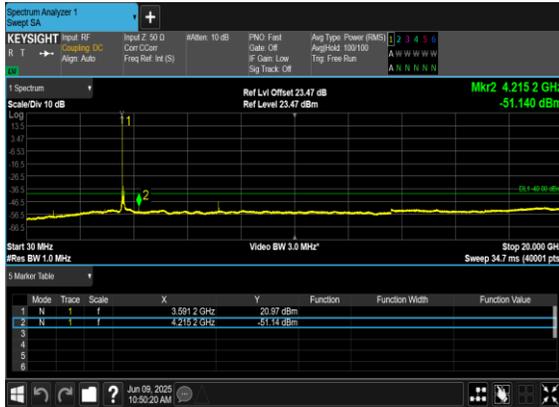


N78(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





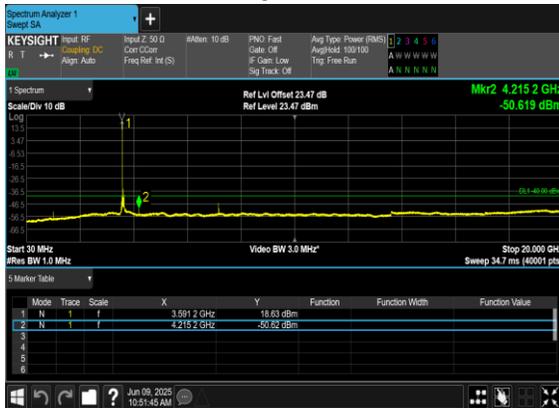
N78(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



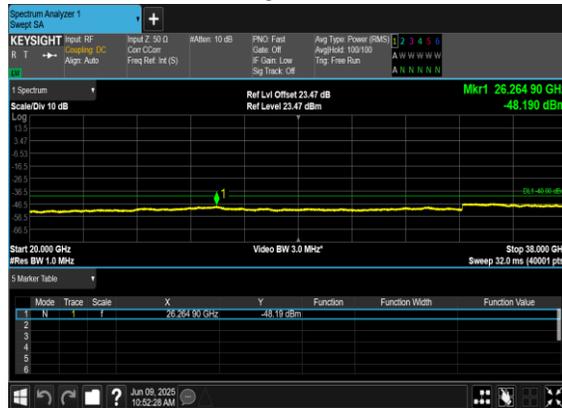
N78(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

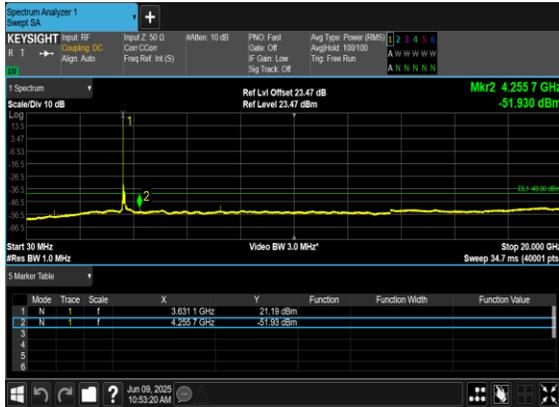


N78(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





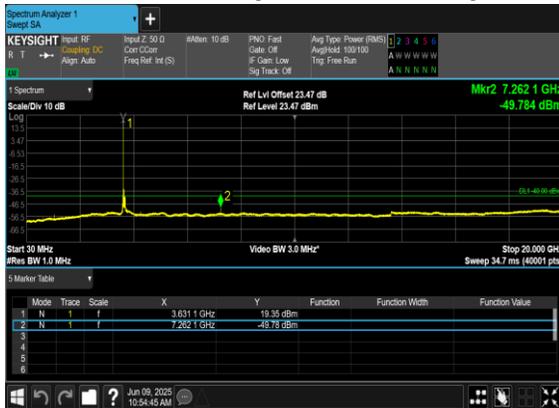
N78(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



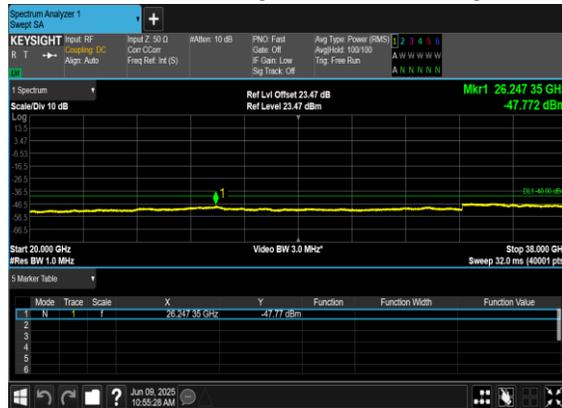
N78(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N78(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

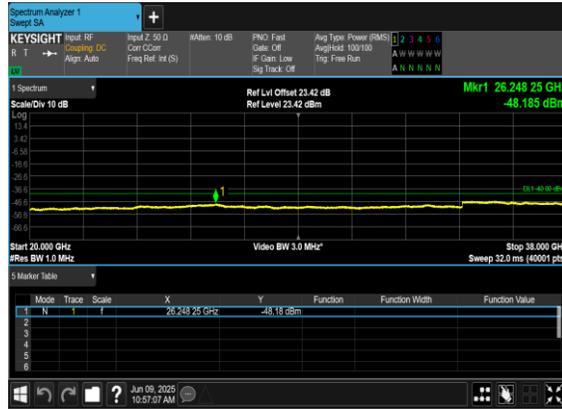




N78(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

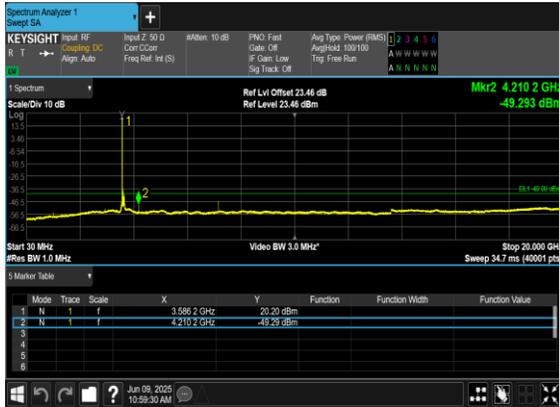


N78(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N78(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

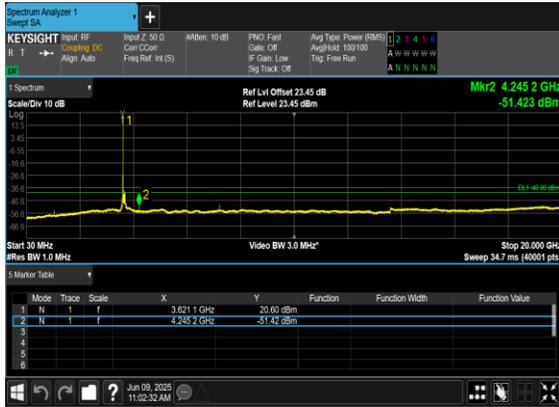


N78(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





N78(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N78(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

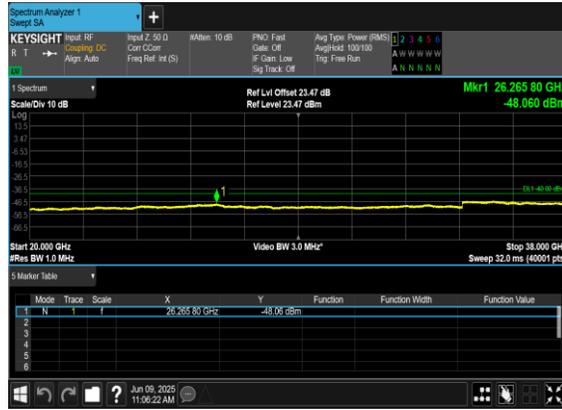




N78(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(90M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

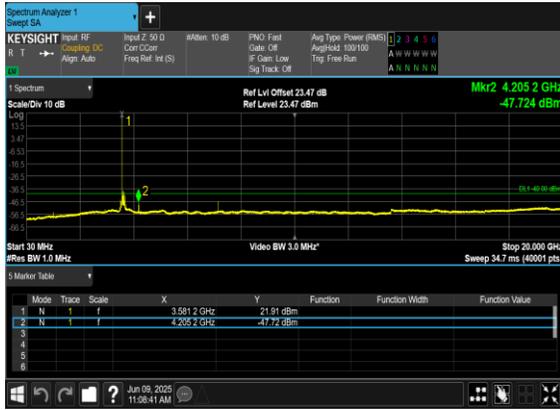


N78(90M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH

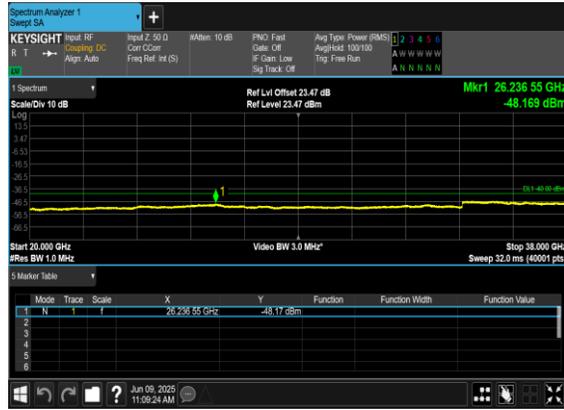




N78(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(90M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N78(90M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH





N78(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(90M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N78(90M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH





N78(100M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(100M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N78(100M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N78(100M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH





N78(100M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(100M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N78(100M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N78(100M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH

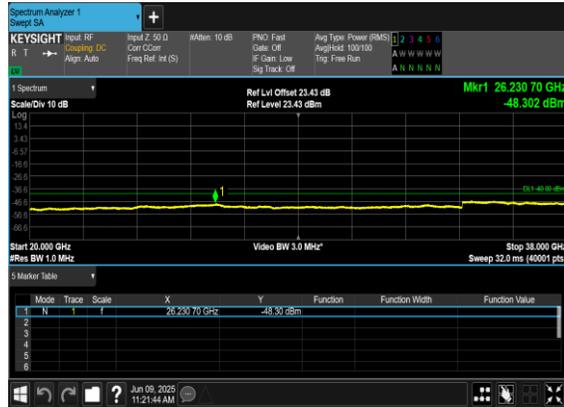




N78(100M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(100M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



N78(100M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



N78(100M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH

