

The schematic diagram illustrates a portable transmitter circuit, labeled "PERIODIC TX 439 MHz". The circuit is powered by a 9V battery (U1) and includes a DC JACK (J1) for power input. The power supply section features a fuse (F1), a diode (D1), and a capacitor (C1) for filtering. The main circuit includes a 741C04 (U2) and a 741C04 (U3) for signal processing. The output stage consists of a 2SC2414 (Q1) transistor, which is connected to an antenna (ANT) and a speaker (SP-1T). The circuit also includes a 3.58MHz crystal (X1) for frequency stabilization. Various capacitors (C1 through C17) and resistors (R1 through R10) are used for tuning and biasing. The circuit is designed to operate at 439 MHz.

FCC ID: 03G-DVL2001R

鼎新科技股份有限公司
DIGICOM Corp.

Still

DVL-1000/1001 TX Baseband Schematic Drawing

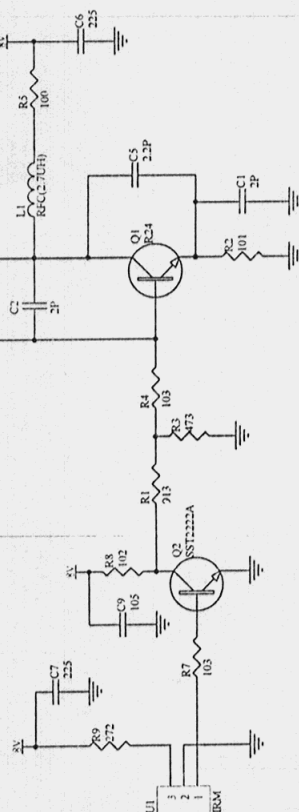
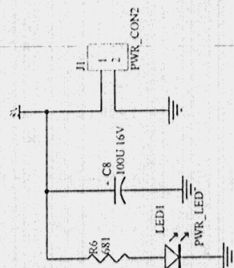
ENGINEER	York Chen	06/16/00	DATE
DRAW	York Chen	06/16/00	
CHECK	York Chen	06/16/00	
APPROVE		6/16/00	
RESPONSE	SIGN		

York Chen	06/16/00
ENGINEER	DATE

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C	890616	Artwork Revise
REV	ECO No.	Description

Note: IR extended function is not available for DVL-1000TX model



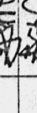
2 → PERIODIC TX 439 MHz

TRANSMISSION STOP AUTOMATICALLY
WITHIN 3 SECONDS OF PUSH-BUTTON
BEING HELD DOWN.

ALSO, WILL STOP TRANSMISSION
IMMEDIATELY UPON PUSH-BUTTON
BEING RELEASED.

PERIODICTX 439 MHz

FCC ID: 03G-DVL2001D

ENGINEER	陳燦煌	06/16/00'	TITLE: DVL-2000/2001RX 面板線路圖	SIZE A3	PCNM No.	DWG No.	Rev B
DRAW	陳燦煌	06/16/00'					
CHECK		06/16/00'					
APPROVE		06/16/00'					
RESPONSE	SIG.	DATE	Scale	NA	CAD FILE NAME	Q40101RXB SCH	Scale 1:1

B	890604	修改線路功能	陳燦煌	06/16/00'
EV	ECO No.	DESCRIPTION	ENGINEER	DATE