representing the presence of signal (logic 1) or absence of signal (logic 0) at the "D-D" pin of the receiver hybrid module.

3. The "DOWN-LINK DATA" signal driver block conditions the "DOWN-LINK DATA" from the receiver hybrid module and formats the "DOWN-LINK DATA" signal into a pseudo, differential, RS485 signal. The driver section is configured as an "OPEN COLLECTOR" output. When the "DOWN-LINK DATA" from the "D-D" pin of the receiver hybrid module (logic 0) is inverted (logic 1) and sent to the driver input the driver is in a high impedance state allowing for a "WIRED OR" multiple transceiver configuration. The drivers are only active when a logic 0 is at the driver input, and outputs a logic 1 on the "RF_DL-" line and a logic 0 on the "RF_DL+" line.

FXV 01 BLOCK DIAGRAM