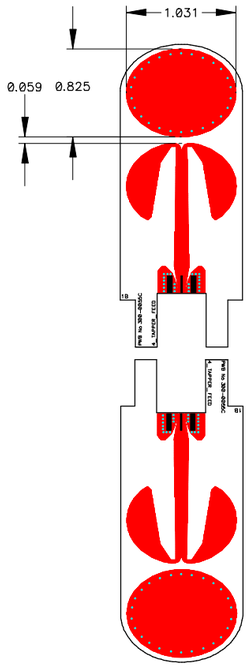
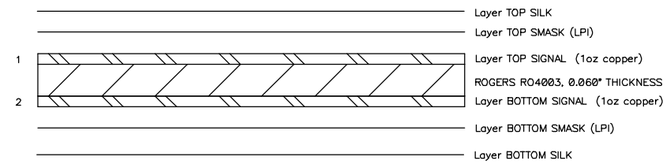


Top Layer

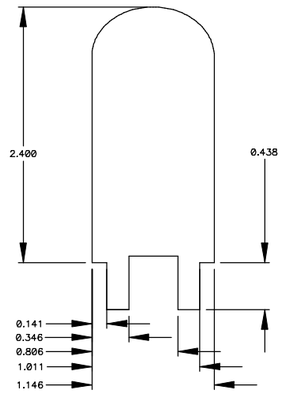
Note Table

1. ELECTRONIC DATA FILES ARE CREATED AT NOMINAL DIMENSIONS. ARTWORK COMPENSATION HAS NOT BEEN MADE BY DESIGNER. LINE, PAD AND VIA SIZES ARE AFTER MANUFACTURING. THE SUPPLIER IS RESPONSIBLE FOR FACTORING IN THE MANUFACTURING PROCESS ALLOWANCES.
2. HOLE SIZE SPECIFIED ARE AFTER PLATING/MANUFACTURING.
   SMALLEST HOLE SIZE: 0.010"
3. BOARD THICKNESS: 0.062" +/-10% PER IPC CLASS 2
   BOARD FINISH: HOT AIR SOLDER LEVEL (HASL)
4. DIELECTRIC MATERIAL IS ROGERS RO4003
5. SOLDERMASK CLEARANCE SET AT.005" EXCEPT WHERE SPECIFIED. SOLDERMASK = SMOBC USING LPI, GREEN
6. USE 1 oz. COPPER ON ALL SIGNAL LAYERS.
7. ***DO NOT PUT VENDOR MARKINGS AND DATE CODE ON THIS BOARD.***

LAYER STACKUP



Drill Symbol Table			
Hole Dia (inch)	Symbol	Quantity	Plated
0.010	+	96	yes



	APPROVALS	DATE
ORIGINATOR	Sean Miner	06-26-02
ENGINEER		
APPROVED		
RELEASED		

ALL DIMENSIONS ARE IN INCHES UNLESS SPECIFIED
DIMESIONAL TOLERANCES UNLESS OTHERWISE SPECIFIED
2 PLACE +/-.01 3 PLACE +/-.005
4 PLACE +/-.0005 ANGLES +/-0.5

3rd ANGLE PROJECTION

FILE NAME:

DO NOT SCALE DRAWING

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SIZE	Drawing No.	REV
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SCALE	RELEASE NO.	SHEET 1 OF 1