

WCID-R1000

This model is a receiver for accepting the interpreted caller ID information from WCID-B1000. The data is modulated with a carrier frequency of 480 Mhz.

U201 HD404888/4889 is the CPU controlling the unit. It periodically pull low pin 22 to enable VX and the 480 Mhz receiving circuit.

When there is RF data, the CPU will start a receive cycle to accept all bits of information from WCID-B1000. In turn the same will be output to the LCD.

The record is stored in the non-volatile memory U202 24C16 for further review. 60 records maximum can be stored.

Looking into the receive circuit, C102 - C103, CT1 20pF tunable capacitor and L107 forms the tuner circuit at 480 Mhz.

C105 - C107, L101 - L102 forms a filter for retaining signal at around the carrier frequency.

After Q101 the RF pre-amplifier, Q102, C115, IFT1 will further amplify the signal and the envelope is detected by R107, C116 and Q103. Afterwards the signal is re-shaped by U101 2904 operational amplifier and detected by the CPU (DATAIN).