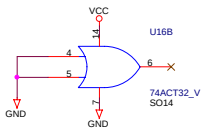
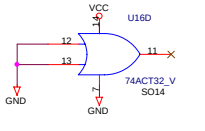
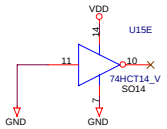
Model-1000 GPIO ASSIGNMENT

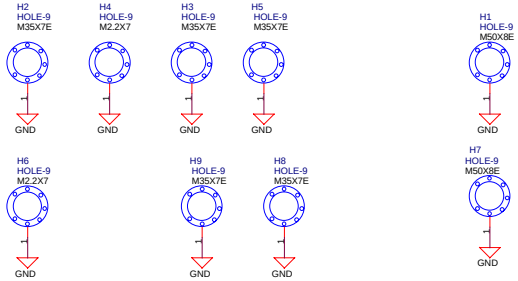
GPI0	DOCKSMI#	FOR DOCKING	ACTIVE LOW
GPI1	PIIX4 GPI1#	FOR ACPI	ACTIVE LOW
GPI2	FANFAULT#	FOR W83L784	ACTIVE LOW
GPI3	CARDOC#	FOR CARD BUS	ACTIVE LOW
GPI4	AD_PRESENT	FOR HPM	ACTIVE HIGH
GPI5	CHARGING#	FOR HPM	ACTIVE LOW
GPI6	BAT_PRESENT#	FOR HPM	ACTIVE LOW
GPI7	D/P	DOCK/PORTICLE SWICH	0 : PORTICLE 1 : DOCKING
GPI8	784_SMI#	FOR W83L784	ACTIVE LOW
GPI9	BATLOW#		ACTIVE LOW
GPI10	LID#	FOR PME (W83L784)	ACTIVE LOW
GPI11	SMALERT#	FOR SM BUS	ACTIVE LOW
GPI12	WAKE UP#	FOR ACPI (RING)	ACTIVE LOW
GPI13	GPI13	FOR AUDIO	ACTIVE LOW
GPI14	CRT ON#		ACTIVE LOW
GPI15	MID0		ACTIVE HIGH
GPI16	MID1		ACTIVE HIGH
GPI17	MID2		ACTIVE HIGH
GPI18	MID3		ACTIVE HIGH
GPI19	VGA_BUSY#	FOR HPM	ACTIVE LOW
GPI20	CARD_LED#	FOR HPM	ACTIVE LOW
GPI21	VRCHANGE	RESERVED	
***	*****	*****	*****
GPO0	SENSOR_PD#	FOR TOUCH PANEL	ACTIVE LOW
GPO1	PDPWR	FOR PRIMARY HDD	ACTIVE HIGH
GPO2	BAY_PWR	FOR SECONDARY CD-ROM	ACTIVE HIGH
GPO3	INHIB		ACTIVE HIGH
GPO4	DRAMENA		ACTIVE HIGH
GPO5	DRAMENB		ACTIVE HIGH
GPO6	USB/SW	DOCK/PORTICLE SWICH	1 : CCD USE 0 : DOCKING USE
GPO7	PIIX4 EVEN#	CARDBUS ZVPORT ENABLE	ACTIVE LOW
GPO8	PDRESET#	FOR HDD RESET	ACTIVE LOW
GPO9	2RTX_PWR	PULL OR 2/3 PWR	
GPO10	X	X	
GPO11	X	X	
GPO12	SDRESET#	FOR CD-ROM RESET	ACTIVE LOW
GPO13	IO_OFF#	FOR SERIES PORT	ACTIVE LOW
GPO14	BIOSWP#		ACTIVE LOW
GPO15	SUSB#		ACTIVE LOW
GPO16	SUSC#		ACTIVE LOW
GPO17	CPU_STP#		ACTIVE LOW
GPO18	PCI_STP#		ACTIVE LOW
GPO19	CCD_OFF	FOR CCD	ACTIVE HIGH
GPO20	SUS_STAT1#	FOR 440BX	ACTIVE LOW
GPO21	SUS_STAT2#	FOR VGA_STP_AGP#	ACTIVE LOW
GPO22	784_PWRDN#	FOR W83L784	ACTIVE LOW
GPO23	GPO23#	FOR VGA AGP SUS#	ACTIVE LOW
GPO24	CCD_PD	FOR HPM	ACTIVE HIGH
GPO25	ROM12V		ACTIVE HIGH
GPO26	KBCS#		ACTIVE LOW
GPO27	PIIX4_GPO27	FOR DOCKING	ACTIVE HIGH
GPO28	IWINI-IN	DOCKING MINIPCI IN	ACTIVE HIGH
GPO29	X	X	X
GPO30	OP_PD#	FOR AUDIO	ACTIVE LOW

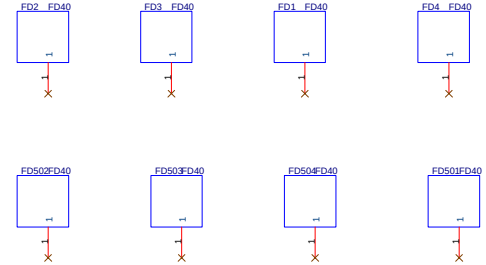
LINK
2.SCH
3.SCH
4.SCH
5.SCH
6.SCH
7.SCH
8.SCH
9.SCH
10.SCH
11.SCH
12.SCH
13.SCH
14.SCH
15.SCH
16.SCH
17.SCH
18.SCH
19.SCH
20.SCH
21.SCH
22.SCH
23.SCH
24.SCH











	PCIINT	440BX REQ/GNT	PIIX4 REQ	REMARK
	A B C D	0 1 2 3 4	A B C D	
PIIX4	X X X X		X X X X	
TONGA		X X X X X		
1394	X	X	x	
CARDBUS	X X	X	X	
AUDIO	x	X	X	
MINIPCI	X X	X	X	
DOCKING	X X X X	X X	X	
VGA	X		XX	AGP_REQ/GNT; AGP_BUSY -> PCIREQD#

PCI IDSEL ASSIGNMENT

PIIX4	AD18
12LV22	AD15
TI1250A	AD14
ES1980-M3	AD24
MINIPCI	AD13
VGA-SM721	AD16
DOCK MINIPCI	AD20

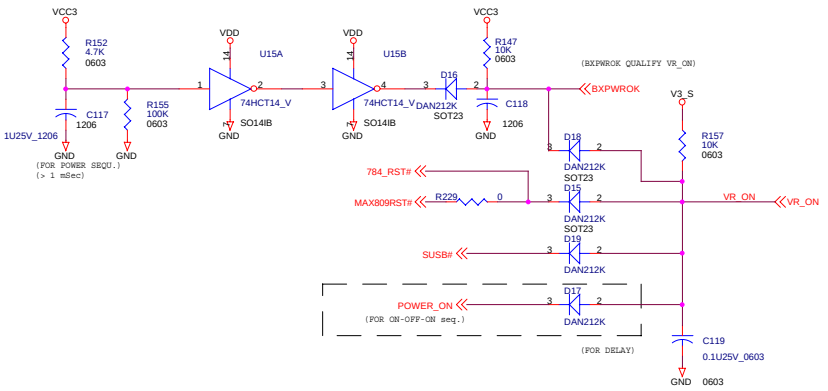
Innolabs Corp.

Title: SPARE PARTS

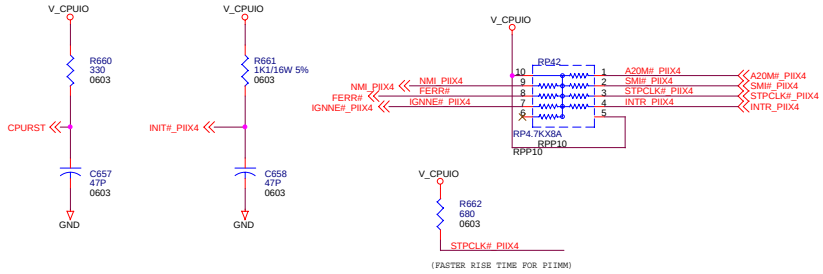
Size: C | Document Number: Model-1000 | Rev: 01BA

Date: Monday, October 11, 1999 | Sheet 1 of 28

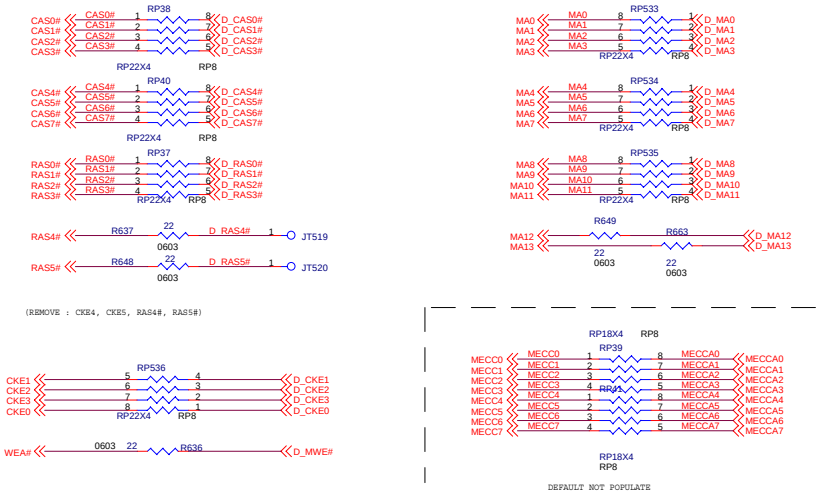
VR_ON



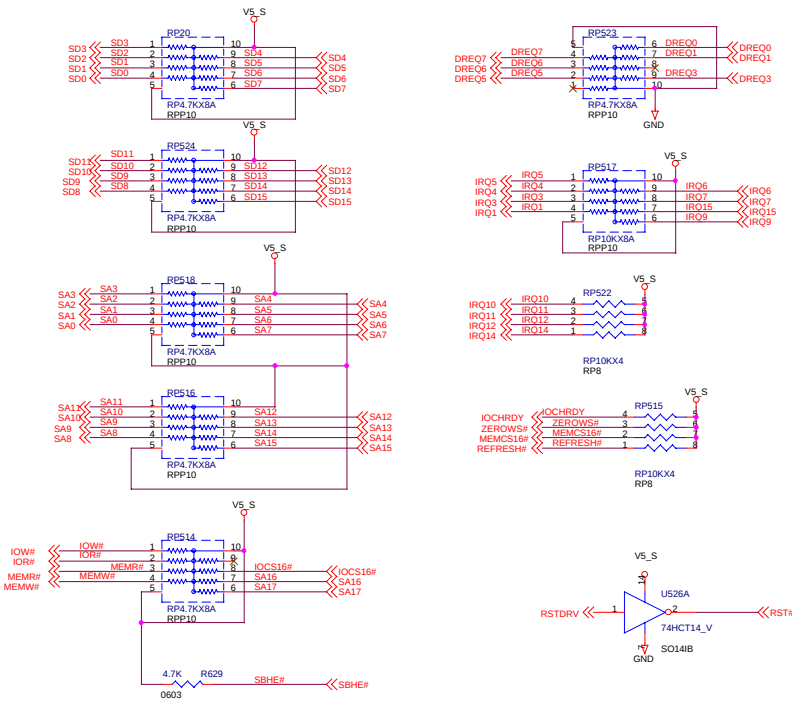
PIIX4 <--> CPU SIDEBAND SIGNAL



MEM I/F DAMPING

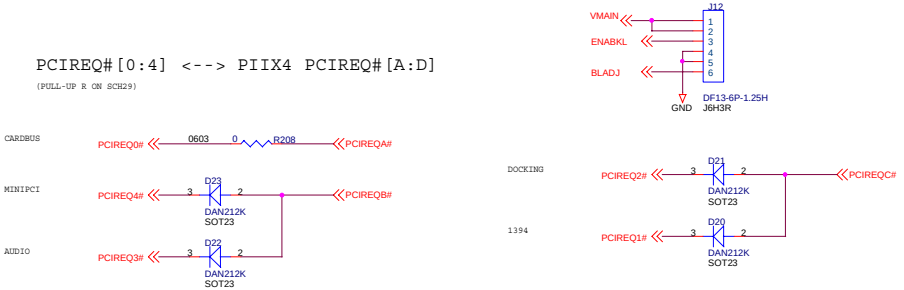


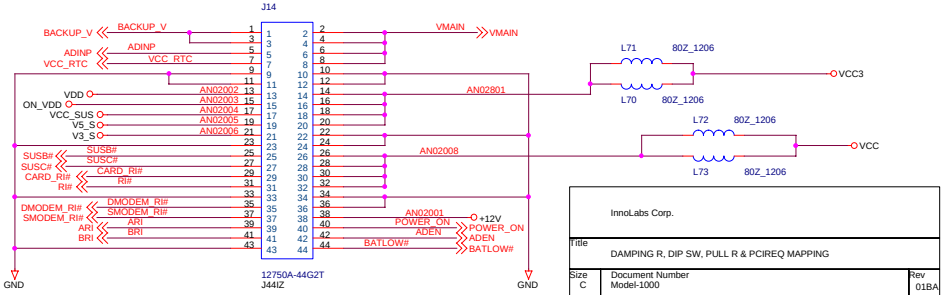
ISA PULL UP/DOWN



PCIREQ#[0:4] <--> PIIX4 PCIREQ#[A:D]

(PULL-UP R ON SCH29)





InnoLabs Corp.		
Title: DAMPING R, DIP SW, PULL R & PCIREQ MAPPING		
Size: C	Document Number: Model-1000	Rev: 01BA
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MEM 64-BITS DATA DAMPING

(MUST 18 OHM/R-PACK)

XMD[63:0] → RP18X4 RP52 … RP59 → MD[0..63]

PCI PULL UP/DOWN

BANK2,3

BANK0,1

D_SRASA#,D_SCASA# & D_MWE# NO NEED BUFFER

InnoLabs Corp.	
Title	DIMM SOCKETS (BANK0~5)
Size C	Document Number Model-1000
Rev	01BA
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430TX

PIIX4

ID :AD18/(AD12)

SUSPEND WELL PINS : BATLOW#,CONFIG[1:2]

EXTSMI#,GPI1,IRQ8#,LID,RI#

SMBALERT#,SMBCLK,SMBDATA,PWRBTN#

SUS[A:C],SUSCLK,SUS_STAT[1:2],TEST#

VCC(RTC) : RTCX1, RTCX2, RSMRST#
PWROK

SCI : ACPI' SYSTEM CONTROL INTERRUPTS

PWRBTN#, LID, GPI1#, THRM#, RI#
IRQ9 NOT AVAILABLE WHEN SCI ENABLED

GPI1# : RUNTIME
LID : PME/EC
RI : PME/DIRECT

AD12->AD18

(DEBUG ONLY)

(ERRATA)

(10p -> 33p)

CRYSTAL CAP. LOAD

CITIZEN: 12.5P

EPSON: 6P ~ INF.

* SLEEP# IS RESERVED FOR PENTIUM PRO
* RSMRST# USES VCC_RTC AND MUST NOT BE 5 VOLT
* PWRGOOD MUST BE 3.3V VERSION
* SMBUS IS USED TO READ SDRAM PD

MCCS# IS DECODED FOR 62/66 PORT

SUS_STAT1# : PIIX4 & MMC2
SUS_STAT2# : PIIX4 & DEVICE NEED BE INFORMED

InnoLabs Corp.	
Title: PIIX4	
Size: C	Document Number: Model-1000
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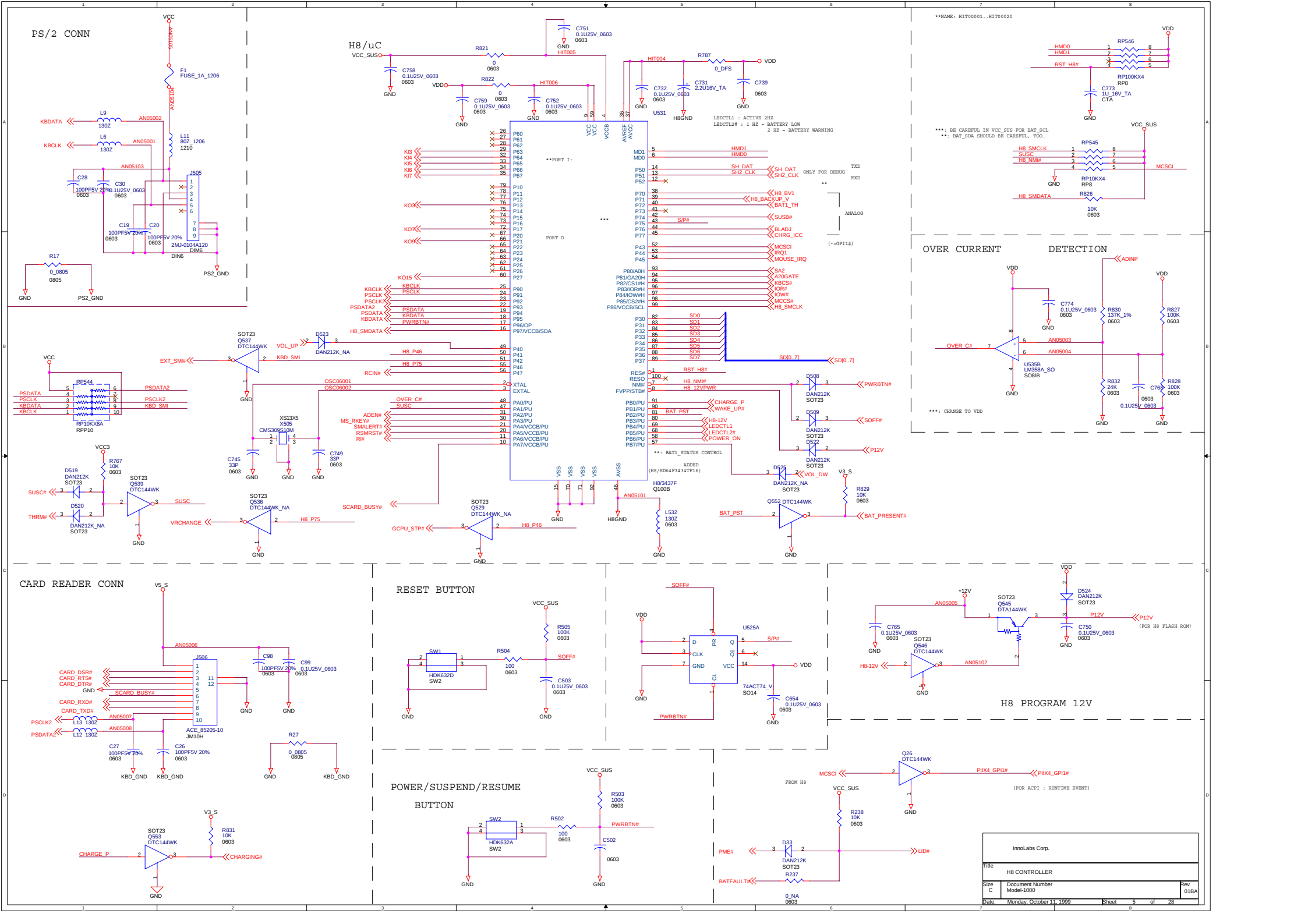
Date: Monday, October 11, 1999	Sheet 4 of 28


PS/2 CONN
H8/uC
OVER CURRENT DETECTION
CARD READER CONN
RESET BUTTON
H8 PROGRAM 12V
POWER/SUSPEND/RESUME BUTTON
U531
H8/3437F Q100B
**NAME: HIT00001..HIT00020
LEDCTL1 : ACTIVE 2HZ
LEDCTL2# : 1 HZ - BATTERY LOW
2 HZ - BATTERY WARNING
**PORT I:
PORT O
ONLY FOR DEBUG
ANALOG
ADDED (H8/HD64F3434TF16)
**: BAT1_STATUS CONTROL
***: BE CAREFUL IN VCC_SUS FOR BAT_SCL
**: BAT_SDA SHOULD BE CAREFUL, TOO.
***: CHANGE TO VDD
U535B LM358A_SO SO8IB
U525A 74ACT74_V SO14
U532 130Z 0603
(->GPI1#)
FROM H8
(FOR ACPI : RUNTIME EVENT)
(FOR H8 FLASH ROM)
InnoLabs Corp.
Title
H8 CONTROLLER
Size C
Document Number Model-1000
Rev 01BA
Date: Monday, October 11, 1999
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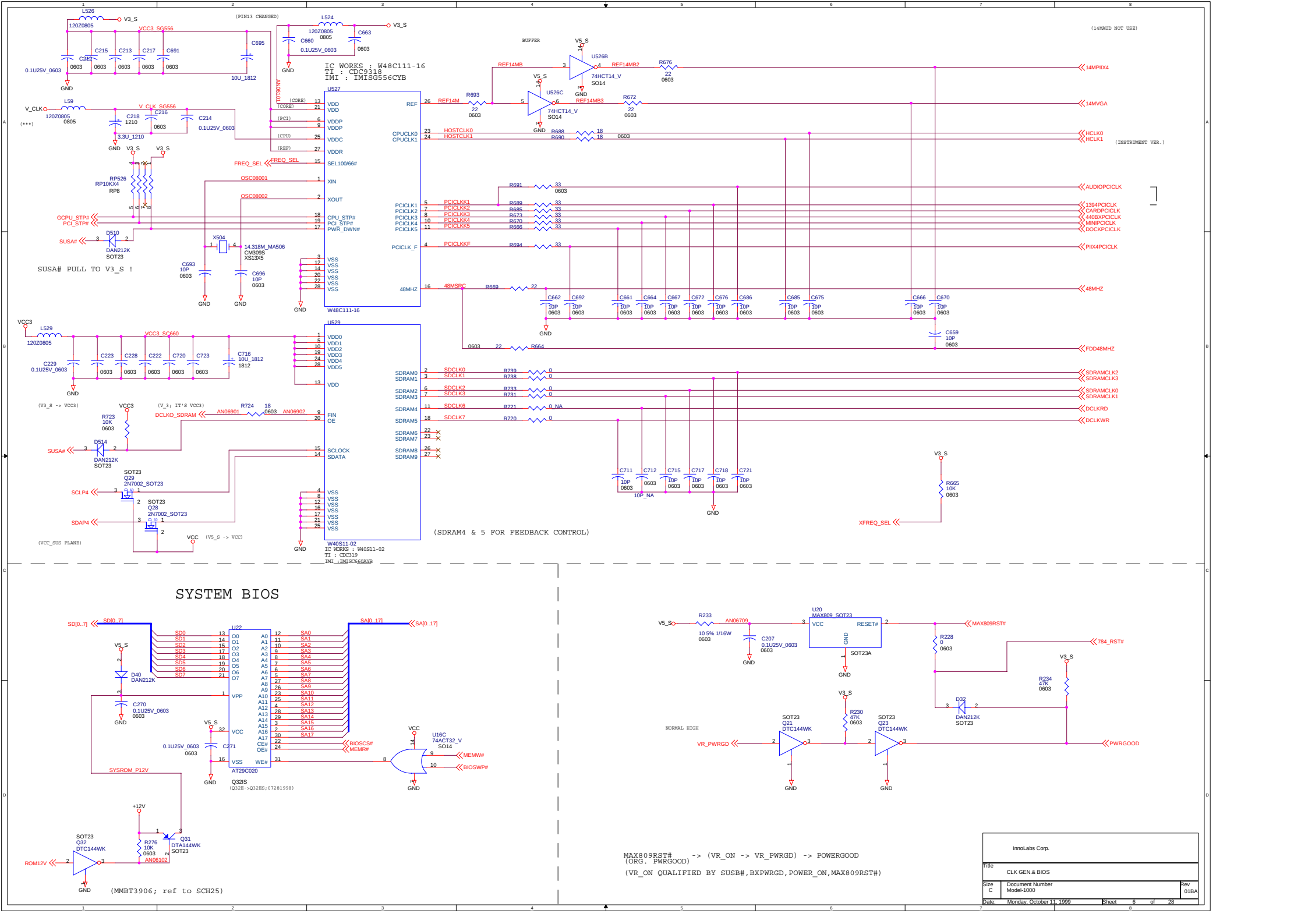
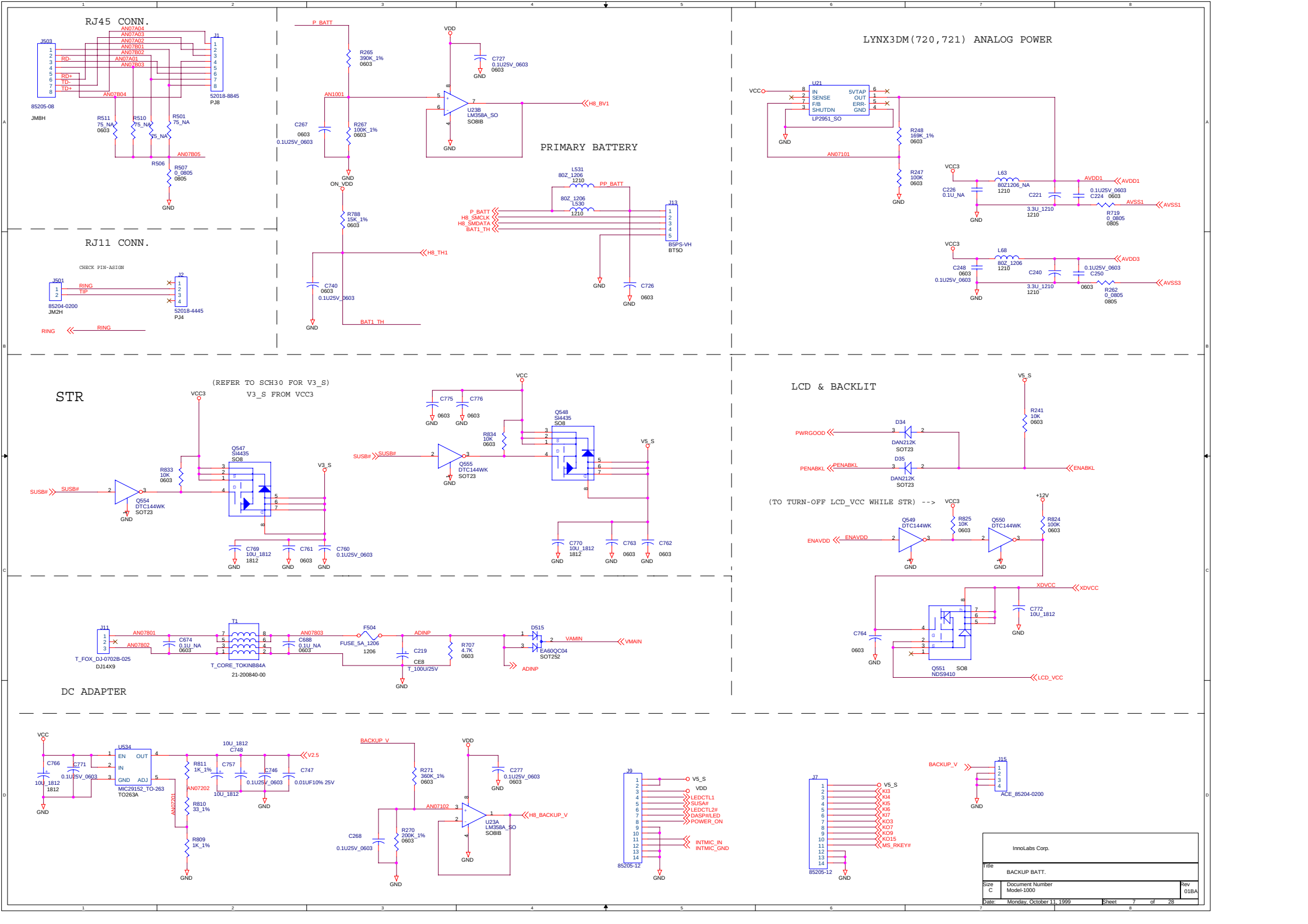

IC WORKS : W48C111-16
TI : CDC9318
IMI : IMISG556CYB
W48C111-16
SUSA# PULL TO V3_S !
(SDRAM4 & 5 FOR FEEDBACK CONTROL)
W40S11-02
IC WORKS : W40S11-02
TI : CDC319
IMI :IMISC560AYB
SYSTEM BIOS
(MMBT3906; ref to SCH25)
MAX809RST# -> (VR_ON -> VR_PWRGD) -> POWERGOOD
(ORG. PWRGOOD)
(VR_ON QUALIFIED BY SUSB#,BXPWRGD,POWER_ON,MAX809RST#)
InnoLabs Corp.
CLK GEN.& BIOS
Document Number
Model-1000
Rev
01BA
Monday, October 11, 1999
Sheet 6 of 28


RJ45 CONN.
J503
85205-08
JM8H
J1
52018-8845
PJ8
AN07A04
AN07A03
AN07A02
AN07B01
AN07B02
AN07A01
AN07B03
AN07B04
RD-
RD+
TD-
TD+
R511 75_NA 0603
R510 75_NA
R501 75_NA
R506
R507 0_0805 0805
AN07B05
GND
P_BATT
R265 390K_1% 0603
AN1001
C267 0603 0.1U25V_0603
R267 100K_1% 0603
VDD
C727 0.1U25V_0603 0603
U23B LM358A_SO SO8IB
H8_BV1
ON_VDD
R788 15K_1% 0603
H8_TH1
C740 0603 0.1U25V_0603
BAT1_TH
PRIMARY BATTERY
L531 80Z_1206 1210
PP_BATT
L530 80Z_1206 1210
J13
P_BATT
H8_SMCLK
H8_SMDATA
BAT1_TH
B5PS-VH
BT5O
C726 0603
LYNX3DM(720,721) ANALOG POWER
U21
LP2951_SO
IN
SENSE
F/B
SHUTDN
5VTAP
OUT
ERR-
VCC
R248 169K_1% 0603
AN07101
R247 100K 0603
L63 80Z1206_NA 1210
C226 0.1U_NA
C221 3.3U 1210
C224 0603
AVDD1
AVSS1
R719 0_0805 0805
L68 80Z_1206 1210
C248 0603 0.1U25V_0603
C240 3.3U_1210 1210
C250 0603
AVDD3
AVSS3
R262 0_0805 0805
RJ11 CONN.
CHECK PIN-ASIGN
J501
85204-0200
JM2H
J2
52018-4445
PJ4
RING
TIP
STR
(REFER TO SCH30 FOR V3_S)
V3_S FROM VCC3
Q547 SI4435 SO8
R833 10K 0603
Q554 DTC144WK SOT23
C769 10U_1812 1812
C761 0603
C760 0.1U25V_0603
C775 C776
Q548 SI4435 SO8
R834 10K 0603
Q555 DTC144WK SOT23
C770 10U_1812 1812
C763 0603
C762 0603
LCD & BACKLIT
R241 10K 0603
D34 DAN212K SOT23
D35 DAN212K SOT23
PWRGOOD
PENABKL
ENABKL
(TO TURN-OFF LCD_VCC WHILE STR) -->
Q549 DTC144WK
R825 10K 0603
Q550 DTC144WK
R824 100K 0603
+12V
ENAVDD
C772 10U_1812
XDVCC
C764 0603
Q551 SO8 NDS9410
LCD_VCC
DC ADAPTER
J11
T_FOX_DJ-0702B-025
DJ14X9
AN07801
AN07802
C674 0.1U_NA 0603
T1
T_CORE_TOKINB84A
21-200840-00
C688 0.1U_NA 0603
AN07803
F504
FUSE_5A_1206 1206
ADINP
C219 CE8 T_100U/25V
R707 4.7K 0603
D515 EA60QC04 SOT252
VAMIN
VMAIN
U534 MIC29152_TO-263 TO263A
EN
OUT
IN
GND
ADJ
C766 10U_1812 1812
C771 0.1U25V_0603
10U_1812 C748
R811 1K_1%
C757 10U_1812
C746 0.1U25V_0603
C747 0.01UF10% 25V
V2.5
AN07201
AN07202
R810 33_1%
R809 1K_1%
BACKUP_V
R271 360K_1% 0603
C277 0.1U25V_0603 0603
AN07102
R270 200K_1% 0603
C268 0.1U25V_0603
U23A LM358A_SO SO8IB
H8_BACKUP_V
J9
85205-12
V5_S
VDD
LEDCTL1
SUSA#
LEDCTL2#
DASP#/LED
POWER_ON
INTMIC_IN
INTMIC_GND
J7
KI3
KI4
KI5
KI6
KI7
KO3
KO7
KO9
KO15
MS_RKEY#
J15
ACE_85204-0200
InnoLabs Corp.
Title
BACKUP BATT.
Size C
Document Number Model-1000
Rev 01BA
Date: Monday, October 11, 1999
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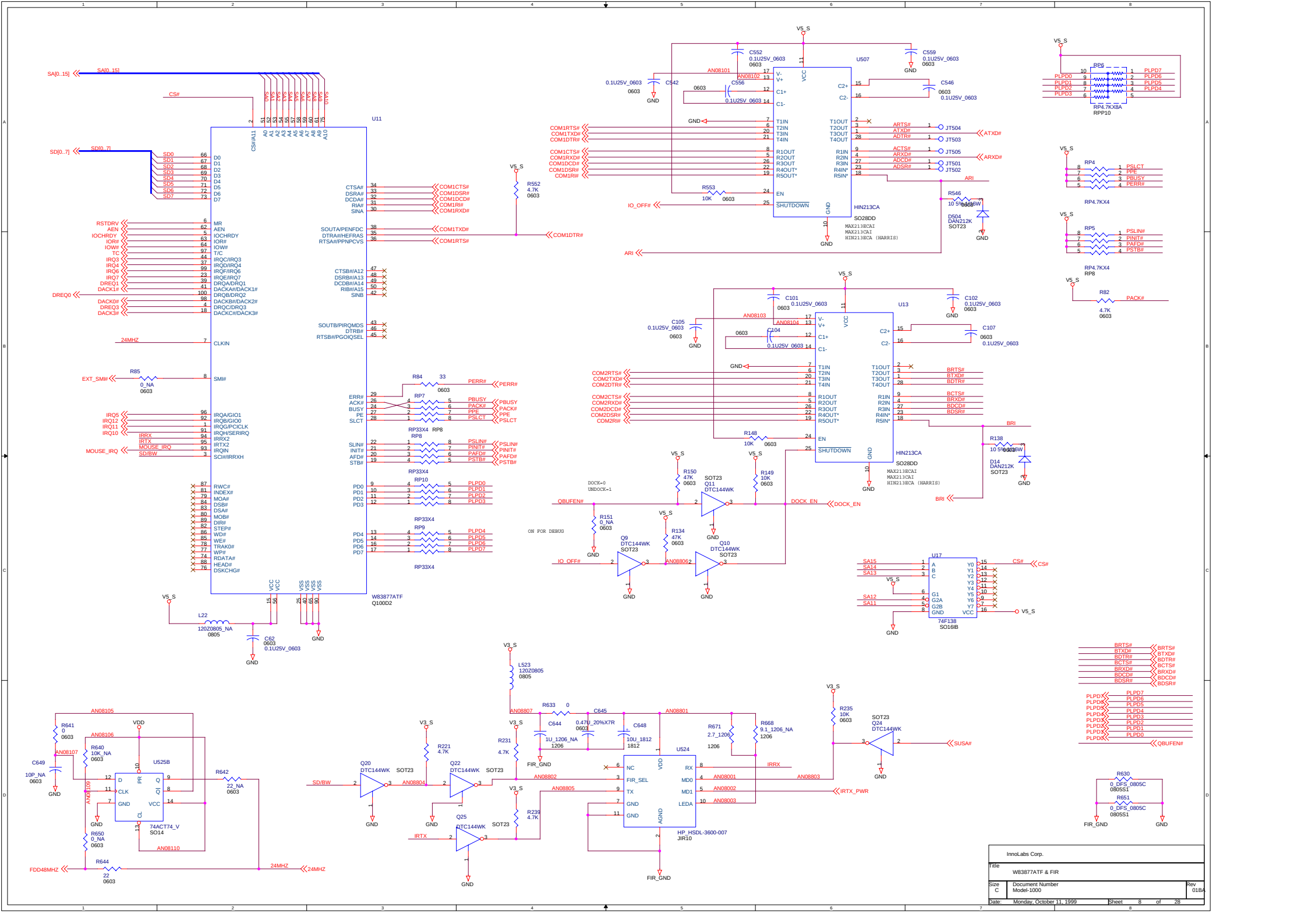
U11
W83877ATF
Q100D2
U507
HIN213CA
SO28DD
U13
HIN213CA
SO28DD
U524
HP_HSDL-3600-007
JIR10
U525B
74ACT74_V
SO14
U17
74F138
SO16IB
RP6
RP4.7KX8A
RPP10
RP4
RP4.7KX4
RP5
RP4.7KX4
RP8
RP4.7KX4
RP7
RP33X4
RP8
RP10
RP33X4
RP9
RP33X4
Q9
DTC144WK
SOT23
Q10
DTC144WK
SOT23
Q11
DTC144WK
SOT23
Q20
DTC144WK
SOT23
Q22
DTC144WK
SOT23
Q25
DTC144WK
SOT23
Q24
DTC144WK
SOT23
D504
DAN212K
SOT23
D14
DAN212K
SOT23
JT504
JT503
JT505
JT501
JT502
ATXD#
ARXD#
ARI
BRI
IO_OFF#
DOCK_EN
QBUFEN#
SUSA#
IRTX_PWR
IRRX
FIR_GND
ON FOR DEBUG
DOCK=0
UNDOCK=1
SA[0..15]
SD[0..7]
CS#
EXT_SMI#
MOUSE_IRQ
DREQ0
24MHZ
FDD48MHZ
InnoLabs Corp.
W83877ATF & FIR
Document Number
Model-1000
Rev
01BA
Monday, October 11, 1999
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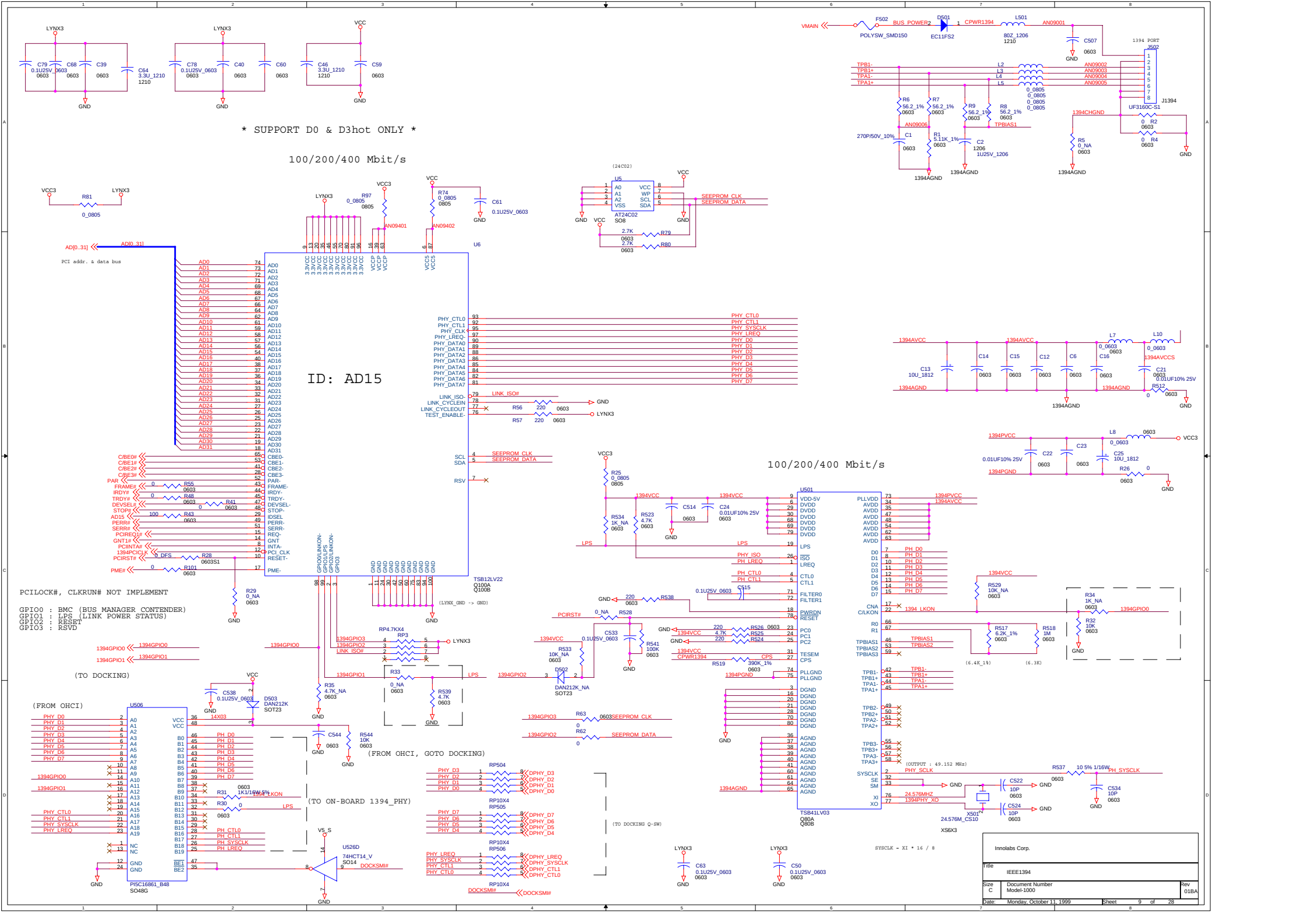
* SUPPORT D0 & D3hot ONLY *
100/200/400 Mbit/s
ID: AD15
PCILOCK#, CLKRUN# NOT IMPLEMENT
GPIO0 : BMC (BUS MANAGER CONTENDER)
GPIO1 : LPS (LINK POWER STATUS)
GPIO2 : RESET
GPIO3 : RSVD
(TO DOCKING)
(FROM OHCI)
(FROM OHCI, GOTO DOCKING)
(TO ON-BOARD 1394_PHY)
(TO DOCKING Q-SW)
SYSCLK = XI * 16 / 8
Innolabs Corp.
IEEE1394
Document Number Model-1000
Rev 01BA
Monday, October 11, 1999
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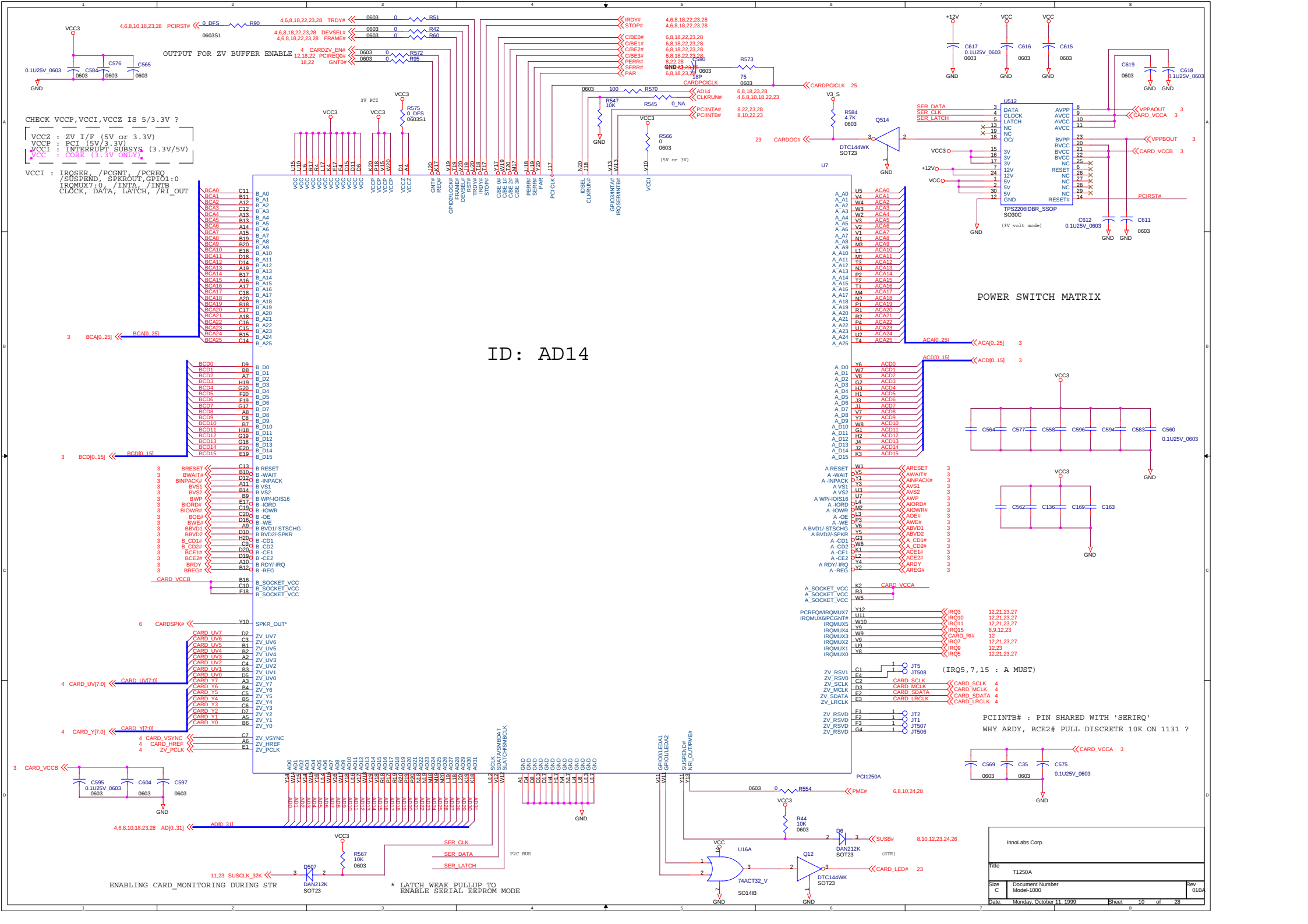
OUTPUT FOR ZV BUFFER ENABLE
CHECK VCCP,VCCI,VCCZ IS 5/3.3V ?
VCCZ : ZV I/F (5V or 3.3V)
VCCP : PCI (5V/3.3V)
VCCI : INTERRUPT SUBSYS (3.3V/5V)
VCC : CORE (3.3V ONLY)
VCCI : IRQSER, /PCGNT, /PCREQ
/SUSPEND, SPKROUT,GPIO1:0
IRQMUX7:0, /INTA, /INTB
CLOCK, DATA, LATCH, /RI_OUT
ID: AD14
POWER SWITCH MATRIX
PCIINTB# : PIN SHARED WITH 'SERIRQ'
WHY ARDY, BCE2# PULL DISCRETE 10K ON 1131 ?
(IRQ5,7,15 : A MUST)
ENABLING CARD_MONITORING DURING STR
* LATCH WEAK PULLUP TO
ENABLE SERIAL EEPROM MODE
P2C BUS
PCI1250A
U7
U512
TPS2206IDBR_SSOP
SO30C
(3V volt mode)
U16A
74ACT32_V
SO14IB
DTC144WK
SOT23
DAN212K
SOT23
InnoLabs Corp.
T1250A
Document Number
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Rev
01BA
Monday, October 11, 1999
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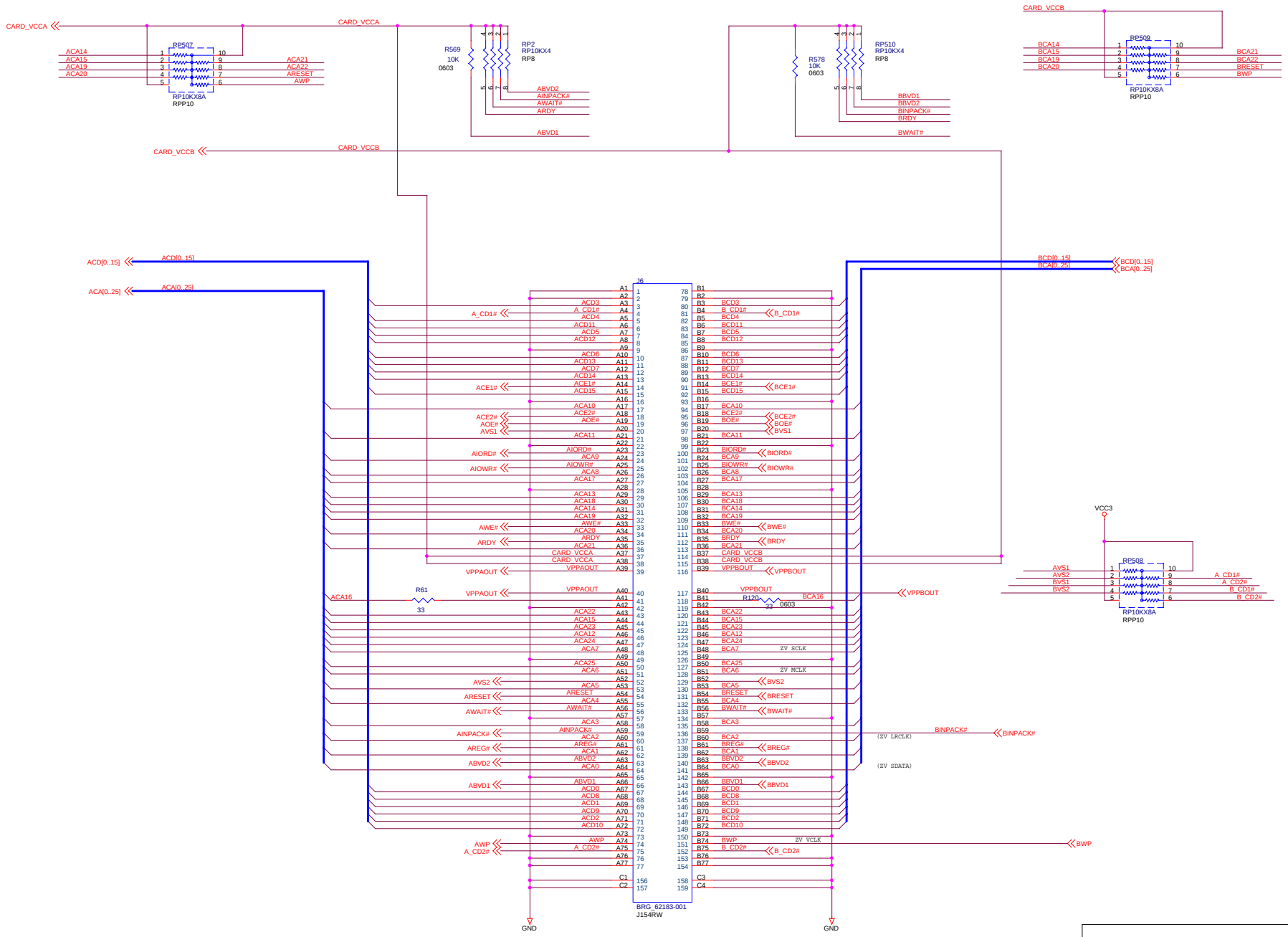
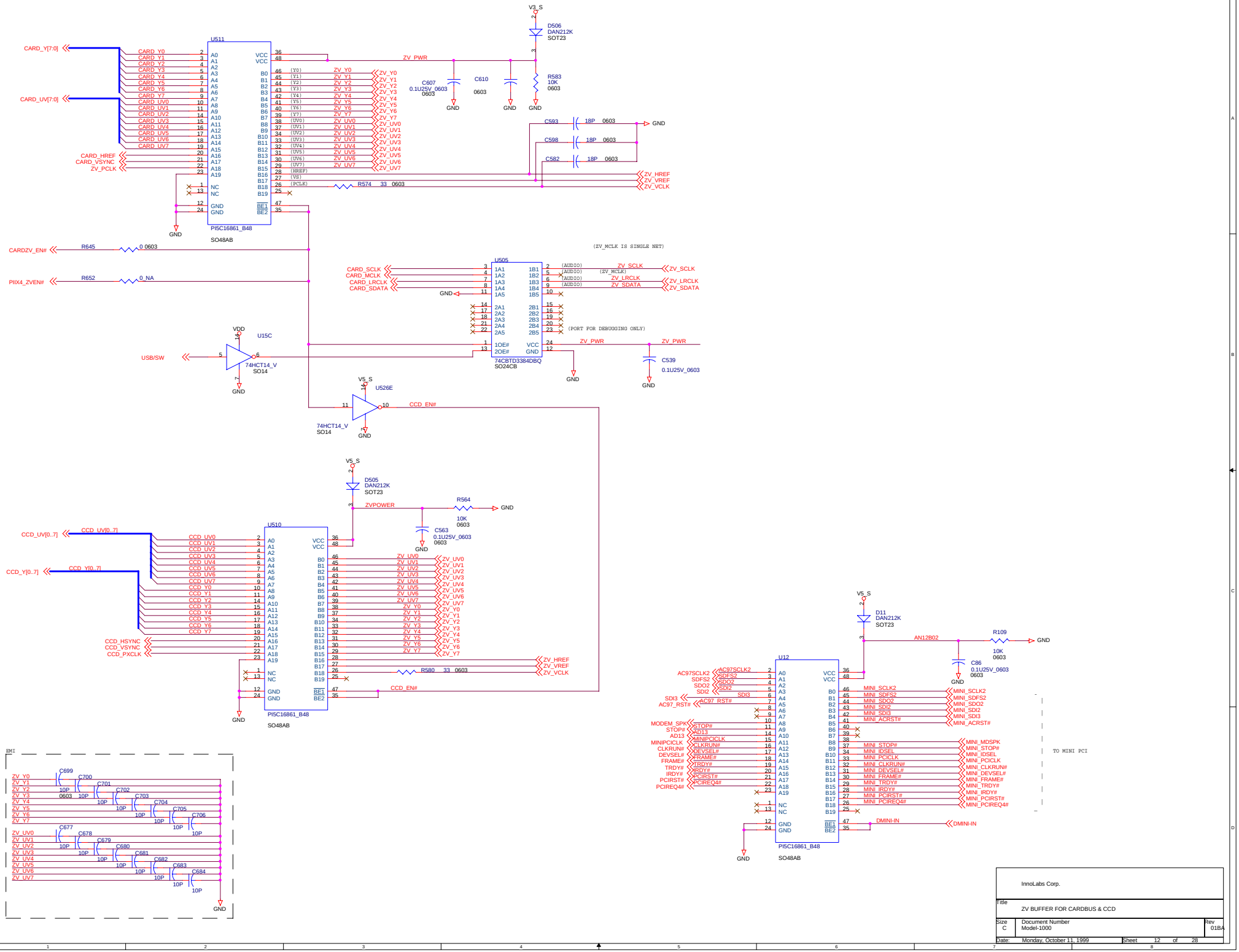


PCCARD SOCKET

InnoLabs Corp.	
Title: CARD BUS SOCKETS	
Size: C	Document Number: Model-1000 — Rev: 01BA
Date: Monday, October 11, 1999	Sheet 11 of 28


U511
PI5C16861_B48
SO48AB
CARD_Y[7:0]
CARD_UV[7:0]
CARD_HREF
CARD_VSYNC
ZV_PCLK
ZV_PWR
D506
DAN212K
SOT23
V3_S
C607
0.1U25V_0603
C610
0603
R583
10K
0603
C593
C598
C582
18P
0603
ZV_HREF
ZV_VREF
ZV_VCLK
R574
33
0603
CARDZV_EN#
R645
0 0603
PIIX4_ZVEN#
R652
0_NA
(ZV_MCLK IS SINGLE NET)
U505
74CBTD3384DBQ
SO24CB
CARD_SCLK
CARD_MCLK
CARD_LRCLK
CARD_SDATA
ZV_SCLK
ZV_LRCLK
ZV_SDATA
(PORT FOR DEBUGGING ONLY)
U15C
74HCT14_V
SO14
USB/SW
VDD
U526E
V5_S
CCD_EN#
C539
0.1U25V_0603
U510
PI5C16861_B48
SO48AB
CCD_UV[0..7]
CCD_Y[0..7]
CCD_HSYNC
CCD_VSYNC
CCD_PXCLK
D505
DAN212K
SOT23
ZVPOWER
R564
10K
0603
C563
0.1U25V_0603
0603
R580
33
0603
EMI
C699
C700
C701
C702
C703
C704
C705
C706
C677
C678
C679
C680
C681
C682
C683
C684
10P
U12
PI5C16861_B48
SO48AB
D11
DAN212K
SOT23
AN12B02
R109
10K
0603
C86
0.1U25V_0603
0603
MINI_SCLK2
MINI_SDFS2
MINI_SDO2
MINI_SDI2
MINI_SDI3
MINI_ACRST#
MINI_MDSPK
MINI_STOP#
MINI_IDSEL
MINI_PCICLK
MINI_CLKRUN#
MINI_DEVSEL#
MINI_FRAME#
MINI_TRDY#
MINI_IRDY#
MINI_PCIRST#
MINI_PCIREQ4#
DMINI-IN
TO MINI PCI
GND
InnoLabs Corp.
ZV BUFFER FOR CARDBUS & CCD
Size C
Document Number Model-1000
Rev 01BA
Date: Monday, October 11, 1999
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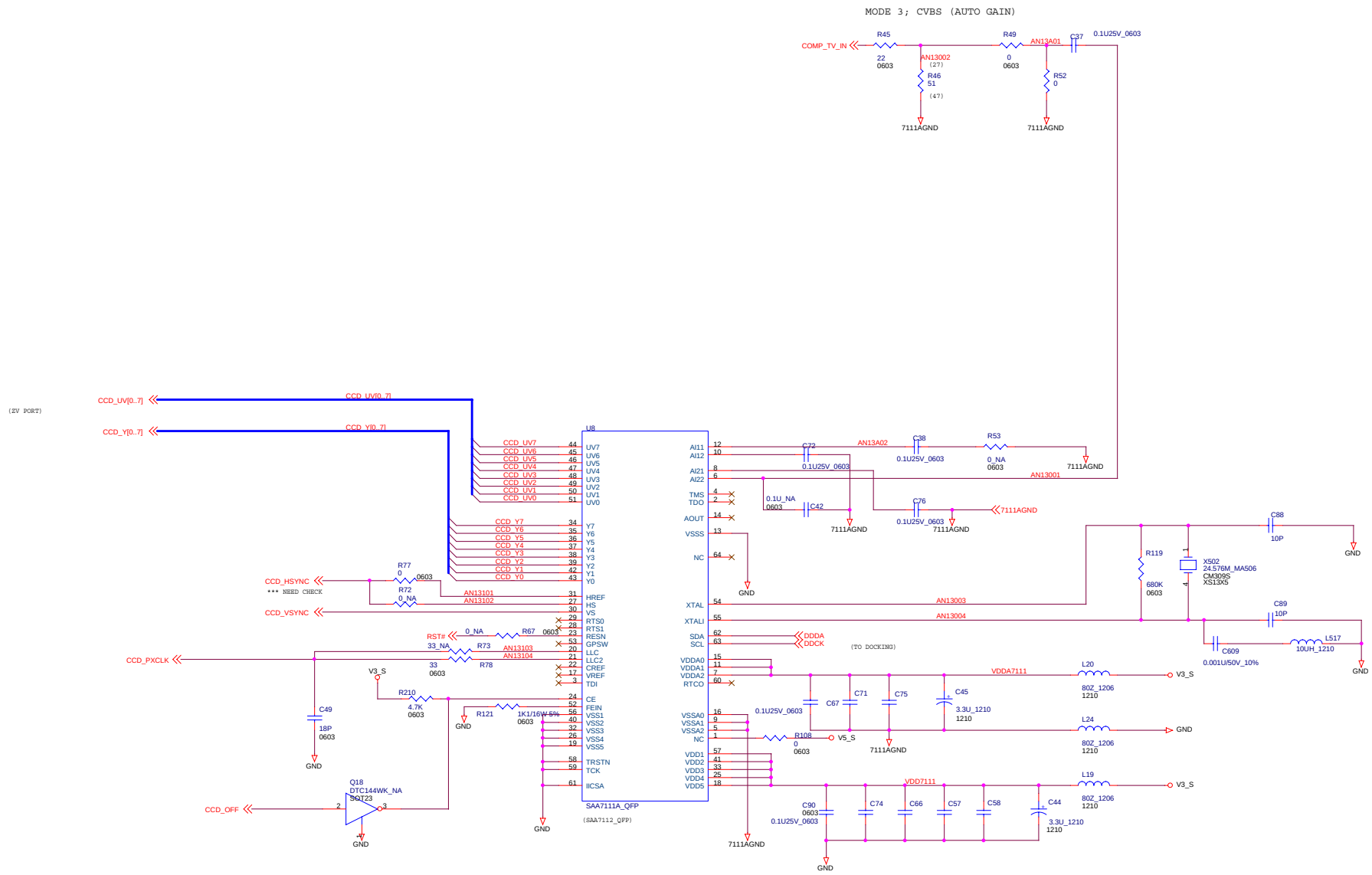
MODE 3; CVBS (AUTO GAIN)
COMP_TV_IN
R45
22
0603
AN13002
(27)
R46
51
(47)
7111AGND
R49
0
0603
AN13A01
C37
0.1U25V_0603
R52
0
7111AGND
(2V PORT)
CCD_UV[0..7]
CCD_Y[0..7]
U8
CCD_UV7
CCD_UV6
CCD_UV5
CCD_UV4
CCD_UV3
CCD_UV2
CCD_UV1
CCD_UV0
CCD_Y7
CCD_Y6
CCD_Y5
CCD_Y4
CCD_Y3
CCD_Y2
CCD_Y1
CCD_Y0
CCD_HSYNC
*** NEED CHECK
R77
0
0603
R72
0_NA
AN13101
AN13102
CCD_VSYNC
RST#
0_NA
R67
0603
33_NA
R73
AN13103
AN13104
CCD_PXCLK
R78
33
0603
V3_S
R210
4.7K
0603
C49
18P
0603
GND
R121
1K1/16W 5%
0603
Q18
DTC144WK_NA
SOT23
CCD_OFF
UV7
UV6
UV5
UV4
UV3
UV2
UV1
UV0
Y7
Y6
Y5
Y4
Y3
Y2
Y1
Y0
HREF
HS
VS
RTS0
RTS1
RESN
GPSW
LLC
LLC2
CREF
VREF
TDI
CE
FEIN
VSS1
VSS2
VSS3
VSS4
VSS5
TRSTN
TCK
IICSA
SAA7111A_QFP
(SAA7112_QFP)
AI11
AI12
AI21
AI22
TMS
TDO
AOUT
VSSS
NC
XTAL
XTALI
SDA
SCL
VDDA0
VDDA1
VDDA2
RTCO
VSSA0
VSSA1
VSSA2
NC
VDD1
VDD2
VDD3
VDD4
VDD5
C72
0.1U25V_0603
AN13A02
C38
0.1U25V_0603
R53
0_NA
0603
7111AGND
AN13001
0.1U_NA
0603
C42
7111AGND
C76
0.1U25V_0603
7111AGND
DDDA
DDCK
(TO DOCKING)
AN13003
AN13004
R119
680K
0603
X502
24.576M_MA506
CM309S
XS13XS
C88
10P
GND
C89
10P
C609
0.001U/50V_10%
L517
10UH_1210
VDDA7111
L20
80Z_1206
1210
V3_S
C67
C71
C75
C45
3.3U_1210
1210
0.1U25V_0603
L24
GND
R108
0
0603
V5_S
7111AGND
VDD7111
L19
80Z_1206
1210
V3_S
C90
0603
0.1U25V_0603
C74
C66
C57
C58
C44
3.3U_1210
1210
7111AGND
GND
FRONTEND SAA7110
InnoLabs Corp.
Title
VIDEO INPUT DECODER
Size
C
Document Number
Model-1000
Rev
01BA
Date:
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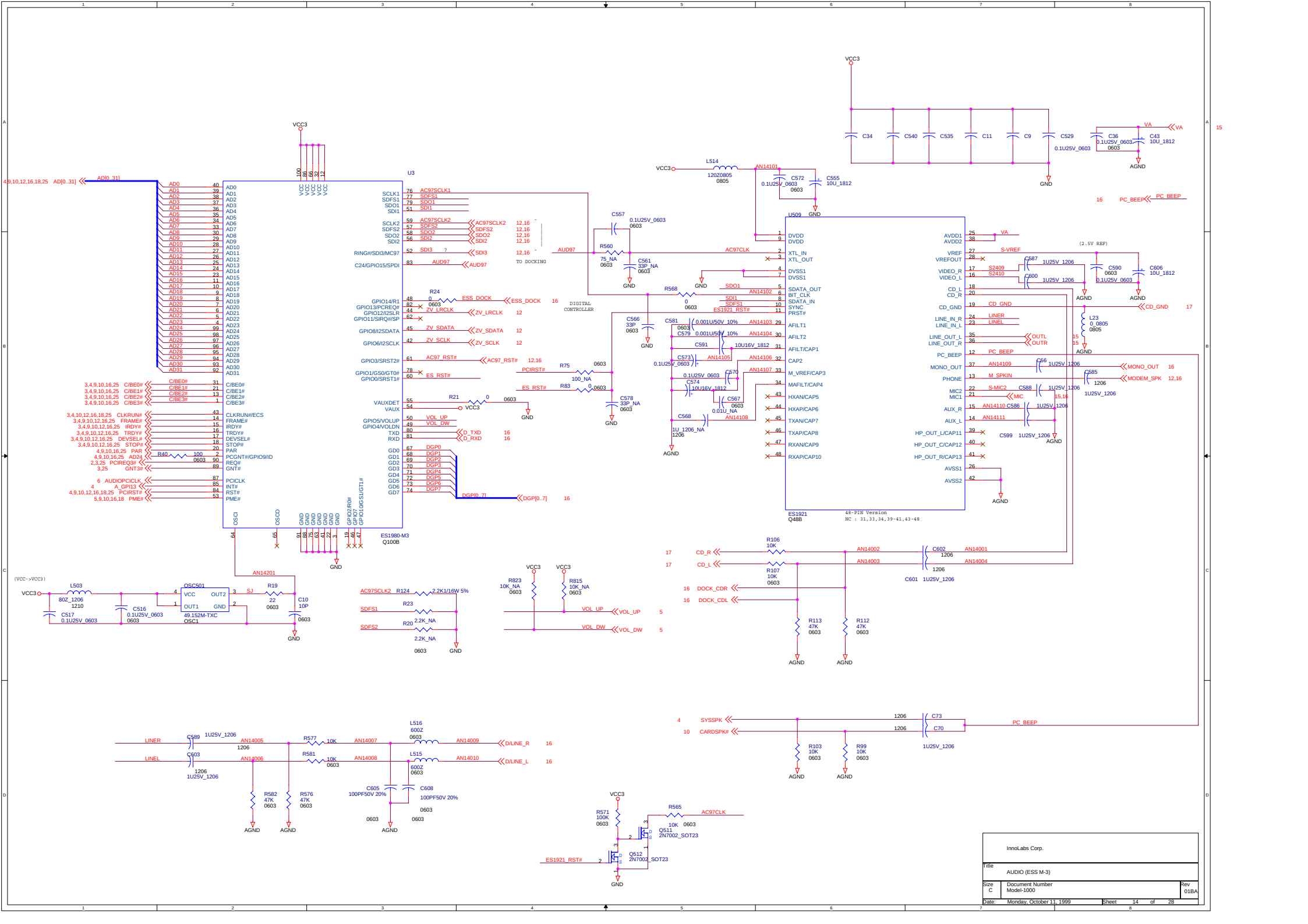
28


U3
ES1980-M3
Q100B
U509
ES1921
Q48B
48-PIN Version
NC : 31,33,34,39-41,43-48
OSC501
49.152M-TXC
OSC1
InnoLabs Corp.
Title
AUDIO (ESS M-3)
Size
C
Document Number
Model-1000
Rev
01BA
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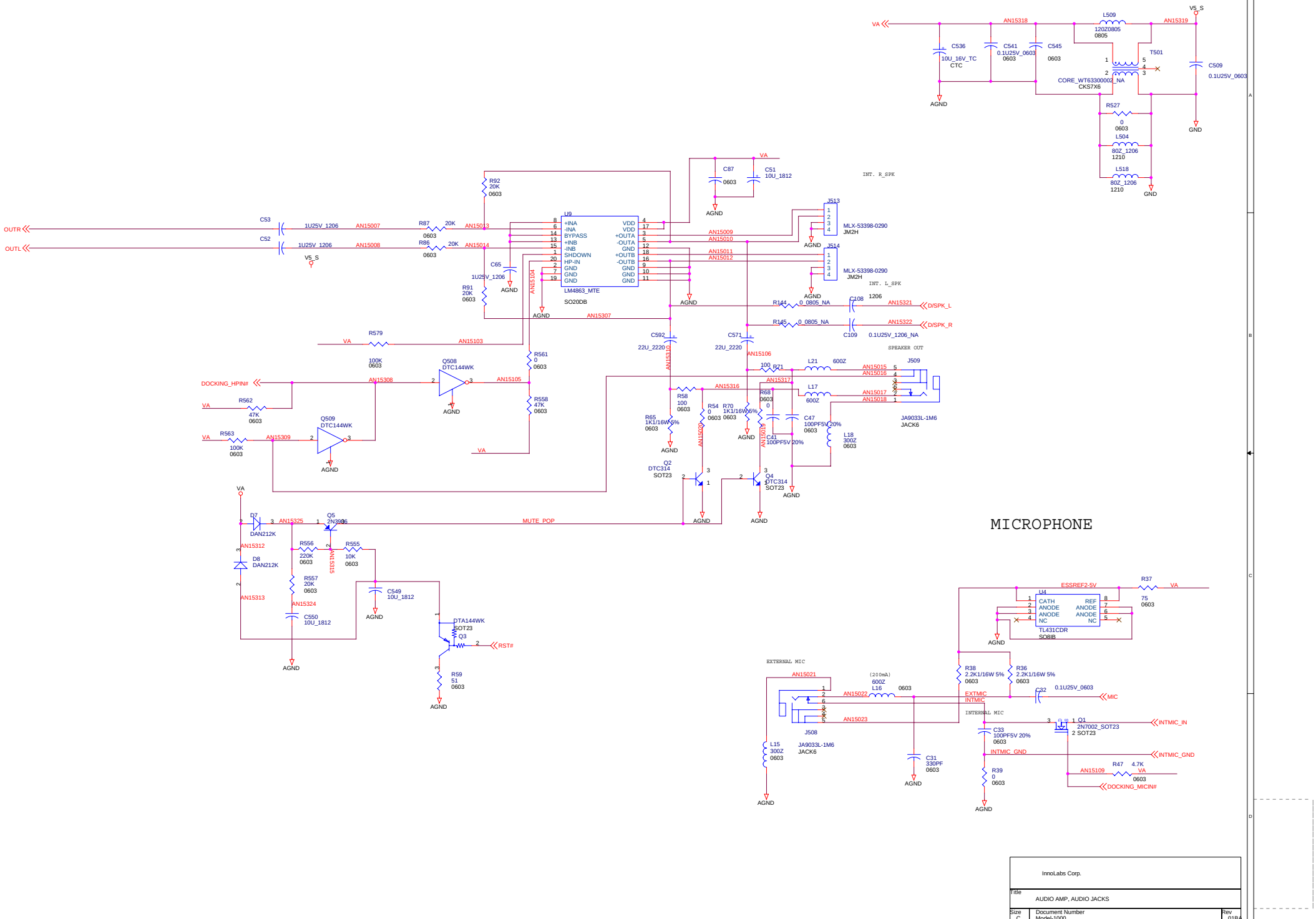
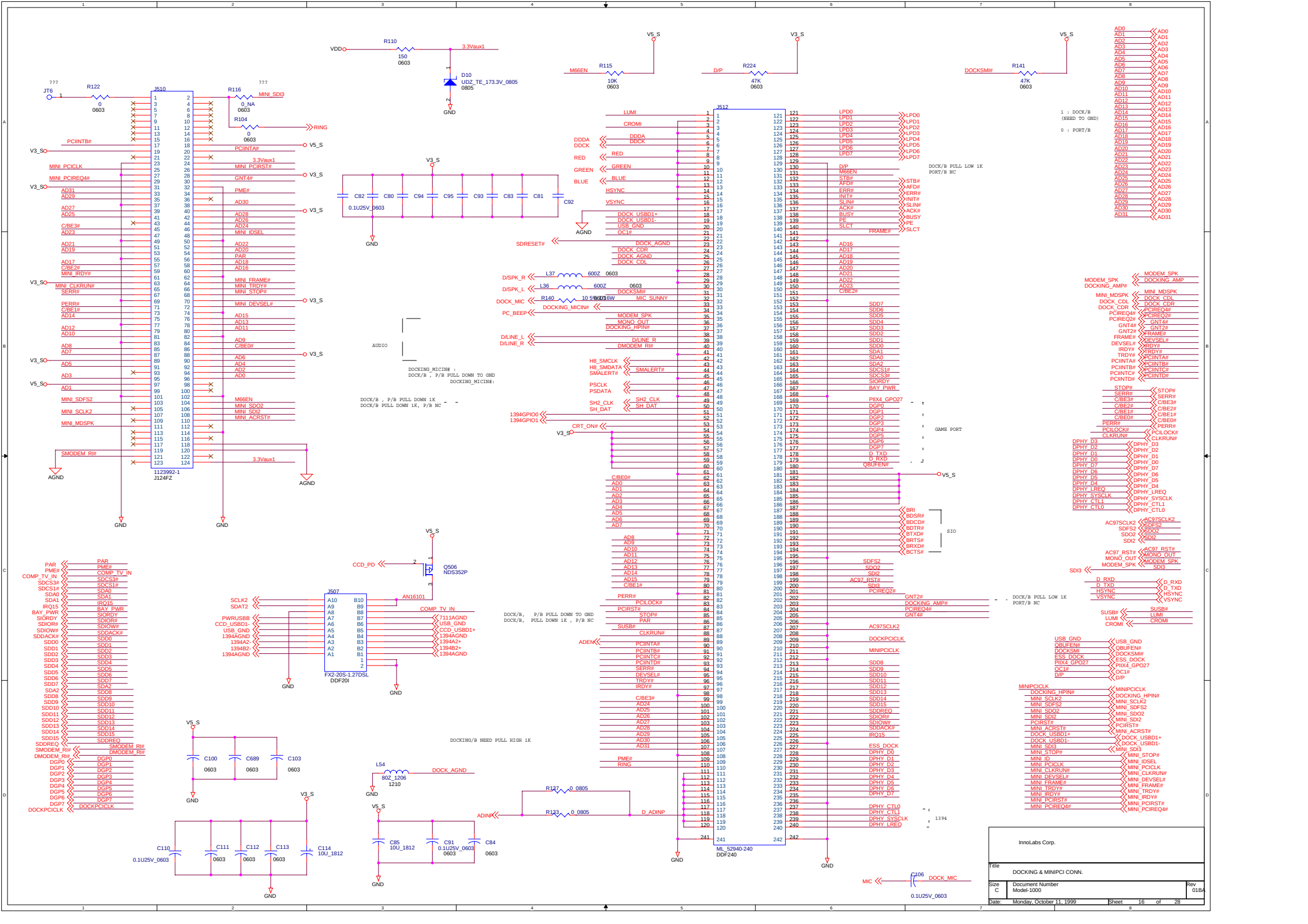

MICROPHONE
U9
LM4863_MTE
SO20DB
+INA
-INA
BYPASS
+INB
-INB
SHDOWN
HP-IN
GND
VDD
+OUTA
-OUTA
+OUTB
-OUTB
OUTR
OUTL
DOCKING_HPIN#
MUTE_POP
RST#
EXTERNAL MIC
INTERNAL MIC
INT. R_SPK
INT. L_SPK
SPEAKER OUT
JA9033L-1M6
JACK6
MLX-53398-0290
JM2H
TL431CDR
SO8IB
DTC144WK
DTC314
SOT23
DAN212K
2N3906
2N7002_SOT23
CORE_WT63300002_NA
CKS7X6
InnoLabs Corp.
AUDIO AMP, AUDIO JACKS
Model-1000
Monday, October 11, 1999


R110
150
0603
VDD
3.3Vaux1
D10
UDZ_TE_173.3V_0805
0805
GND
V5_S
M66EN
R115
10K
0603
V3_S
D/P
R224
47K
0603
DOCKSMI#
R141
47K
0603
1 : DOCK/B
(NEED TO GND)
0 : PORT/B
JT6
R122
0
0603
R116
0_NA
0603
R104
0
0603
MINI_SDI3
RING
???
J510
1123992-1
J124FZ
PCIINTB#
PCIINTA#
MINI_PCICLK
MINI_PCIREQ4#
MINI_PCIRST#
GNT4#
PME#
MINI_IDSEL
MINI_SMBDAT
MINI_SMBCLK
MINI_CLKRUN#
SERR#
MINI_DEVSEL#
MINI_FRAME#
MINI_TRDY#
MINI_STOP#
PERR#
C/BE1#
MINI_IRDY#
C/BE2#
C/BE3#
MINI_SDFS2
MINI_SCLK2
MINI_MDSPK
SMODEM_RI#
M66EN
MINI_SDO2
MINI_SDI2
MINI_ACRST#
AGND
3.3Vaux1
V5_S
V3_S
AD31
AD29
AD27
AD25
AD23
AD21
AD19
AD17
AD15
AD13
AD11
AD9
AD8
AD7
AD6
AD4
AD2
AD0
AD5
AD3
AD1
C/BE0#
C/BE#
C/BE2#
J512
ML_52940-240
DDF240
LUMI
CROMI
DDDA
DDCK
RED
GREEN
BLUE
HSYNC
VSYNC
DOCK_USBD1+
DOCK_USBD1-
USB_GND
OC1#
DOCK_AGND
DOCK_CDR
DOCK_AGND
DOCK_CDL
SDRESET#
AGND
C82
C80
C94
C95
C93
C83
C81
C92
0.1U25V_0603
L37
600Z
0603
L36
600Z
0603
D/SPK_R
D/SPK_L
DOCK_MIC
R140
10 5%
0603
DOCKING_MICIN#
PC_BEEP
MODEM_SPK
MONO_OUT
DOCKING_HPIN#
MIC_SUNNY
DOCKSMI#
D/LINE_L
D/LINE_R
DMODEM_RI#
AUDIO
DOCKING_MICIN# :
DOCK/B , P/B PULL DOWN TO GND
DOCKING_MICIN#:
DOCK/B , P/B PULL DOWN 1K
DOCK/B PULL DOWN 1K, P/B NC
H8_SMCLK
H8_SMDATA
SMALERT#
PSCLK
PSDATA
SH2_CLK
SH_DAT
1394GPIO0
1394GPIO1
CRT_ON#
LPD0
LPD1
LPD2
LPD3
LPD4
LPD5
LPD6
LPD7
STB#
AFD#
ERR#
INIT#
SLIN#
ACK#
BUSY
PE
SLCT
FRAME#
AD16
AD17
AD18
AD19
AD20
AD21
AD22
AD23
C/BE2#
SDD7
SDD6
SDD5
SDD4
SDD3
SDD2
SDD1
SDD0
SDA1
SDA0
SDCS1#
SDCS3#
BAY_PWR
PIIX4_GPO27
DGP0
DGP1
DGP2
DGP3
DGP4
DGP5
DGP6
DGP7
D_TXD
D_RXD
QBUFEN#
GAME PORT
BRI
BDSR#
BDCD#
BDTR#
BTXD#
BRTS#
BRXD#
BCTS#
SIO
SDFS2
SDO2
SDI2
AC97_RST#
SDI3
PCIREQ2#
GNT2#
DOCKING_AMP#
PCIREQ4#
GNT4#
DOCK/B PULL LOW 1K
PORT/B NC
AC97SCLK2
DOCKPCICLK
MINIPCICLK
ESS_DOCK
DPHY_D0
DPHY_D1
DPHY_D2
DPHY_D3
DPHY_D4
DPHY_D5
DPHY_D6
DPHY_D7
DPHY_CTL0
DPHY_CTL1
DPHY_SYSCLK
DPHY_LREQ
1394
MODEM_SPK
DOCKING_AMP#
MINI_MDSPK
DOCK_CDL
DOCK_CDR
PCIREQ4#
PCIREQ2#
GNT4#
GNT2#
FRAME#
DEVSEL#
IRDY#
TRDY#
PCIINTA#
PCIINTB#
PCIINTC#
PCIINTD#
STOP#
SERR#
C/BE3#
C/BE1#
PERR#
PCILOCK#
CLKRUN#
DPHY_D3
DPHY_D2
DPHY_D1
DPHY_D0
DPHY_D7
DPHY_D6
DPHY_D5
DPHY_D4
DPHY_LREQ
DPHY_SYSCLK
DPHY_CTL1
DPHY_CTL0
AC97SCLK2
SDFS2
SDO2
SDI2
AC97_RST#
MONO_OUT
MODEM_SPK
SDI3
D_RXD
D_TXD
HSYNC
VSYNC
SUSB#
LUMI
CROMI
USB_GND
QBUFEN#
DOCKSMI#
ESS_DOCK
PIIX4_GPO27
OC1#
D/P
MINIPCICLK
DOCKING_HPIN#
MINI_SCLK2
MINI_SDFS2
MINI_SDO2
MINI_SDI2
PCIRST#
MINI_ACRST#
DOCK_USBD1+
DOCK_USBD1-
MINI_SDI3
MINI_STOP#
MINI_ID
MINI_PCICLK
MINI_CLKRUN#
MINI_DEVSEL#
MINI_FRAME#
MINI_TRDY#
MINI_IRDY#
MINI_PCIRST#
MINI_PCIREQ4#
Q506
NDS352P
CCD_PD
AN16101
COMP_TV_IN
J507
FX2-20S-1.27DSL
DDF20I
SCLK2
SDAT2
PWRUSBB
CCD_USBD1-
USB_GND
1394AGND
1394A2-
1394B2-
1394AGND
7111AGND
USB_GND
CCD_USBD1+
1394AGND
1394A2+
1394B2+
1394AGND
DOCK/B, P/B PULL DOWN TO GND
DOCK/B, PULL DOWN 1K , P/B NC
PCILOCK#
PCIRST#
STOP#
PAR
SUSB#
CLKRUN#
ADEN
PCIINTA#
PCIINTB#
PCIINTC#
PCIINTD#
SERR#
DEVSEL#
TRDY#
IRDY#
C/BE3#
AD24
AD25
AD26
AD27
AD28
AD29
AD30
AD31
PME#
RING
DOCKING/B NEED PULL HIGH 1K
L54
80Z_1206
1210
DOCK_AGND
R127
0
0805
R123
0
0805
ADINP#
D_ADINP
C100
C689
C103
0603
C110
0.1U25V_0603
C111
C112
C113
C114
10U_1812
C85
10U_1812
C91
0.1U25V_0603
C84
MIC
C106
DOCK_MIC
0.1U25V_0603
InnoLabs Corp.
Title
DOCKING & MINIPCI CONN.
Size
C
Document Number
Model-1000
Rev
01BA
Date:
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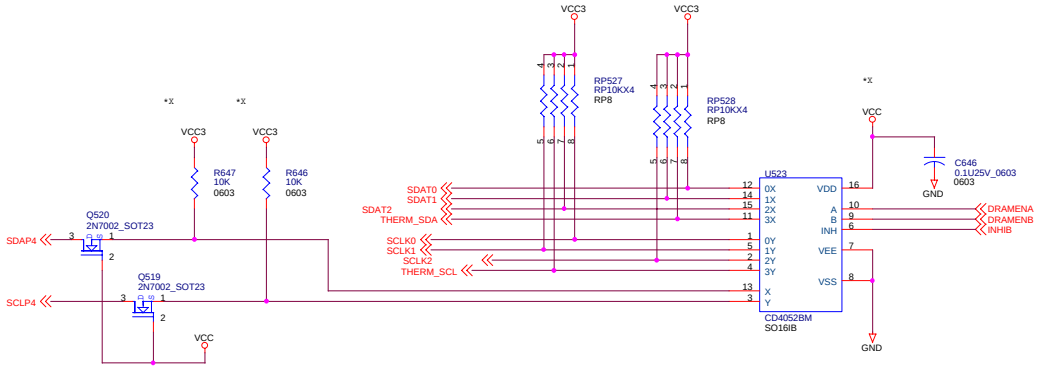
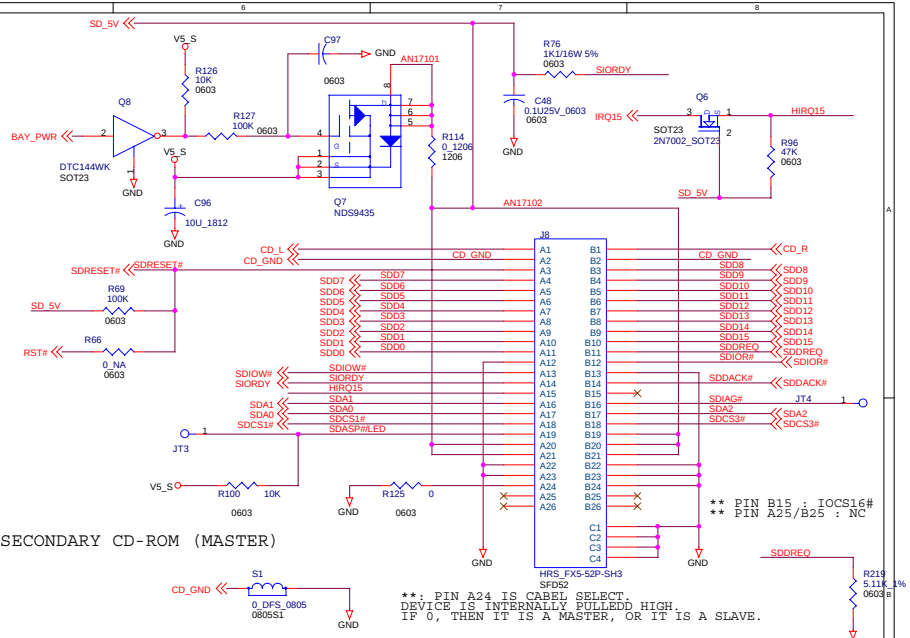
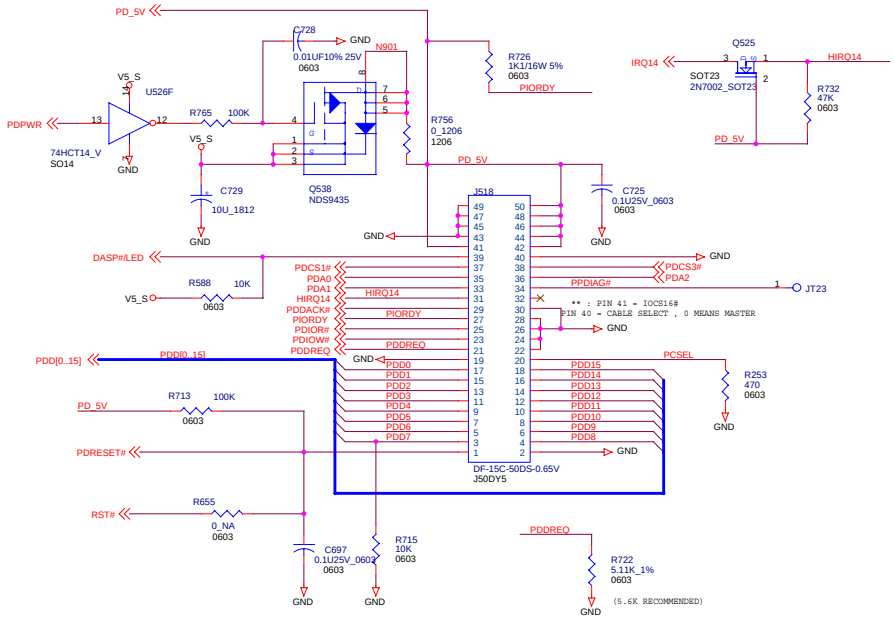
DIMM PD CIRCUITS

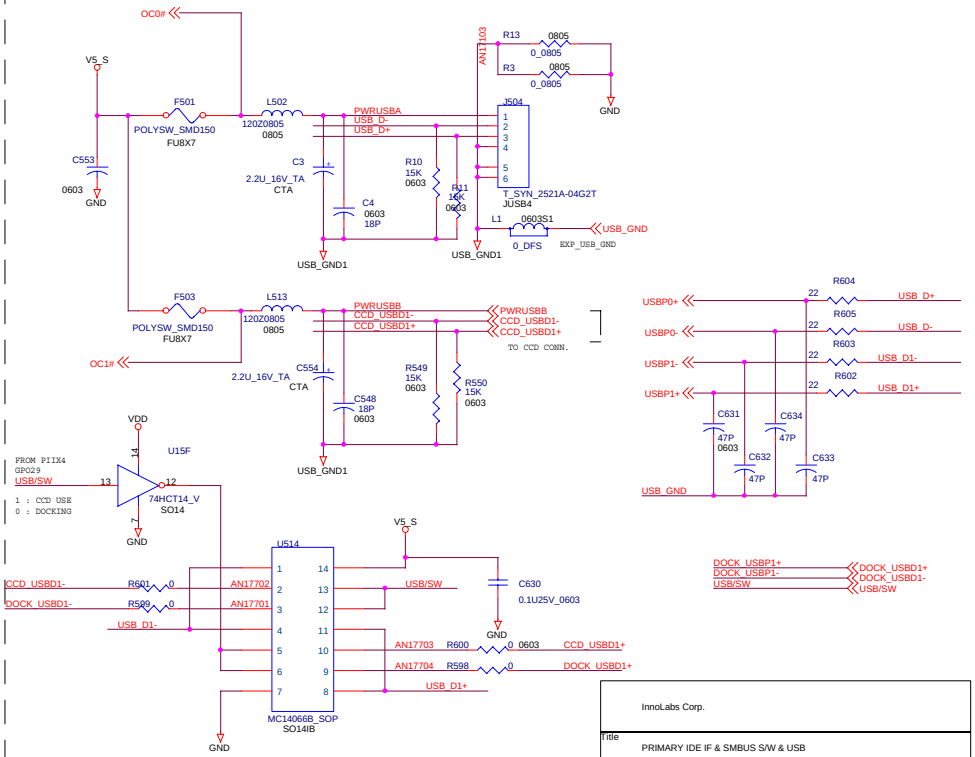




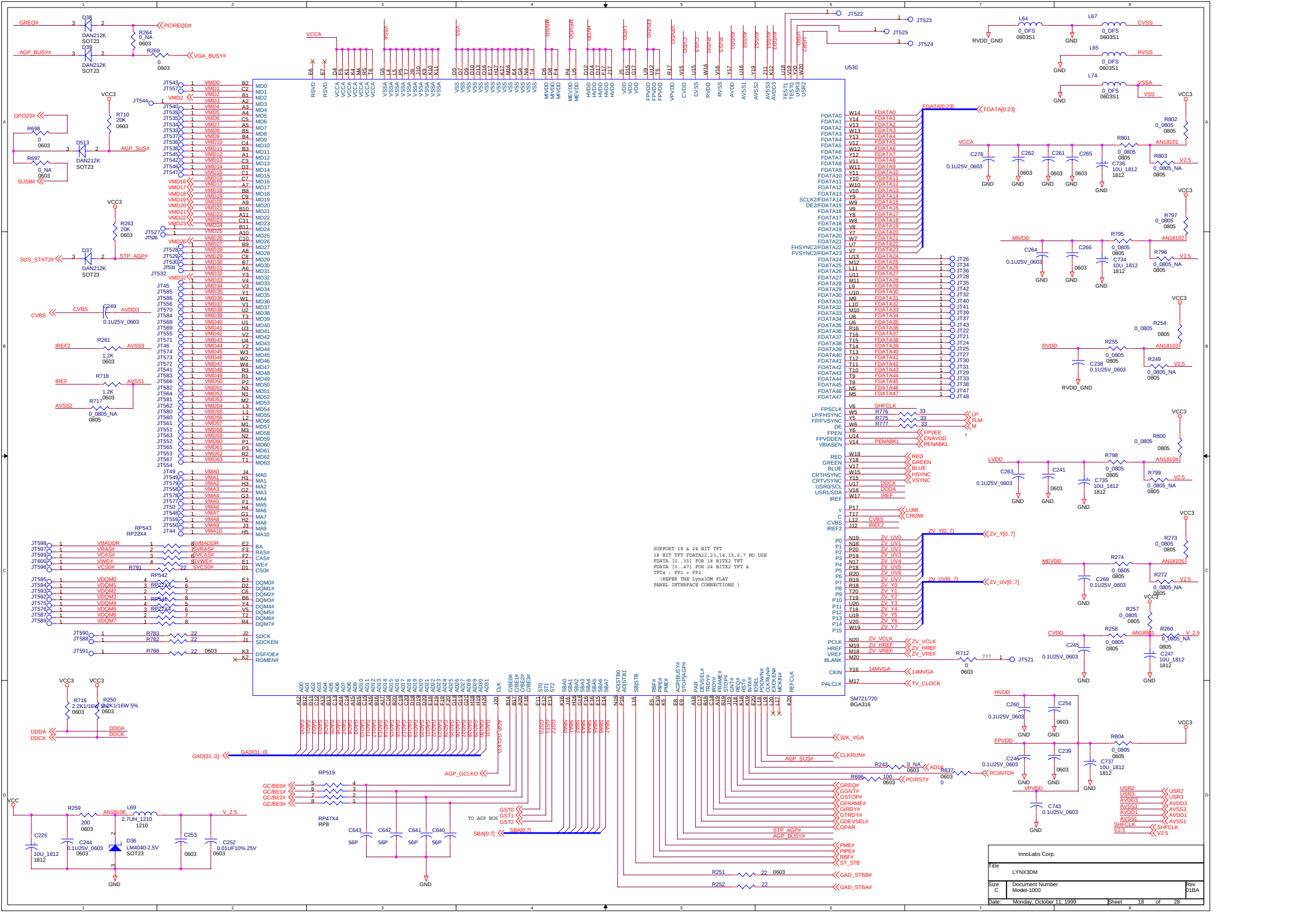
PRIMARY HDD (MASTER)



USB



InnoLabs Corp.		
Title: PRIMARY IDE IF & SMBUS S/W & USB		
Size: C	Document Number: Model-1000	Rev: 01BA
Date: Monday, October 11, 1999	Sheet 17 of 28	


U530
SM721/720
BGA316
SUPPORT 18 & 24 BIT TFT
16 BIT TFT FDATA22,23,14,15,6,7 NO USE
FDATA [0..35] FOR 18 BITX2 TFT
FDATA [0..47] FOR 24 BITX2 TFT &
TFTs : FP1 + FP2
(REFER THE Lynx3DM FLAT
PANEL INTERFACE CONNECTIONS)
TO AGP BUS
InnoLabs Corp.
Title
LYNX3DM
Size C
Document Number
Model-1000
Rev
01BA
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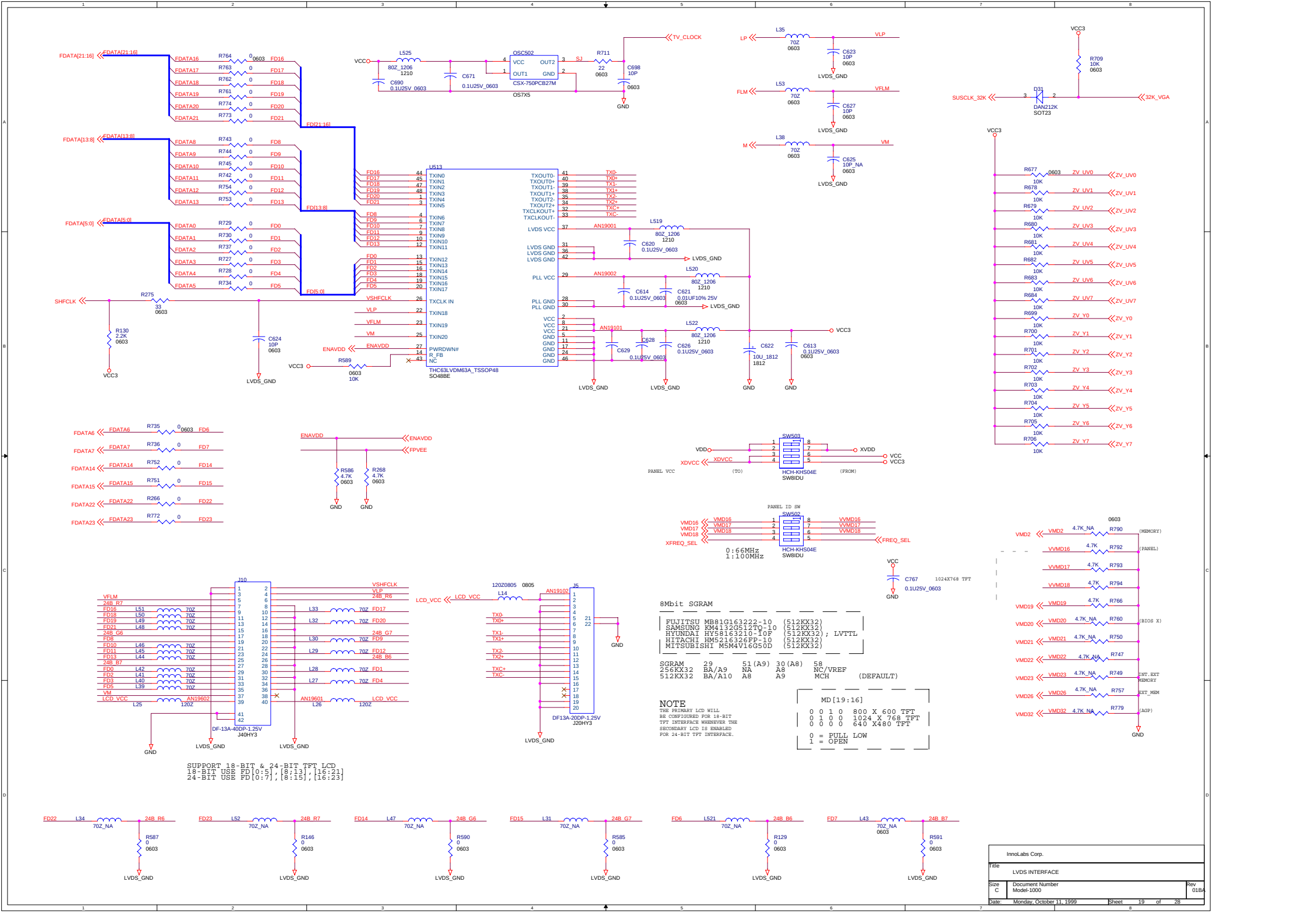
FDATA[21:16]
FDATA[13:8]
FDATA[5:0]
FD[21:16]
FD[13:8]
FD[5:0]
U513
THC63LVDM63A_TSSOP48
SO48BE
OSC502
CSX-750PCB27M
OS7X5
TV_CLOCK
LVDS_GND
VCC3
SHFCLK
ENAVDD
FPVEE
SW503
HCH-KHS04E
SW8IDU
SW502
PANEL ID SW
FREQ_SEL
XFREQ_SEL
0:66MHz
1:100MHz
J10
DF-13A-40DP-1.25V
J40HY3
J5
DF13A-20DP-1.25V
J20HY3
8Mbit SGRAM
FUJITSU MB81G163222-10 (512KX32)
SAMSUNG KM4132G512TQ-10 (512KX32)
HYUNDAI HY58I63210-10F (512KX32); LVTTL
HITACHI HM5216326FP-10 (512KX32)
MITSUBISHI M5M4V16G50D (512KX32)
SGRAM 29 51(A9) 30(A8) 58
256KX32 BA/A9 NA A8 NC/VREF
512KX32 BA/A10 A8 A9 MCH (DEFAULT)
MD[19:16]
0 0 1 0 800 X 600 TFT
0 1 0 0 1024 X 768 TFT
0 0 0 0 640 X480 TFT
0 = PULL LOW
1 = OPEN
NOTE
THE PRIMARY LCD WILL BE CONFIGURED FOR 18-BIT TFT INTERFACE WHENEVER THE SECONDARY LCD IS ENABLED FOR 24-BIT TFT INTERFACE.
SUPPORT 18-BIT & 24-BIT TFT LCD
18-BIT USE FD[0:5],[8:13],[16:21]
24-BIT USE FD[0:7],[8:15],[16:23]
DAN212K
SOT23
SUSCLK_32K
32K_VGA
1024X768 TFT
InnoLabs Corp.
LVDS INTERFACE
Document Number
Model-1000
Rev 01BA
Monday, October 11, 1999
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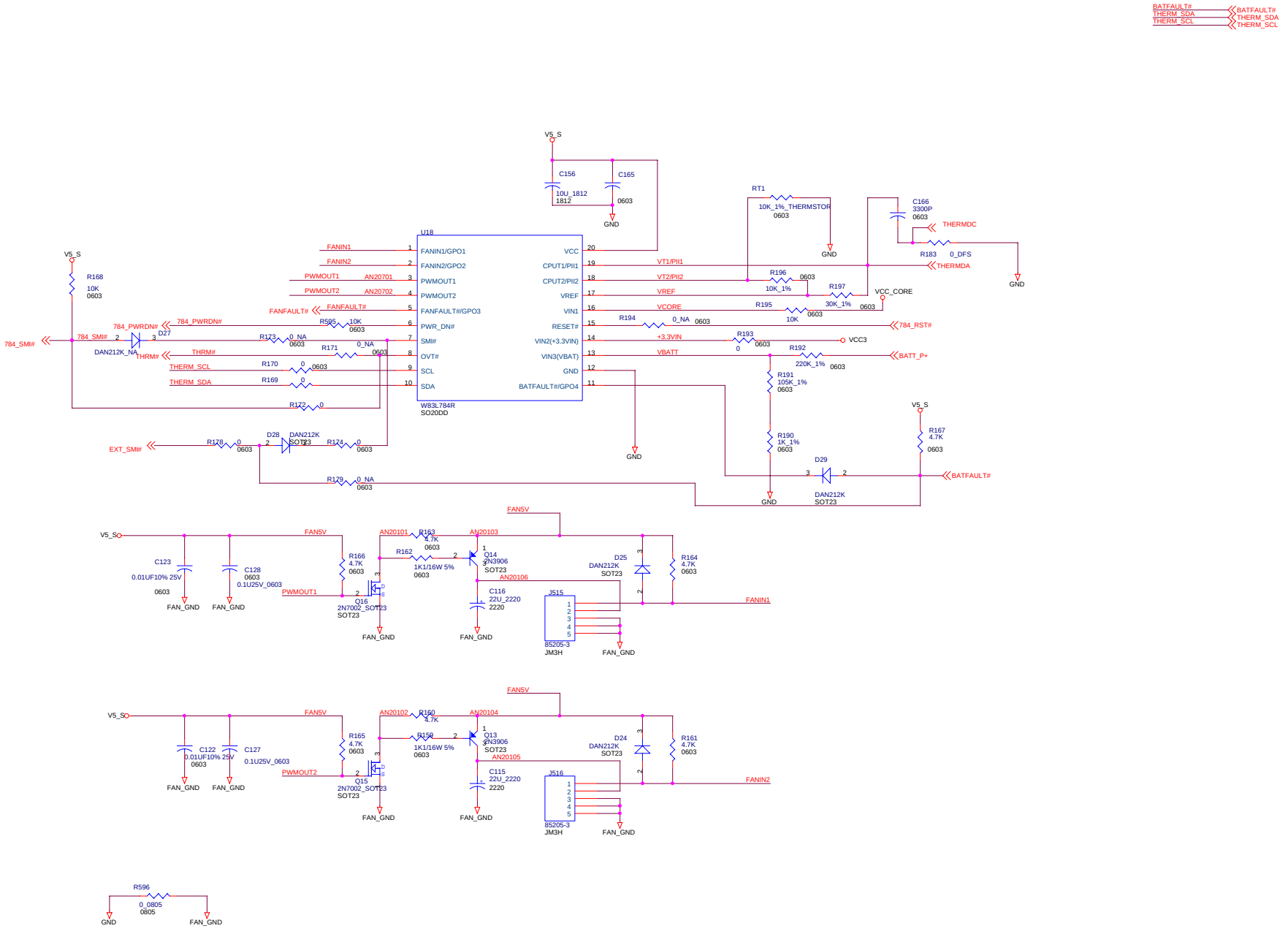
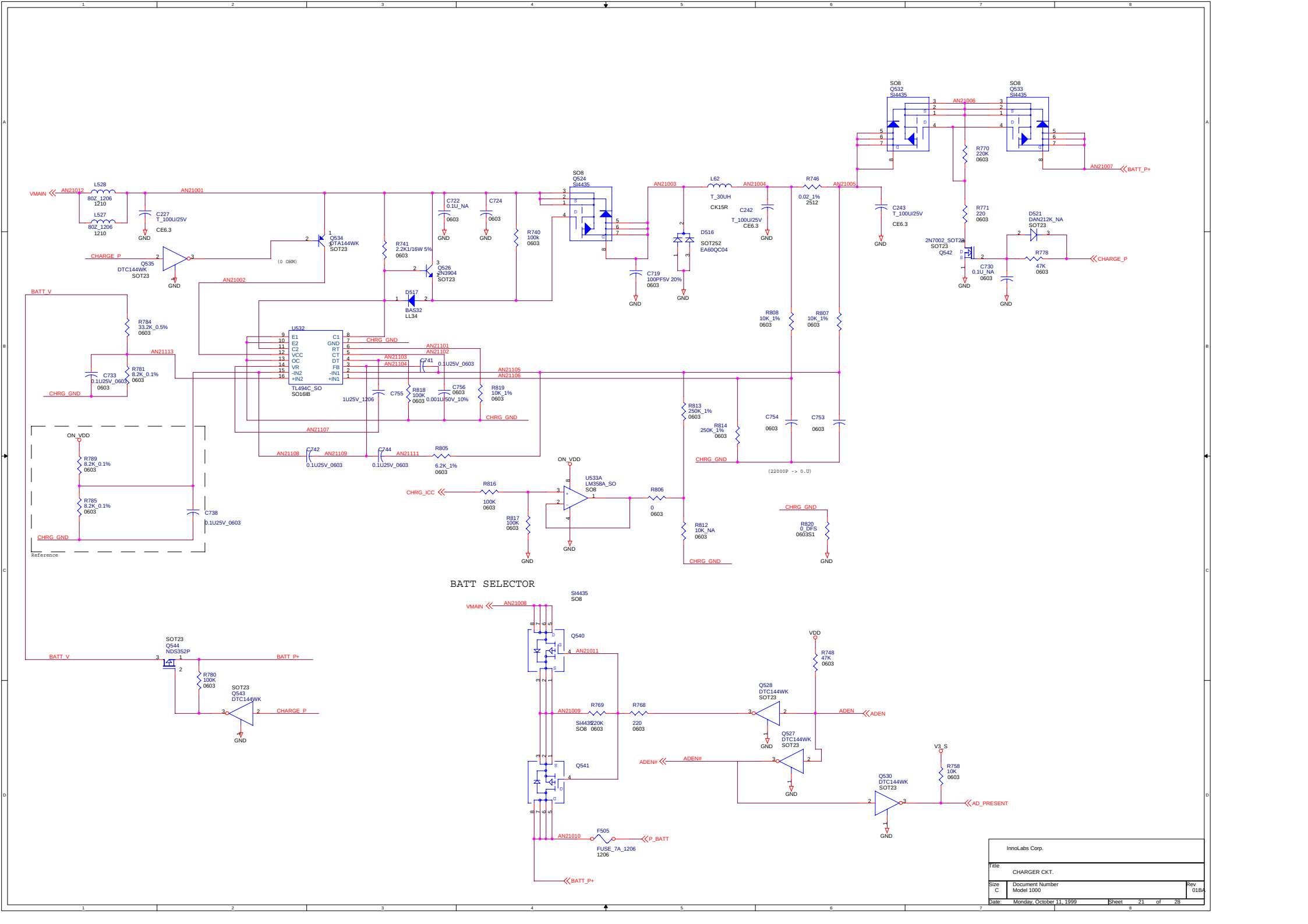


	InnoLabs Corp.	
Title	W83L784R	
Size C	Document Number Model-1000	Rev 01BA
Date: Monday, October 11, 1999	Sheet 20 of 28	


VMAIN
AN21012
L528
80Z_1206
1210
L527
80Z_1206
1210
AN21001
C227
T_100U/25V
CE6.3
GND
CHARGE_P
Q535
DTC144WK
SOT23
AN21002
Q534
DTA144WK
SOT23
(0 OHM)
R741
2.2K1/16W 5%
0603
Q526
2N3904
SOT23
C722
0.1U_NA
0603
C724
0603
R740
100K
0603
D517
BAS32
LL34
SO8
Q524
SI4435
AN21003
C719
100PF5V 20%
0603
L62
T_30UH
CK15R
AN21004
D516
SOT252
EA60QC04
C242
T_100U/25V
CE6.3
R746
0.02_1%
2512
AN21005
C243
T_100U/25V
CE6.3
SO8
Q532
SI4435
SO8
Q533
SI4435
AN21006
R770
220K
0603
AN21007
BATT_P+
R771
220
0603
2N7002_SOT23
SOT23
Q542
D521
DAN212K_NA
SOT23
R778
47K
0603
CHARGE_P
C730
0.1U_NA
0603
BATT_V
R784
33.2K_0.5%
0603
AN21113
C733
0.1U25V_0603
0603
R781
8.2K_0.1%
0603
CHRG_GND
U532
TL494C_SO
SO16IB
E1
E2
C2
VCC
OC
VR
-IN2
+IN2
C1
GND
RT
CT
DT
FB
-IN1
+IN1
CHRG_GND
AN21101
AN21102
AN21103
AN21104
C741
0.1U25V_0603
AN21105
AN21106
1U25V_1206
C755
R818
100K
0603
C756
0603
0.001U/50V_10%
R819
10K_1%
0603
CHRG_GND
R808
10K_1%
0603
R807
10K_1%
0603
R813
250K_1%
0603
R814
250K_1%
0603
C754
0603
C753
0603
CHRG_GND
(22000P -> 0.U)
AN21107
AN21108
C742
0.1U25V_0603
AN21109
C744
0.1U25V_0603
AN21111
R805
6.2K_1%
0603
ON_VDD
R789
8.2K_0.1%
0603
R785
8.2K_0.1%
0603
C738
0.1U25V_0603
CHRG_GND
Reference
CHRG_ICC
R816
100K
0603
U533A
LM358A_SO
SO8
R817
100K
0603
R806
0
0603
R812
10K_NA
0603
CHRG_GND
R820
0_DFS
0603S1
CHRG_GND
BATT SELECTOR
SOT23
Q544
NDS352P
BATT_V
BATT_P+
R780
100K
0603
SOT23
Q543
DTC144WK
CHARGE_P
GND
SI4435
SO8
VMAIN
AN21008
Q540
AN21011
Q541
AN21009
R769
SI4435220K
SO8 0603
R768
220
0603
VDD
R748
47K
0603
Q528
DTC144WK
SOT23
ADEN
ADEN
Q527
DTC144WK
SOT23
ADEN#
ADEN#
V3_S
R758
10K
0603
Q530
DTC144WK
SOT23
AD_PRESENT
AN21010
F505
FUSE_7A_1206
1206
P_BATT
BATT_P+
InnoLabs Corp.
Title
CHARGER CKT.
Size
C
Document Number
Model 1000
Rev
01BA
Date:
Monday, October 11, 1999
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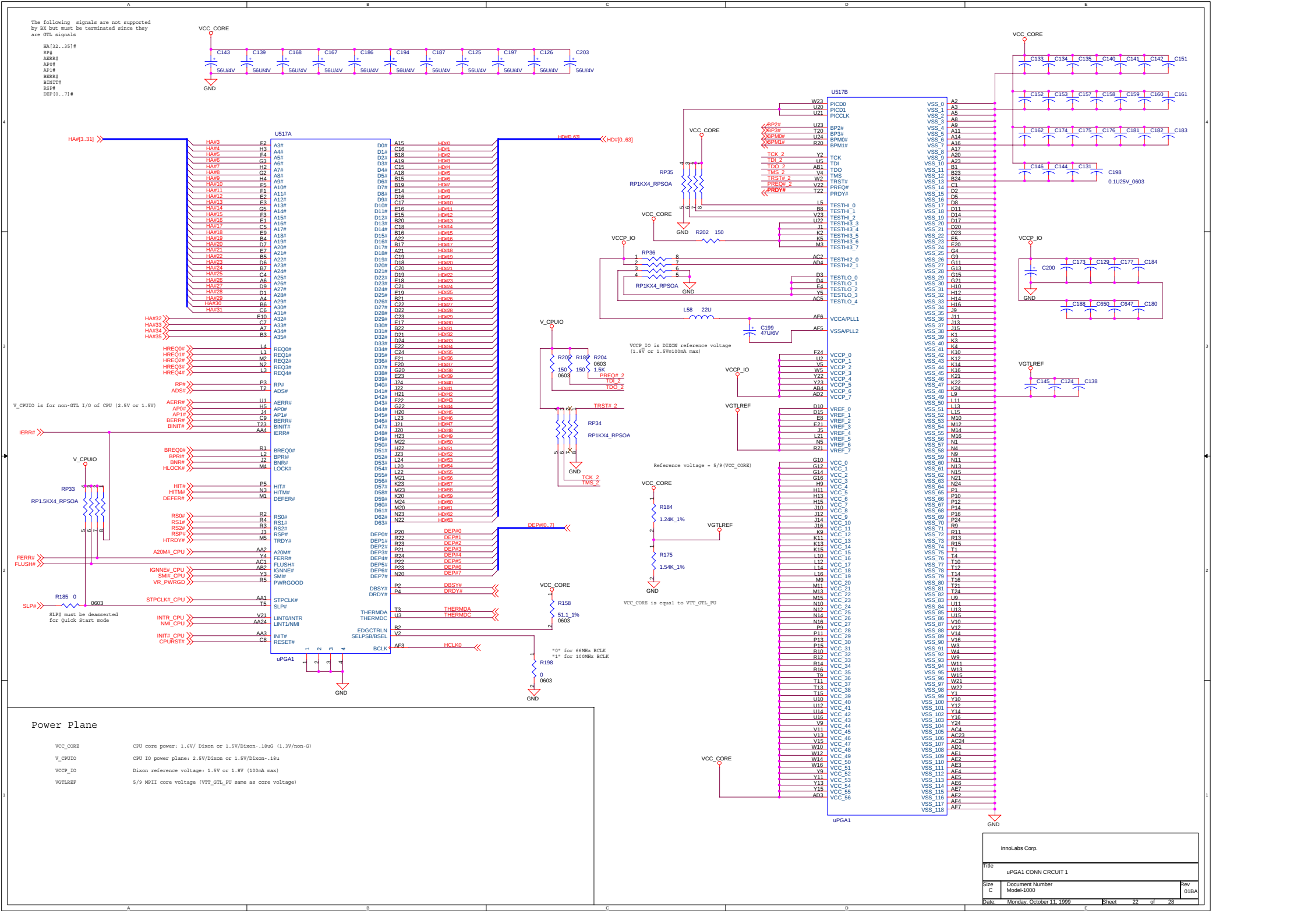
of
28

The following signals are not supported by BX but must be terminated since they are GTL signals

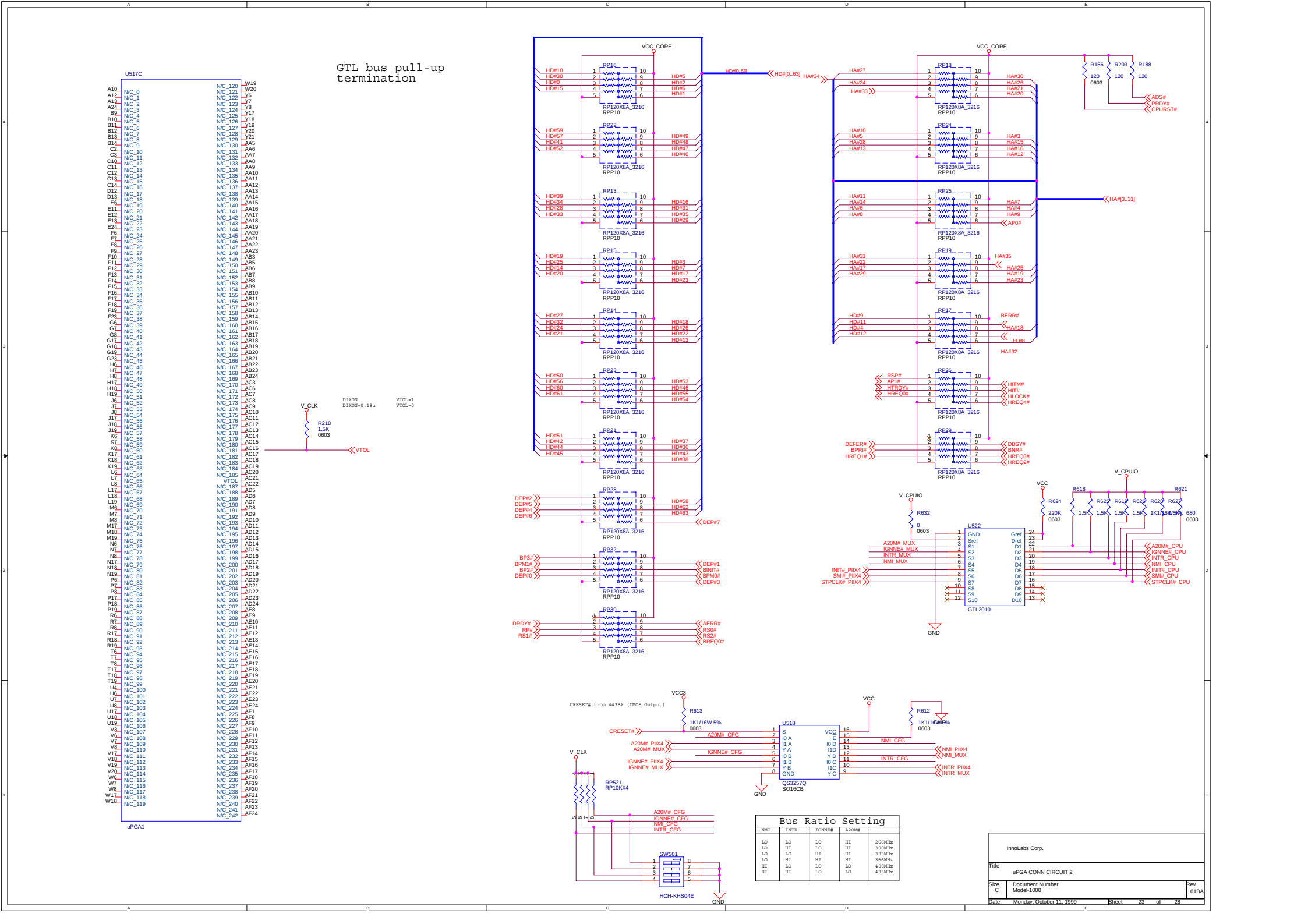
HA[32..35]#
RP#
AERR#
AP0#
AP1#
BERR#
BINIT#
BSP#
DEP[0..7]#



Power Plane

VCC_CORE	CPU core power: 1.6V/ Dixon or 1.5V/Dixon-.18uG (1.3V/non-G)
V_CPUIO	CPU IO power plane: 2.5V/Dixon or 1.5V/Dixon-.18u
VCCP_IO	Dixon reference voltage: 1.5V or 1.8V (100mA max)
VGTLREF	5/9 MPII core voltage (VTT_GTL_PU same as core voltage)

Company	InnoLabs Corp.
Title	uPGA1 CONN CRCUIT 1
Size	C
Document Number	Model-1000
Rev	01BA
Date:	Monday, October 11, 1999
Sheet	22 of 28


GTL bus pull-up termination
U517C
uPGA1
VCC_CORE
RP16
RP22
RP13
RP15
RP14
RP23
RP21
RP28
RP32
RP30
RP120X8A_3216
RPP10
HD#0_63
HD#[0..63]
HA#34
RP18
RP24
RP25
RP19
RP17
RP26
RP29
HA#[3..31]
R156
R203
R188
120
0603
ADS#
PRDY#
CPURST#
RSP#
AP1#
HTRDY#
HREQ0#
HITM#
HIT#
HLOCK#
HREQ4#
DEFER#
BPR#
HREQ1#
DBSY#
BNR#
HREQ3#
HREQ2#
V_CLK
R218
1.5K
0603
VTOL
DIXON
DIXON-0.18u
VTOL=1
VTOL=0
V_CPUIO
R632
0
0603
U522
GTL2010
A20M# MUX
IGNNE# MUX
INTR_MUX
NMI_MUX
INIT#_PIIX4
SMI#_PIIX4
STPCLK#_PIIX4
GND
VCC
R624
220K
0603
R618
R625
R619
R620
R622
R623
1.5K
1K/16W 5%
680
A20M#_CPU
IGNNE#_CPU
INTR_CPU
NMI_CPU
INIT#_CPU
SMI#_CPU
STPCLK#_CPU
VCC3
R613
1K1/16W 5%
0603
CRESET# from 443BX (CMOS Output)
CRESET#
A20M#_CFG
A20M#_PIIX4
A20M#_MUX
IGNNE#_CFG
IGNNE#_PIIX4
IGNNE#_MUX
U518
QS3257Q
SO16CB
R612
NMI_CFG
INTR_CFG
NMI_PIIX4
NMI_MUX
INTR_PIIX4
INTR_MUX
RP521
RP10KX4
SW501
HCH-KHS04E
Bus Ratio Setting
NMI
INTR
IGNNE#
A20M#
266MHz
300MHz
333MHz
366MHz
400MHz
433MHz
InnoLabs Corp.
Title
uPGA CONN CIRCUIT 2
Size
C
Document Number
Model-1000
Rev
01BA
Date:
Monday, October 11, 1999
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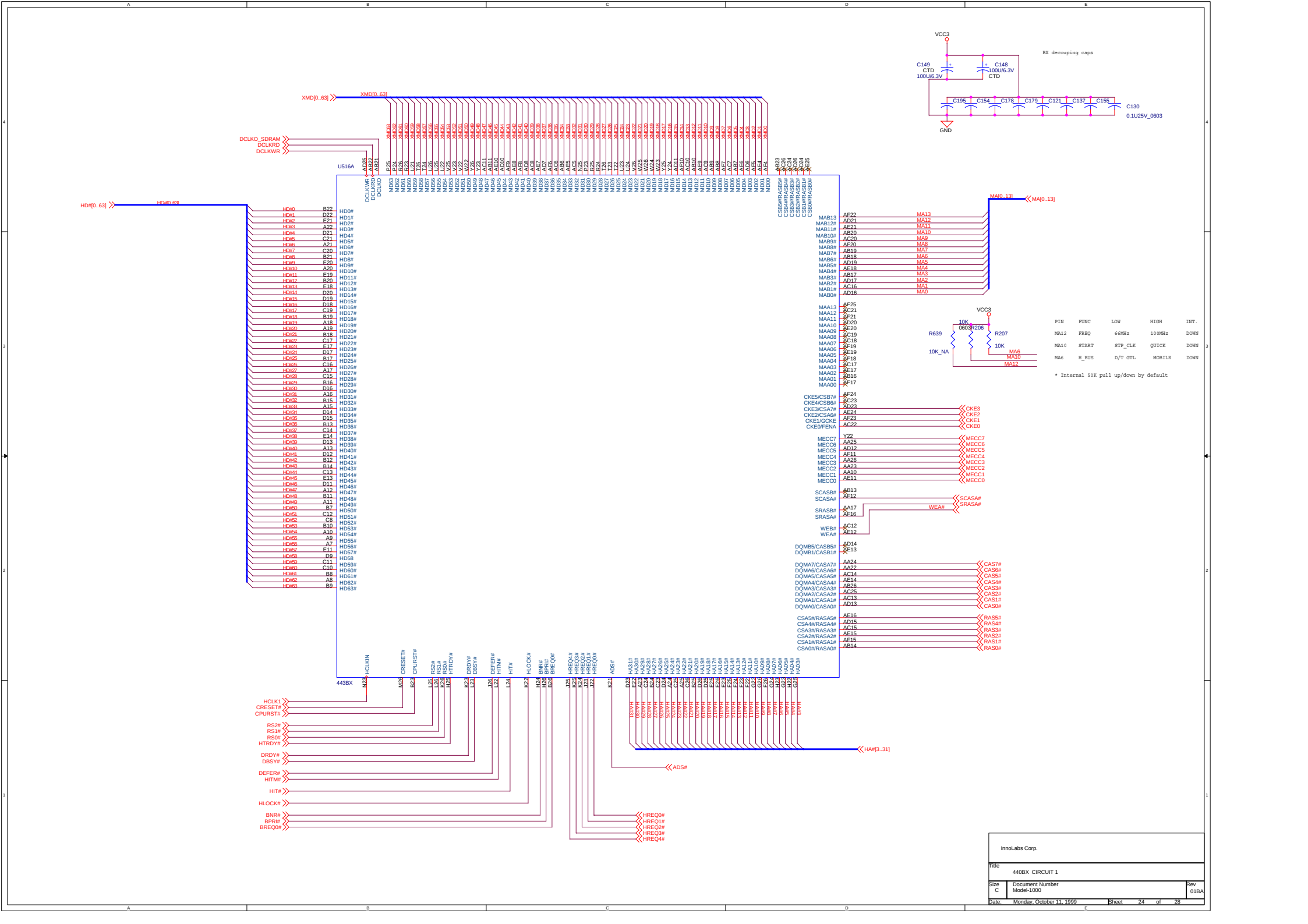
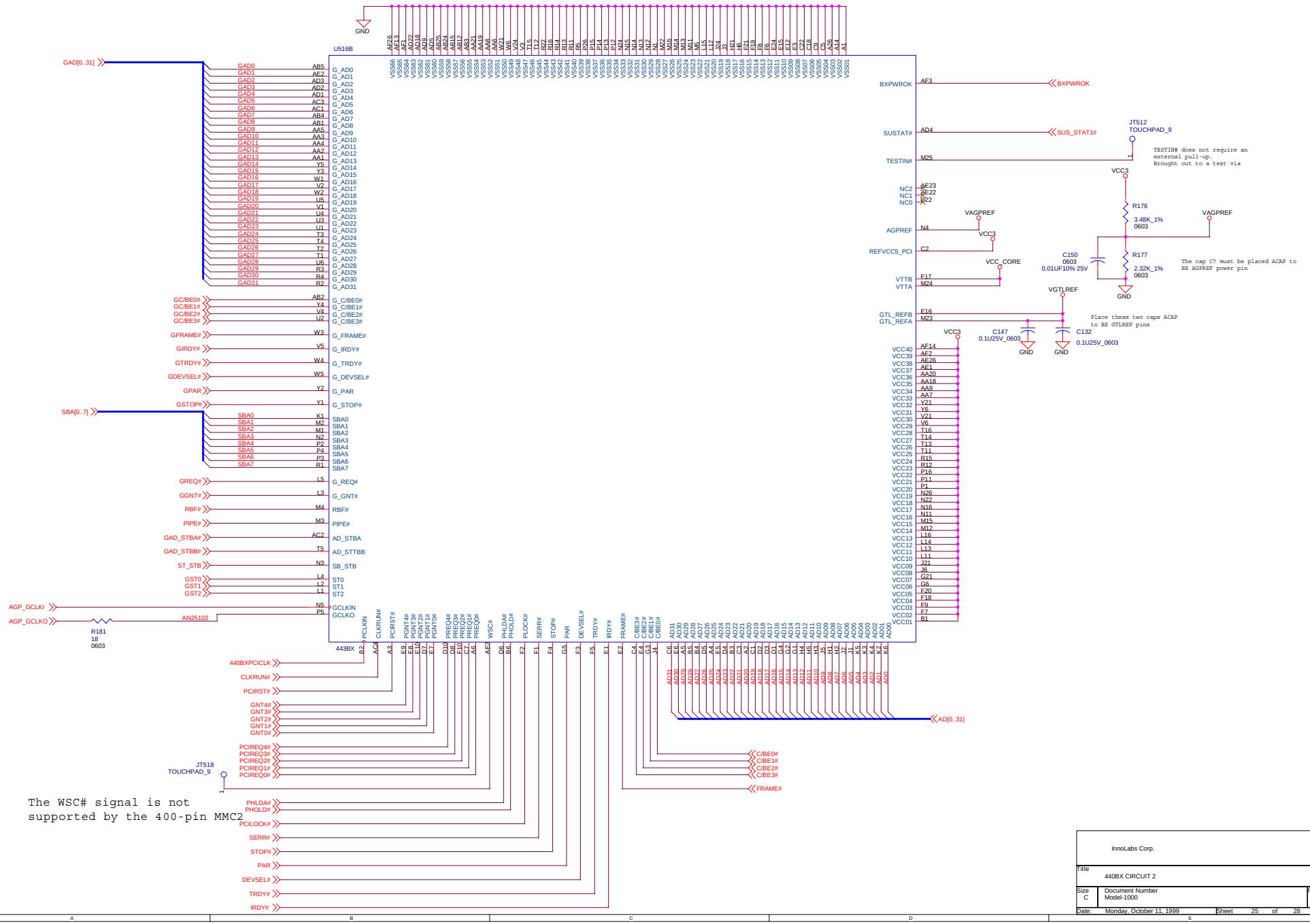
28



Company	InnoLabs Corp.
Title	440BX CIRCUIT 1
Size	C
Document Number	Model-1000
Rev	01BA
Date:	Monday, October 11, 1999
Sheet	24 of 28


U516B
443BX
GAD[0..31]
SBA[0..7]
GND
VCC3
VAGPREF
VCC_CORE
VGTLREF
JT512
TOUCHPAD_9
TESTIN# does not require an external pull-up. Brought out to a test via
R176
3.48K_1%
0603
R177
2.32K_1%
0603
C150
0603
0.01UF10% 25V
The cap C? must be placed ACAP to BX AGPREF power pin
C147
0.1U25V_0603
C132
0.1U25V_0603
Place these two caps ACAP to BX GTLREF pins
BXPWROK
SUS_STAT1#
AGP_GCLKI
AGP_GCLKO
AN25102
R181
18
0603
440BXPCICLK
CLKRUN#
PCIRST#
GNT4#
GNT3#
GNT2#
GNT1#
GNT0#
PCIREQ4#
PCIREQ3#
PCIREQ2#
PCIREQ1#
PCIREQ0#
JT518
TOUCHPAD_9
PHLDA#
PHOLD#
PCILOCK#
SERR#
STOP#
PAR
DEVSEL#
TRDY#
IRDY#
C/BE0#
C/BE1#
C/BE2#
C/BE3#
FRAME#
AD[0..31]
The WSC# signal is not supported by the 400-pin MMC2
InnoLabs Corp.
Title
440BX CIRCUIT 2
Size C
Document Number Model-1000
Rev 01BA
Date: Monday, October 11, 1999
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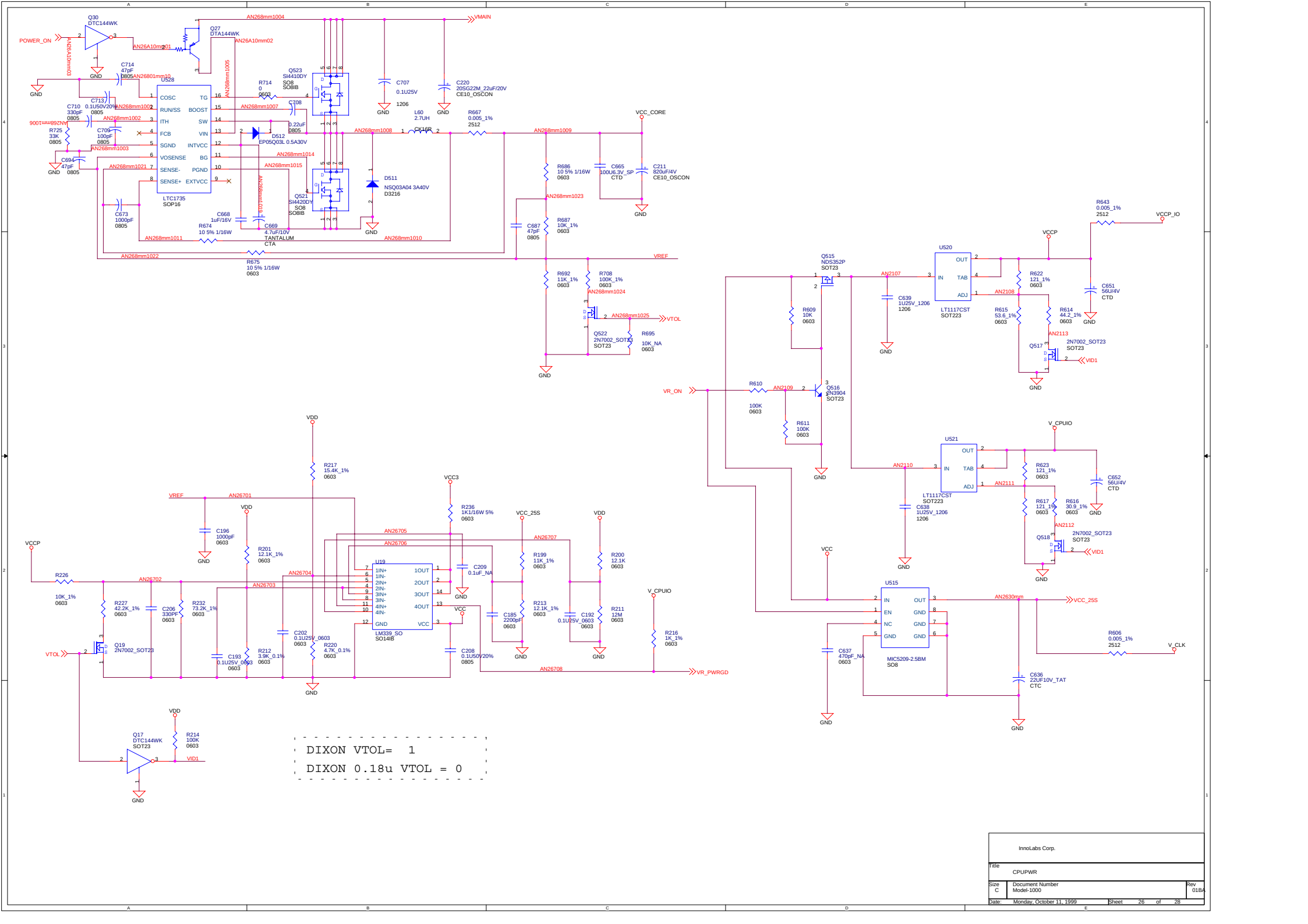
Q30 DTC144WK
POWER_ON
Q27 DTA144WK
AN26A10mm01
AN26A10mm02
AN26A10mm03
GND
C714 47pF 0805
AN26801mm10
U528
COSC
RUN/SS
ITH
FCB
SGND
VOSENSE
SENSE-
SENSE+
TG
BOOST
SW
VIN
INTVCC
BG
PGND
EXTVCC
LTC1735 SOP16
C710 330pF 0805
C713 0.1U50V20% 0805
AN268mm1002
C709 100pF 0805
AN268mm1003
R725 33K 0805
AN268mm1006
C694 47pF 0805
C673 1000pF 0805
AN268mm1021
AN268mm1011
R674 10 5% 1/16W
C668 1uF/16V
C669 4.7uF/10V TANTALUM CTA
AN268mm1022
R675 10 5% 1/16W 0603
AN268mm1004
AN268mm1005
R714 0 0603
C708 0.22uF 0805
AN268mm1007
AN268mm1014
AN268mm1015
AN268mm1019
Q523 SI4410DY SO8 SO8IB
Q521 SI4420DY SO8 SO8IB
D512 EP05Q03L 0.5A30V
D511 NSQ03A04 3A40V D3216
AN268mm1008
VMAIN
C707 0.1U25V 1206
C220 205C22M_22uF/20V CE10_OSCON
L60 2.7UH
CK16R
R667 0.005_1% 2512
AN268mm1009
VCC_CORE
R686 10 5% 1/16W 0603
C665 100U6.3V_SP CTD
C211 820uF/4V CE10_OSCON
AN268mm1023
C687 47pF 0805
R687 10K_1% 0603
R692 11K_1% 0603
R708 100K_1% 0603
AN268mm1024
AN268mm1025
VTOL
Q522 2N7002_SOT23 SOT23
R695 10K_NA 0603
VREF
R643 0.005_1% 2512
VCCP_IO
VCCP
U520
OUT
IN
TAB
ADJ
LT1117CST SOT223
AN2107
C639 1U25V_1206 1206
AN2108
R622 121_1% 0603
R615 53.6_1% 0603
R614 44.2_1% 0603
C651 56U/4V CTD
AN2113
Q517 2N7002_SOT23 SOT23
VID1
Q515 NDS352P SOT23
R609 10K 0603
VR_ON
R610 100K 0603
AN2109
Q516 2N3904 SOT23
R611 100K 0603
V_CPUIO
U521
AN2110
AN2111
C638 1U25V_1206 1206
R623 121_1% 0603
R617 121_1% 0603
R616 30.9_1% 0603
C652 56U/4V CTD
AN2112
Q518 2N7002_SOT23 SOT23
VCC
U515
IN
EN
NC
GND
OUT
MIC5209-2.5BM SO8
AN2630mm
VCC_2S5
C637 470pF_NA 0603
C636 22UF10V_TAT CTC
R606 0.005_1% 2512
V_CLK
R217 15.4K_1% 0603
VCC3
R236 1K1/16W 5% 0603
VCC_2S5
R199 11K_1% 0603
R200 12.1K 0603
AN26705
AN26706
AN26707
AN26704
AN26703
AN26702
AN26701
R201 12.1K_1% 0603
C196 1000pF 0603
R226 10K_1% 0603
R227 42.2K_1% 0603
C206 330PF 0603
R232 73.2K_1% 0603
C193 0.1U25V_0603 0603
R212 3.9K_0.1% 0603
C202 0.1U25V_0603 0603
R220 4.7K_0.1% 0603
U19
1IN+
1IN-
2IN+
2IN-
3IN+
3IN-
4IN+
4IN-
1OUT
2OUT
3OUT
4OUT
VCC
GND
LM339_SO SO14IB
C209 0.1uF_NA
C185 2200pF 0603
R213 12.1K_1% 0603
C192 0.1U25V_0603 0603
R211 12M 0603
C208 0.1U50V20% 0805
R216 1K_1% 0603
V_CPUIO
AN26708
VR_PWRGD
Q19 2N7002_SOT23
Q17 DTC144WK SOT23
R214 100K 0603
VID1
DIXON VTOL= 1
DIXON 0.18u VTOL = 0
InnoLabs Corp.
Title
CPUPWR
Size C
Document Number Model-1000
Rev 01BA
Date: Monday, October 11, 1999
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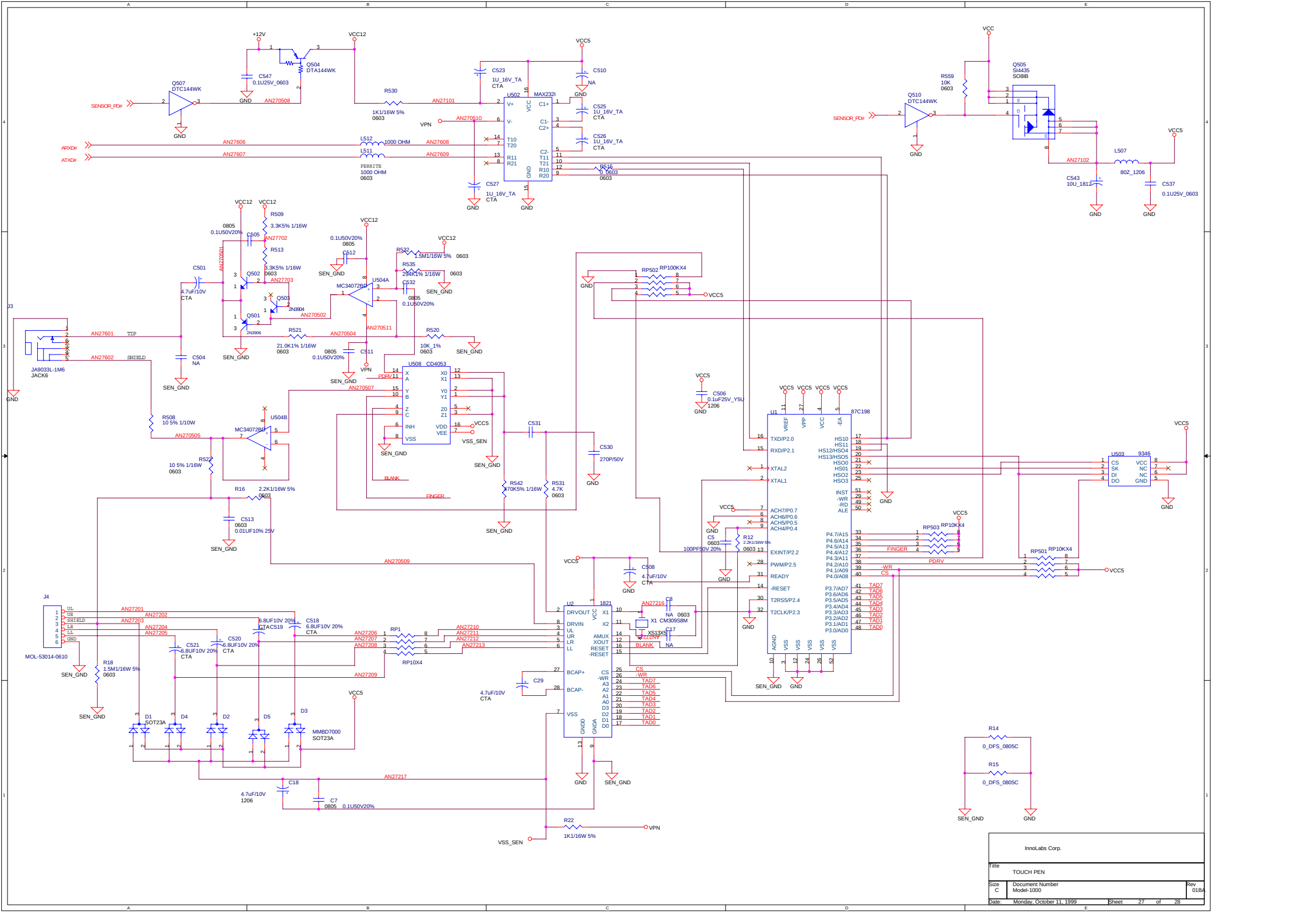
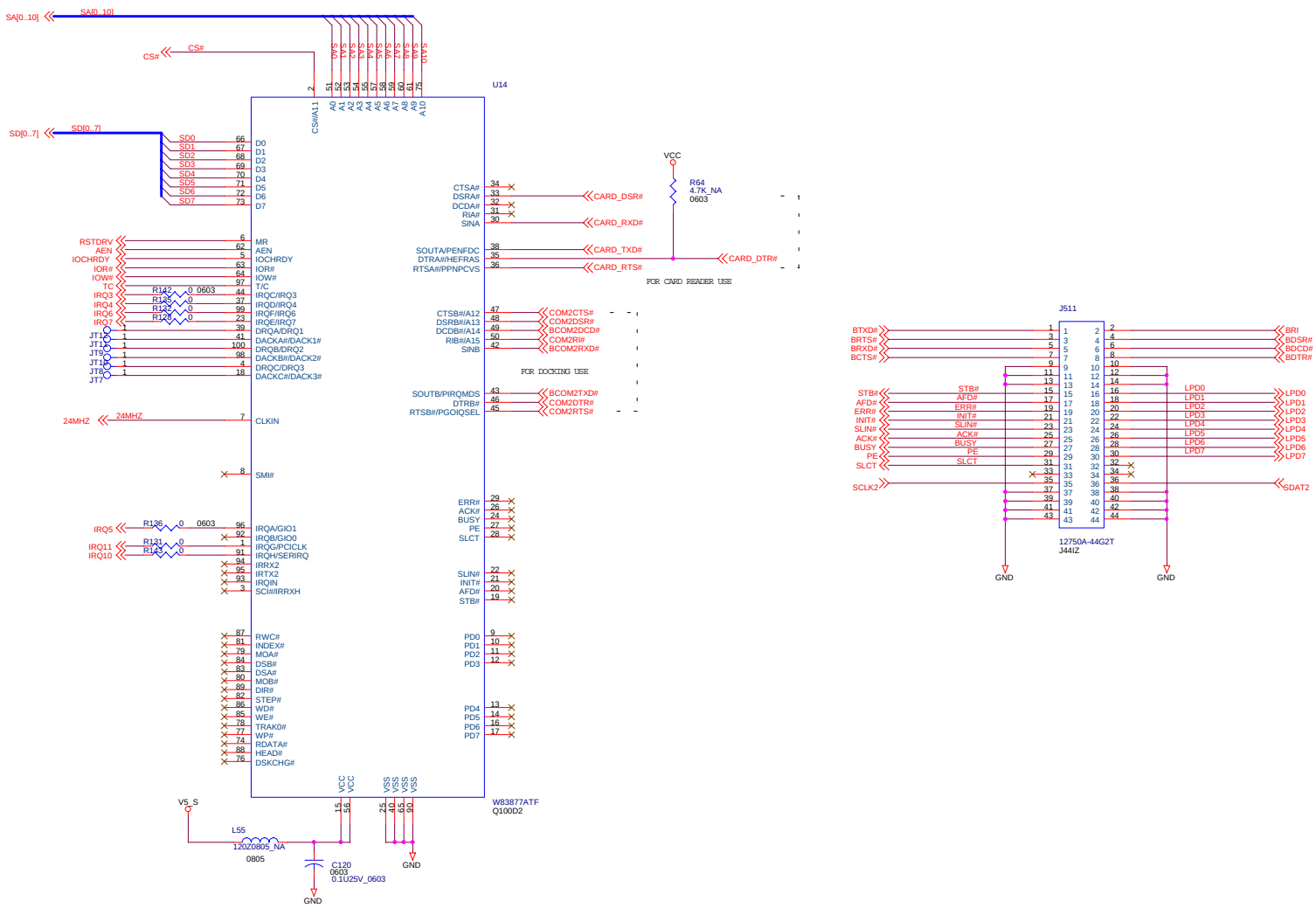

+12V
VCC12
VCC5
VCC
GND
SEN_GND
VSS_SEN
VPN
SENSOR_PD#
ARXD#
ATXD#
Q504 DTA144WK
Q507 DTC144WK
Q510 DTC144WK
Q505 SI4435 SO8IB
C547 0.1U25V_0603
R530 1K1/16W 5% 0603
AN27101
AN270508
AN270510
AN27606
AN27607
AN27608
AN27609
L512 1000 OHM
L511 FERRITE 1000 OHM 0603
U502 MAX232I
C523 1U_16V_TA CTA
C510 NA
C525 1U_16V_TA CTA
C526 1U_16V_TA CTA
C527 1U_16V_TA CTA
R515 0_0603 0603
R559 10K 0603
AN27102
L507 80Z_1206
C543 10U_1812
C537 0.1U25V_0603
J3
JA9033L-1M6 JACK6
AN27601 TIP
AN27602 SHIELD
C501 4.7uF/10V CTA
C504 NA
R509 3.3K5% 1/16W 0805 0.1U50V20%
C505
AN27702
R513 3.3K5% 1/16W 0603
AN27701
Q502
Q501
Q503 2N3904
2N3906
AN27703
AN270502
AN270504
AN270511
R521 21.0K1% 1/16W 0603
R520 10K_1% 0603
C511 0805 0.1U50V20%
C512 0.1U50V20% 0805
R532 1.5M1/16W 5% 0603
R535 294K1% 1/16W 0603
C532 0805 0.1U50V20%
U504A MC34072BD
U504B MC34072BD
U508 CD4053
PDRV
AN270507
BLANK
FINGER
R508 10 5% 1/10W
AN270505
R522 10 5% 1/16W 0603
R16 2.2K1/16W 5% 0603
C513 0603 0.01UF10% 25V
C531
C530 270P/50V
R542 470K5% 1/16W
R531 4.7K 0603
RP502 RP100KX4
C506 0.1uF25V_Y5U 1206
U1 87C198
VREF VPP VCC -EA
TXD/P2.0
RXD/P2.1
XTAL2
XTAL1
ACH7/P0.7
ACH6/P0.6
ACH5/P0.5
ACH4/P0.4
EXINT/P2.2
PWM/P2.5
READY
-RESET
T2RS5/P2.4
T2CLK/P2.3
HS10 HS11 HS12/HSO4 HS13/HSO5 HSO0 HS01 HSO2 HSO3
INST -WR -RD ALE
P4.7/A15 P4.6/A14 P4.5/A13 P4.4/A12 P4.3/A11 P4.2/A10 P4.1/A09 P4.0/A08
P3.7/AD7 P3.6/AD6 P3.5/AD5 P3.4/AD4 P3.3/AD3 P3.2/AD2 P3.1/AD1 P3.0/AD0
AGND VSS
R12 2.2K1/16W 5%
100PF50V 20%
C508 4.7uF/10V CTA
RP503 RP10KX4
RP501 RP10KX4
U503 9346
CS SK DI DO VCC NC NC GND
J4
MOL-53014-0610
UL UR SHIELD LR LL GND
AN27201 AN27202 AN27203 AN27204 AN27205
C518 6.8UF10V 20% CTA
C519 6.8UF10V 20% CTA
C520 6.8UF10V 20% CTA
C521 6.8UF10V 20% CTA
RP1 RP10X4
AN27206 AN27207 AN27208 AN27209 AN27210 AN27211 AN27212 AN27213
R18 1.5M1/16W 5% 0603
D1 SOT23A
D4
D2
D5
D3 MMBD7000 SOT23A
U2 1821
DRVOUT DRVIN UL UR LR LL AMUX XOUT RESET -RESET BCAP+ BCAP- VSS VCC X1 X2 GNDD GNDA
AN27216
C8 NA 0603
X1 CM309S8M
C17 NA
XS13X5
C29 4.7uF/10V CTA
CS -WR A3 A2 A1 A0 D3 D2 D1 D0
TAD7 TAD6 TAD5 TAD4 TAD3 TAD2 TAD1 TAD0
AN27217
C18 4.7uF/10V 1206
C7 0805 0.1U50V20%
R22 1K1/16W 5%
R14 0_DFS_0805C
R15 0_DFS_0805C
InnoLabs Corp.
Title
TOUCH PEN
Size C
Document Number Model-1000
Rev 01BA
Date: Monday, October 11, 1999
Sheet 27 of 28



	InnoLabs Corp.
Title	W83877ATF_2
Size C	Document Number Model-1000
Rev	01BA
Date:	Monday, October 11, 1999
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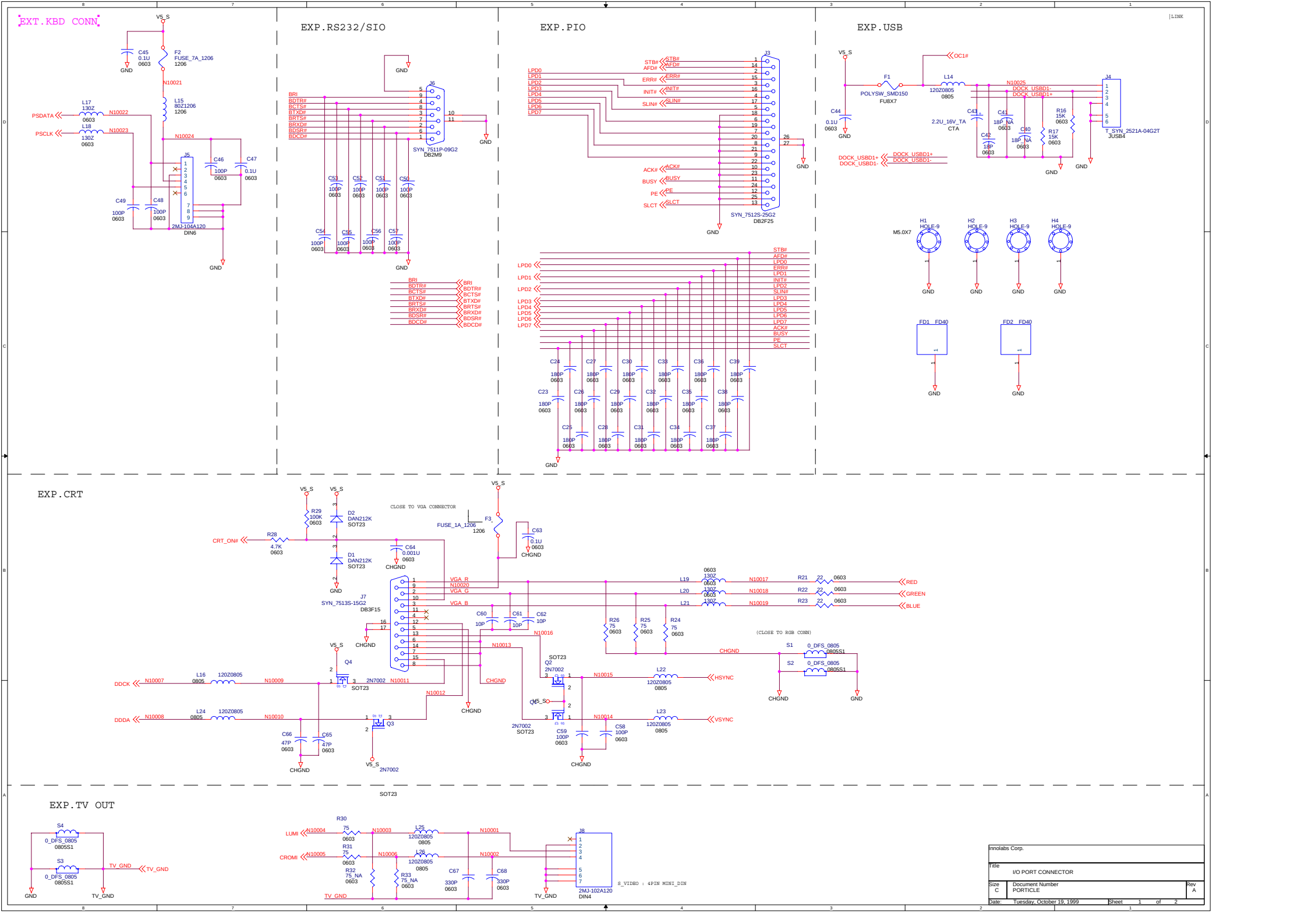
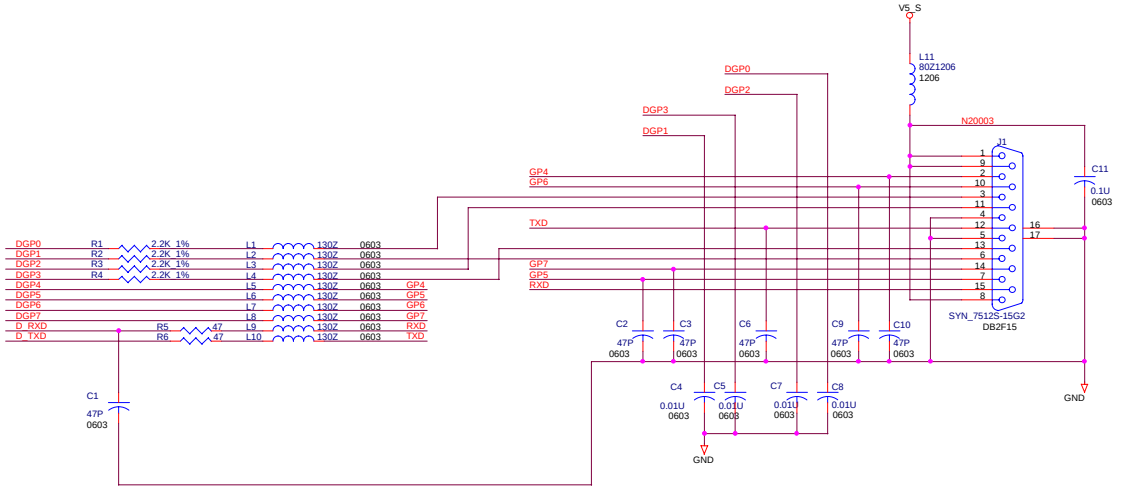
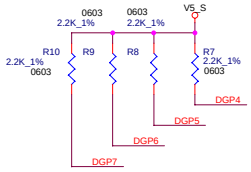
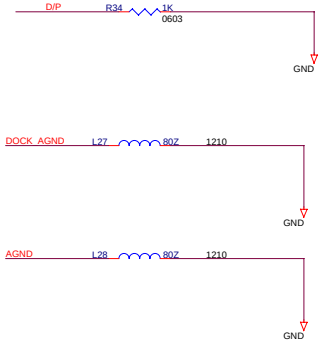
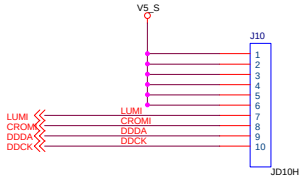

EXT.KBD CONN
EXP.RS232/SIO
EXP.PIO
EXP.USB
EXP.CRT
EXP.TV OUT
SYN_7511P-09G2
DB2M9
SYN_7512S-25G2
DB2F25
SYN_7513S-15G2
DB3F15
2MJ-104A120
DIN6
2MJ-102A120
DIN4
T_SYN_2521A-04G2T
JUSB4
POLYSW_SMD150
FU8X7
FUSE_7A_1206
FUSE_1A_1206
DAN212K
SOT23
2N7002
0_DFS_0805
0805S1
HOLE-9
M5.0X7
CLOSE TO VGA CONNECTOR
(CLOSE TO RGB CONN)
S_VIDEO : 4PIN MINI_DIN
Innolabs Corp.
I/O PORT CONNECTOR
Document Number
PORTICLE
Tuesday, October 19, 1999

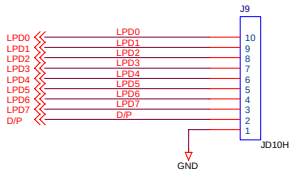
EXP.GP CONN

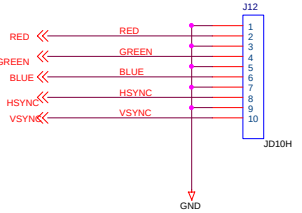


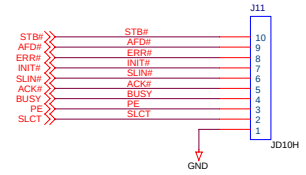


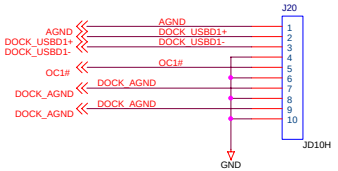


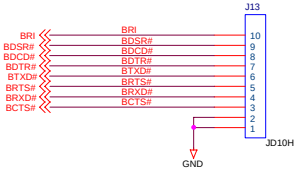


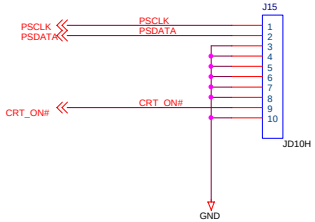


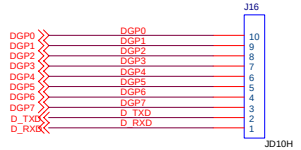


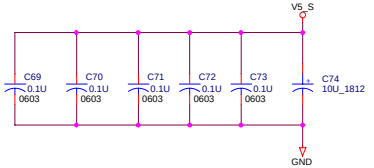












Innolabs Corp.		
Title: GAME PORT CONNECTOR		
Size: C	Document Number: PORTICLE	Rev: A
Date: Tuesday, October 19, 1999	Sheet 2 of 2	