

Intertek Testing Services -Menlo Park

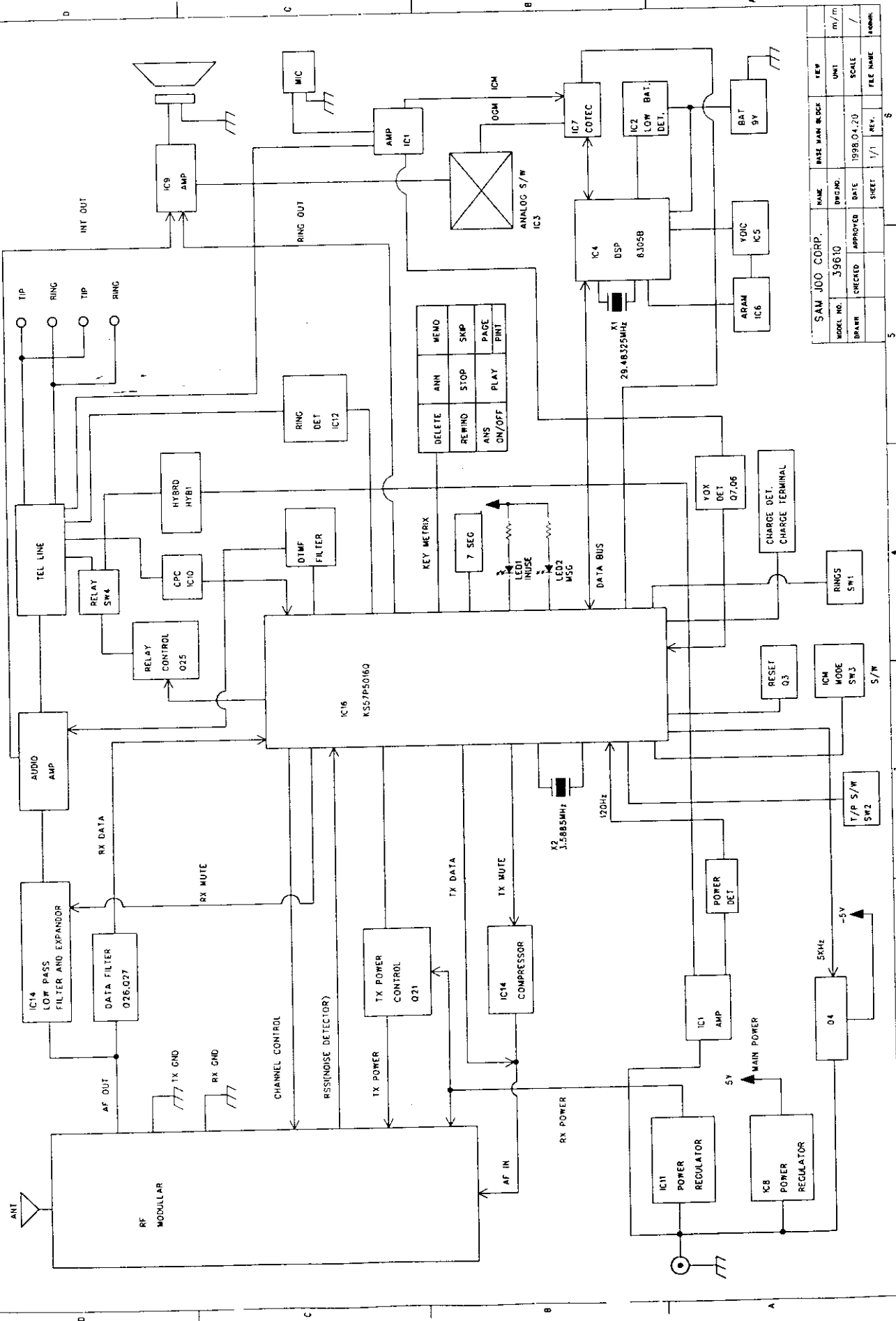
Shen Zhen Taifeng Electronic Co., Ltd., 900 MHz Cordless Telephone Date of Test: May 6, 1998
FCC ID: NR839610

8.0 Technical Specifications

8.1 Transmitter Block Diagram

See attached page.

39610 BASE MAIN BLOCK DIAGRAM



SAM JOO CORP.		NAME	BASE MAIN BLOCK	VIEW
MODEL NO.	39610	DATE	1998.04.20	SCALE
DRAWN	CHECKED	APPROVED	SHEET	1/1
FILE NAME				

1

11

RF BOARD

The schematic diagram illustrates the internal components and connections of an RF Board. Key components include:

- RF AMP (Q1)**: A radio frequency amplifier at the input.
- RX MIXER (Q2)**: A mixer stage for receiving signals.
- RX VCO**: A radio frequency variable capacitor for reception.
- TX VCO**: A radio frequency variable capacitor for transmission.
- RF AMP (Q5)**: A radio frequency amplifier at the output.
- PLL (IC2)**: A phase-locked loop circuit for frequency stability.
- 450KHz Filter (CF2)**: A filter for the 450KHz signal.
- Antenna (ANT)**: Connected to the input and output stages.
- Base Main**: The main control and power supply interface.

The board is labeled with a grid system (1-6, A-D) and includes various components like capacitors (C1-C7), resistors (R1, R2), and inductors (L1, L2). The board is connected to an ANTENNA (ANT) and a BASE MAIN.

RF BLOCK DIAGRAM				VIEW	
NAME	DWG.NO.	UNIT	FILE NAME	610RFBL	
SAM JOO CORP.	39610				
MODEL NO.	CHECKED	APPROVED	DATE	1998.04.20	/
DRAWN	SHEET	REV.			