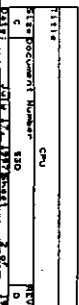


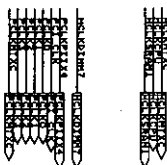
120

Platform : ATX
P54C,P55C,6X86,K5
SIMM : SOCKET 2 X 72 Pins
SIMM : 5V - EDO, FPM DRAM
CACHE : ON BOARD 256K/512K PBSRAM/M-CACHE
BIOS : 5V,12V FLASH ROM,2 Mbits FLASH ROM
USB : 2
ID : SMC 37C672
ULTRA DMA 33 IDE PORT X 2

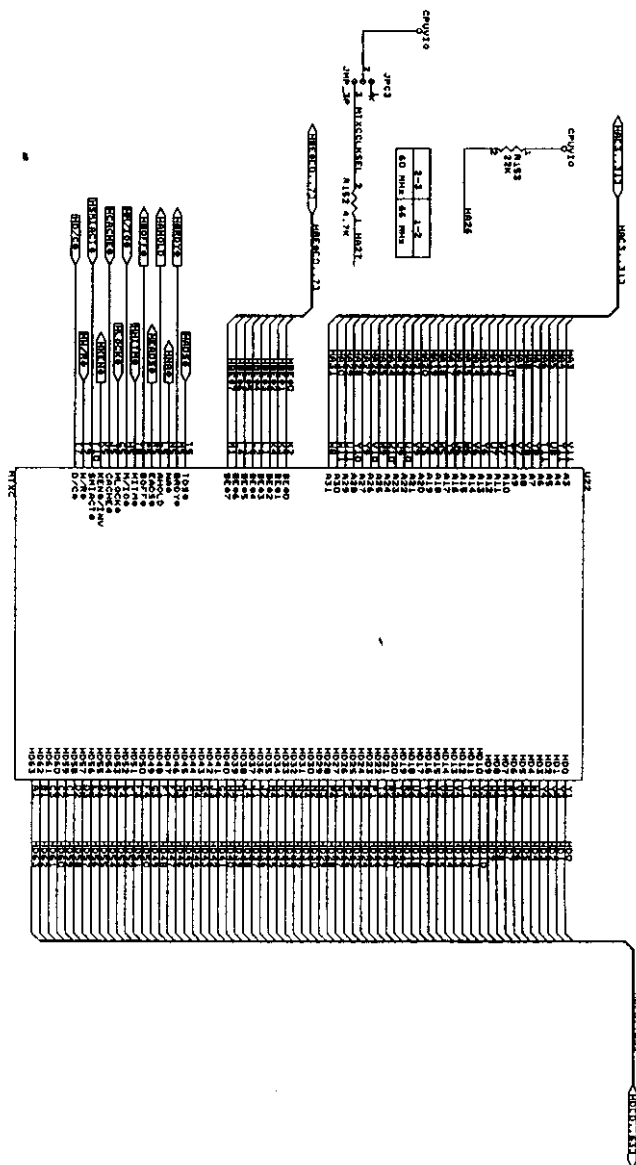
ILINK
IP54C.SCH
ISYSCLK.SCH
IMTXC_A.SCH
IMTXC_B.SCH
ICACHE.SCH
ISIMM.SCH
IP1X4_A.SCH
IP1X4_B.SCH
IDEBUSB.SCH
ISUPER_IO.SCH
ICDM_PORT.SCH
IBIOS.SCH
IPOWER.SCH
IVOLT_REG.SCH
IDECUP.SCH
IPULLUP.SCH
IRTC_KBC.SCH
IT2_ISA16.SCH

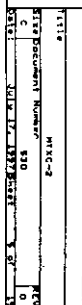
DATE	TIME	NO.	REV
0	0	0	0
NO. 11. 11711111 1.0.1			



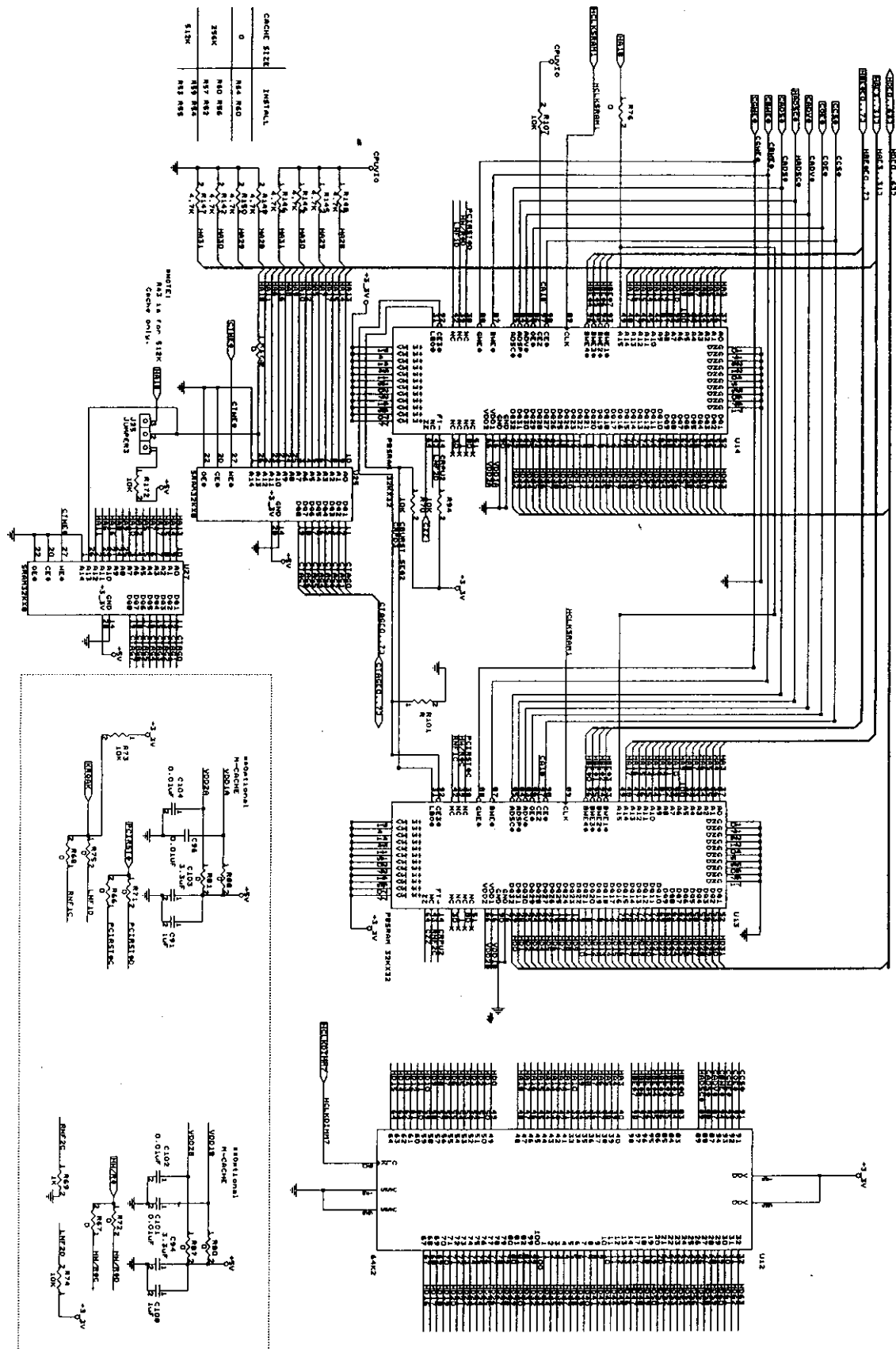


6.33
 C100 0.1uF
 C126 0.1uF
 C127 0.1uF
 C120 0.1uF
 C119 0.1uF
 C12 0.1uF
 C125 0.1uF
 C123 0.1uF
 C105 0.1uF
 147uF

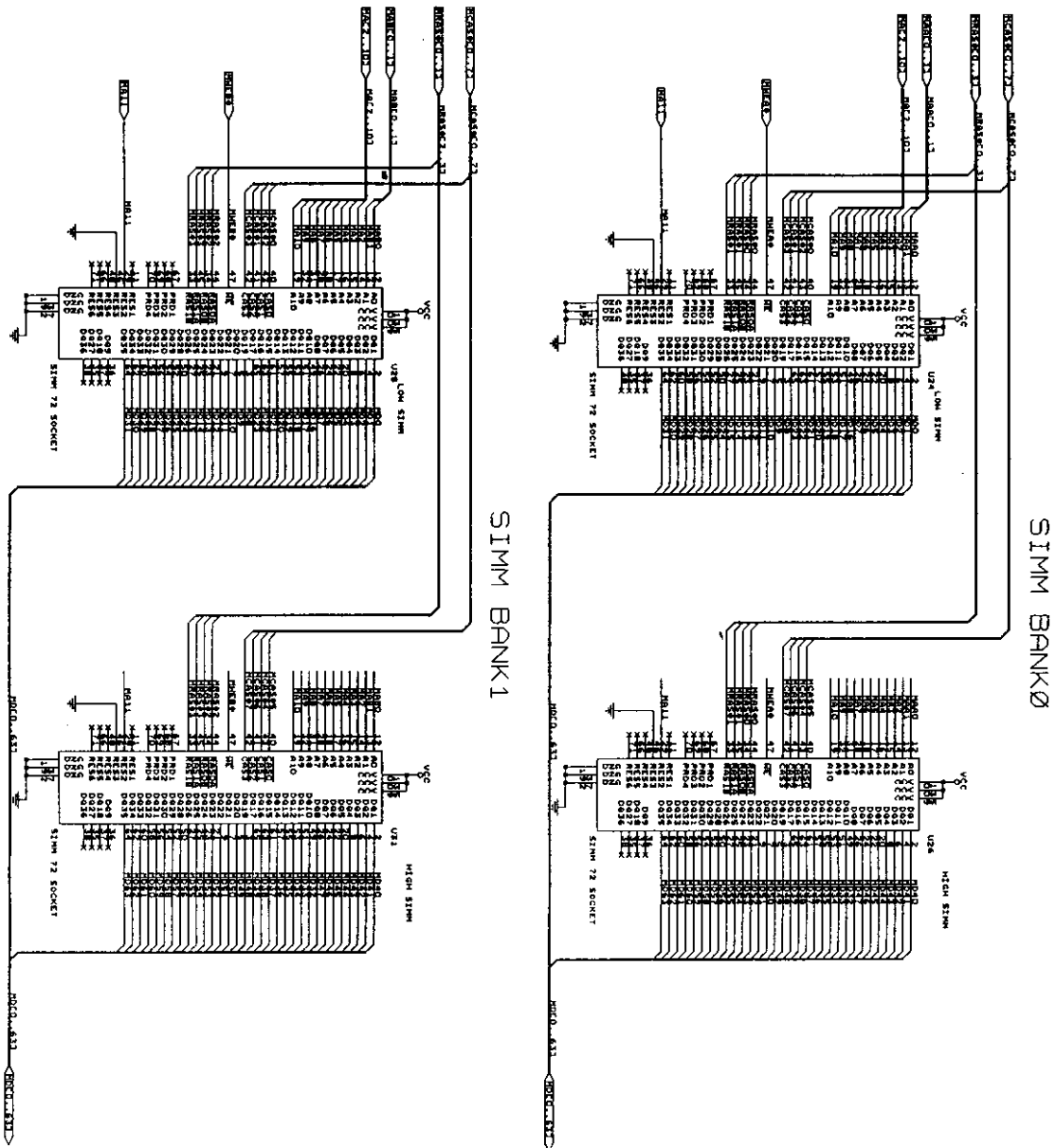




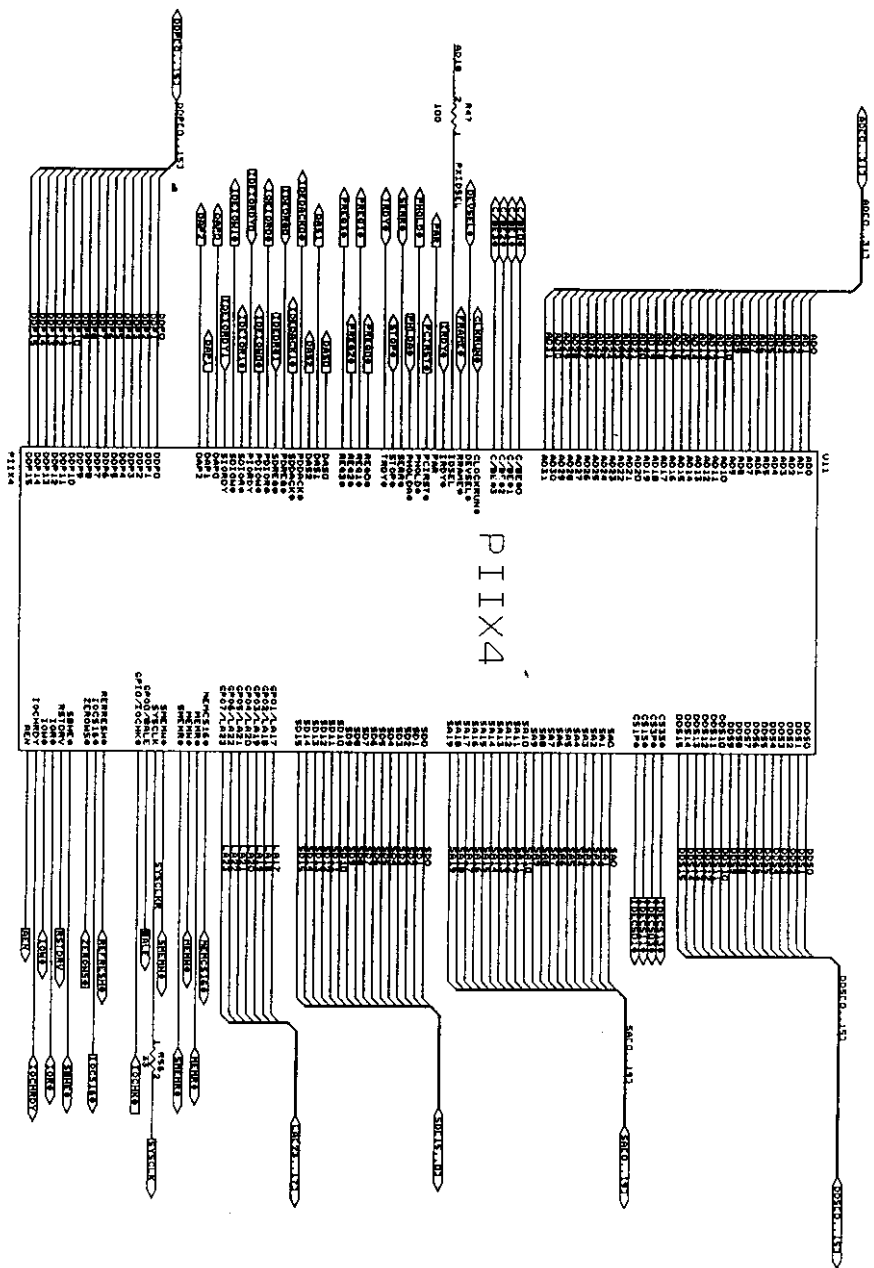
NOTE: PIN 16, 29, 42, 43 & 66 ARE DISCONNECTS FOR PG 8080



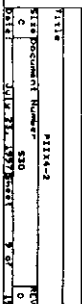
U1	1.0	μF
U2	1.0	μF
U3	1.0	μF
U4	1.0	μF
U5	1.0	μF
U6	1.0	μF
U7	1.0	μF
U8	1.0	μF
U9	1.0	μF
U10	1.0	μF
U11	1.0	μF
U12	1.0	μF
U13	1.0	μF
U14	1.0	μF
U15	1.0	μF
U16	1.0	μF
U17	1.0	μF
U18	1.0	μF
U19	1.0	μF
U20	1.0	μF
U21	1.0	μF
U22	1.0	μF
U23	1.0	μF
U24	1.0	μF
U25	1.0	μF
U26	1.0	μF
U27	1.0	μF
U28	1.0	μF
U29	1.0	μF
U30	1.0	μF
U31	1.0	μF
U32	1.0	μF
U33	1.0	μF
U34	1.0	μF
U35	1.0	μF
U36	1.0	μF
U37	1.0	μF
U38	1.0	μF
U39	1.0	μF
U40	1.0	μF
U41	1.0	μF
U42	1.0	μF
U43	1.0	μF
U44	1.0	μF
U45	1.0	μF
U46	1.0	μF
U47	1.0	μF
U48	1.0	μF
U49	1.0	μF
U50	1.0	μF
U51	1.0	μF
U52	1.0	μF
U53	1.0	μF
U54	1.0	μF
U55	1.0	μF
U56	1.0	μF
U57	1.0	μF
U58	1.0	μF
U59	1.0	μF
U60	1.0	μF
U61	1.0	μF
U62	1.0	μF
U63	1.0	μF
U64	1.0	μF
U65	1.0	μF
U66	1.0	μF
U67	1.0	μF
U68	1.0	μF
U69	1.0	μF
U70	1.0	μF
U71	1.0	μF
U72	1.0	μF
U73	1.0	μF
U74	1.0	μF
U75	1.0	μF
U76	1.0	μF
U77	1.0	μF
U78	1.0	μF
U79	1.0	μF
U80	1.0	μF
U81	1.0	μF
U82	1.0	μF
U83	1.0	μF
U84	1.0	μF
U85	1.0	μF
U86	1.0	μF
U87	1.0	μF
U88	1.0	μF
U89	1.0	μF
U90	1.0	μF
U91	1.0	μF
U92	1.0	μF
U93	1.0	μF
U94	1.0	μF
U95	1.0	μF
U96	1.0	μF
U97	1.0	μF
U98	1.0	μF
U99	1.0	μF
U100	1.0	μF



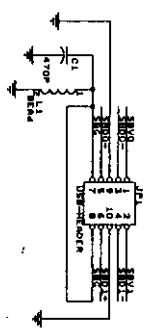
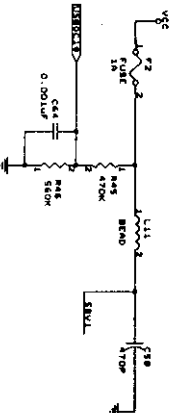
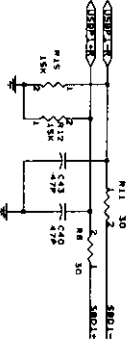
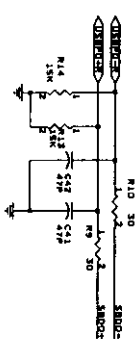
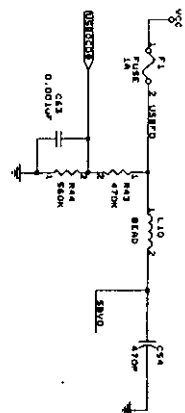
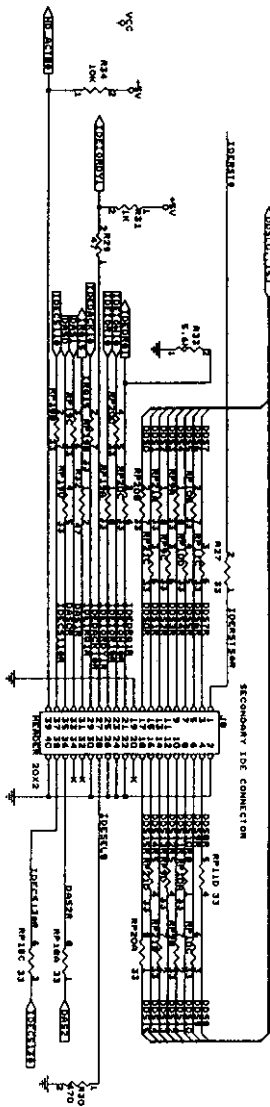
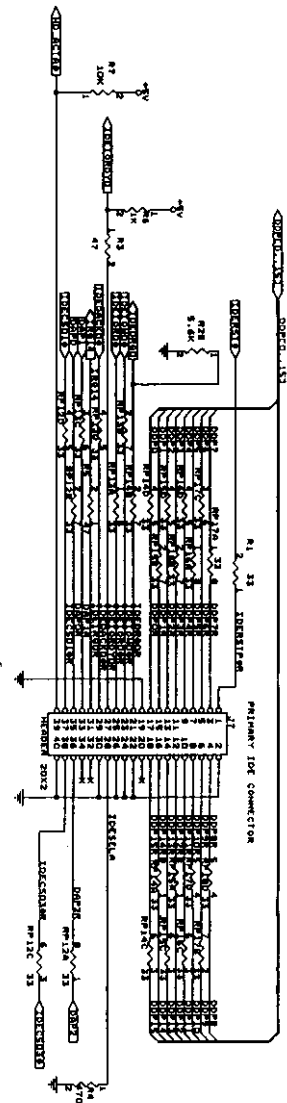
1110	SIMM
1111	1111
1112	1112
1113	1113
1114	1114
1115	1115
1116	1116
1117	1117
1118	1118
1119	1119
1120	1120
1121	1121
1122	1122
1123	1123
1124	1124
1125	1125
1126	1126
1127	1127
1128	1128
1129	1129
1130	1130
1131	1131
1132	1132
1133	1133
1134	1134
1135	1135
1136	1136
1137	1137
1138	1138
1139	1139
1140	1140
1141	1141
1142	1142
1143	1143
1144	1144
1145	1145
1146	1146
1147	1147
1148	1148
1149	1149
1150	1150
1151	1151
1152	1152
1153	1153
1154	1154
1155	1155
1156	1156
1157	1157
1158	1158
1159	1159
1160	1160
1161	1161
1162	1162
1163	1163
1164	1164
1165	1165
1166	1166
1167	1167
1168	1168
1169	1169
1170	1170
1171	1171
1172	1172
1173	1173
1174	1174
1175	1175
1176	1176
1177	1177
1178	1178
1179	1179
1180	1180
1181	1181
1182	1182
1183	1183
1184	1184
1185	1185
1186	1186
1187	1187
1188	1188
1189	1189
1190	1190
1191	1191
1192	1192
1193	1193
1194	1194
1195	1195
1196	1196
1197	1197
1198	1198
1199	1199



Title	
PIX4-1	
Drawing Number 580	
Drawn By	197/2/2/1
Check By	197/2/2/1
Rev	0

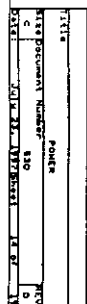


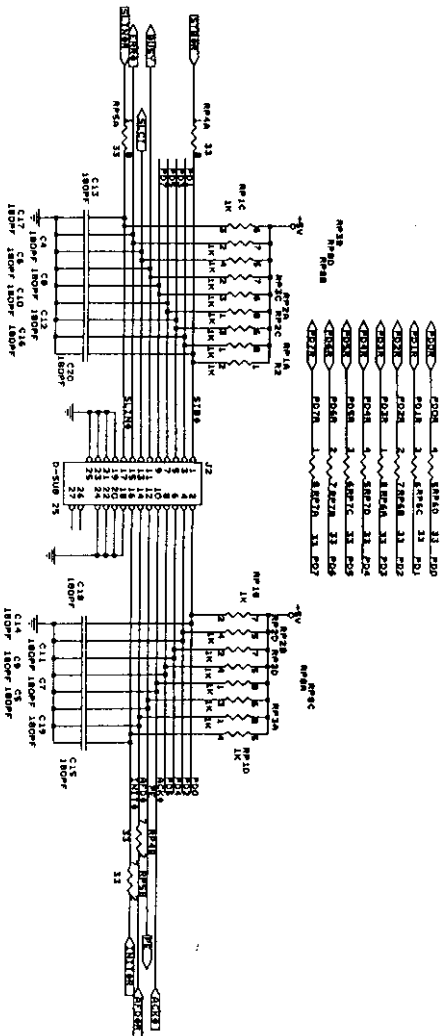
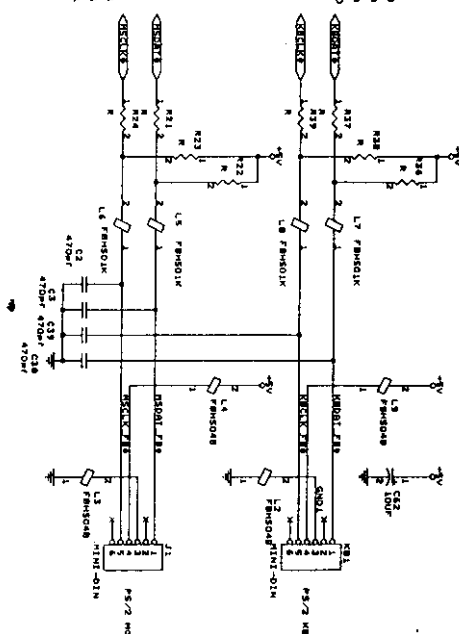
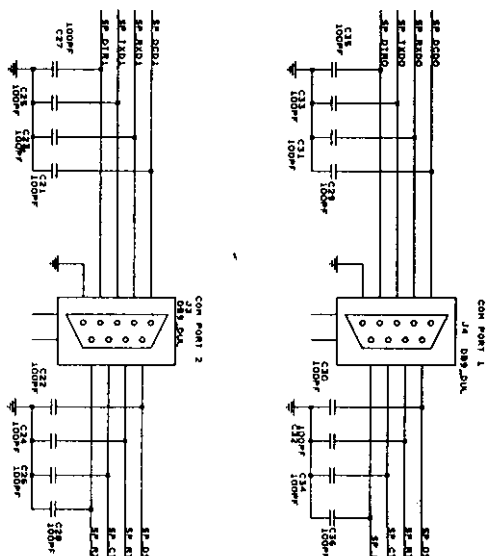
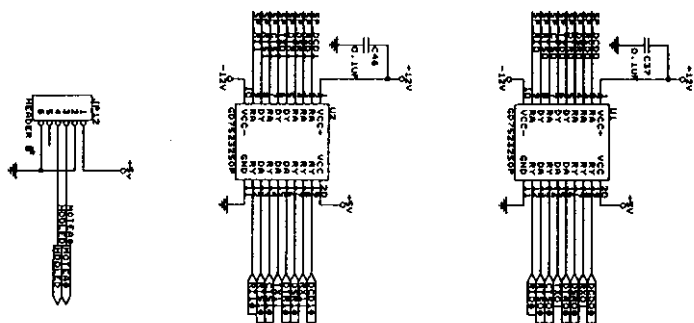
IDE CONNECTORS



USB CONNECTOR

1111	IDE & USB
1112	1112
1113	1113
1114	1114
1115	1115
1116	1116
1117	1117
1118	1118
1119	1119
1120	1120
1121	1121
1122	1122
1123	1123
1124	1124
1125	1125
1126	1126
1127	1127
1128	1128
1129	1129
1130	1130
1131	1131
1132	1132
1133	1133
1134	1134
1135	1135
1136	1136
1137	1137
1138	1138
1139	1139
1140	1140
1141	1141
1142	1142
1143	1143
1144	1144
1145	1145
1146	1146
1147	1147
1148	1148
1149	1149
1150	1150
1151	1151
1152	1152
1153	1153
1154	1154
1155	1155
1156	1156
1157	1157
1158	1158
1159	1159
1160	1160
1161	1161
1162	1162
1163	1163
1164	1164
1165	1165
1166	1166
1167	1167
1168	1168
1169	1169
1170	1170
1171	1171
1172	1172
1173	1173
1174	1174
1175	1175
1176	1176
1177	1177
1178	1178
1179	1179
1180	1180
1181	1181
1182	1182
1183	1183
1184	1184
1185	1185
1186	1186
1187	1187
1188	1188
1189	1189
1190	1190
1191	1191
1192	1192
1193	1193
1194	1194
1195	1195
1196	1196
1197	1197
1198	1198
1199	1199
1200	1200





PARALLEL PORT

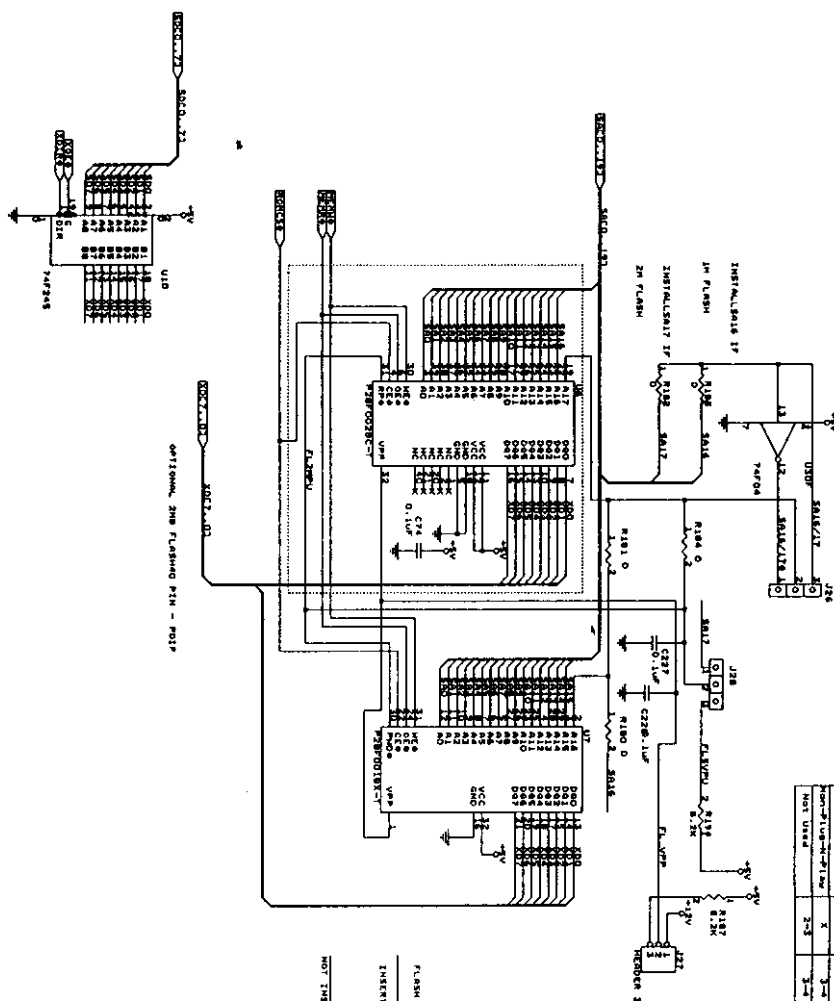
1111	CON-LAT PORT
1112	CON-LAT PORT
1113	CON-LAT PORT
1114	CON-LAT PORT
1115	CON-LAT PORT
1116	CON-LAT PORT
1117	CON-LAT PORT
1118	CON-LAT PORT
1119	CON-LAT PORT
1120	CON-LAT PORT
1121	CON-LAT PORT
1122	CON-LAT PORT
1123	CON-LAT PORT
1124	CON-LAT PORT
1125	CON-LAT PORT
1126	CON-LAT PORT
1127	CON-LAT PORT
1128	CON-LAT PORT
1129	CON-LAT PORT
1130	CON-LAT PORT
1131	CON-LAT PORT
1132	CON-LAT PORT
1133	CON-LAT PORT
1134	CON-LAT PORT
1135	CON-LAT PORT
1136	CON-LAT PORT
1137	CON-LAT PORT
1138	CON-LAT PORT
1139	CON-LAT PORT
1140	CON-LAT PORT
1141	CON-LAT PORT
1142	CON-LAT PORT
1143	CON-LAT PORT
1144	CON-LAT PORT
1145	CON-LAT PORT
1146	CON-LAT PORT
1147	CON-LAT PORT
1148	CON-LAT PORT
1149	CON-LAT PORT
1150	CON-LAT PORT
1151	CON-LAT PORT
1152	CON-LAT PORT
1153	CON-LAT PORT
1154	CON-LAT PORT
1155	CON-LAT PORT
1156	CON-LAT PORT
1157	CON-LAT PORT
1158	CON-LAT PORT
1159	CON-LAT PORT
1160	CON-LAT PORT
1161	CON-LAT PORT
1162	CON-LAT PORT
1163	CON-LAT PORT
1164	CON-LAT PORT
1165	CON-LAT PORT
1166	CON-LAT PORT
1167	CON-LAT PORT
1168	CON-LAT PORT
1169	CON-LAT PORT
1170	CON-LAT PORT
1171	CON-LAT PORT
1172	CON-LAT PORT
1173	CON-LAT PORT
1174	CON-LAT PORT
1175	CON-LAT PORT
1176	CON-LAT PORT
1177	CON-LAT PORT
1178	CON-LAT PORT
1179	CON-LAT PORT
1180	CON-LAT PORT
1181	CON-LAT PORT
1182	CON-LAT PORT
1183	CON-LAT PORT
1184	CON-LAT PORT
1185	CON-LAT PORT
1186	CON-LAT PORT
1187	CON-LAT PORT
1188	CON-LAT PORT
1189	CON-LAT PORT
1190	CON-LAT PORT
1191	CON-LAT PORT
1192	CON-LAT PORT
1193	CON-LAT PORT
1194	CON-LAT PORT
1195	CON-LAT PORT
1196	CON-LAT PORT
1197	CON-LAT PORT
1198	CON-LAT PORT
1199	CON-LAT PORT
1200	CON-LAT PORT

J1

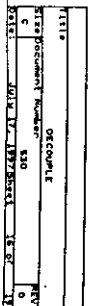
MODE	POS.
NORMAL	1-3
RECOVERY	1-2

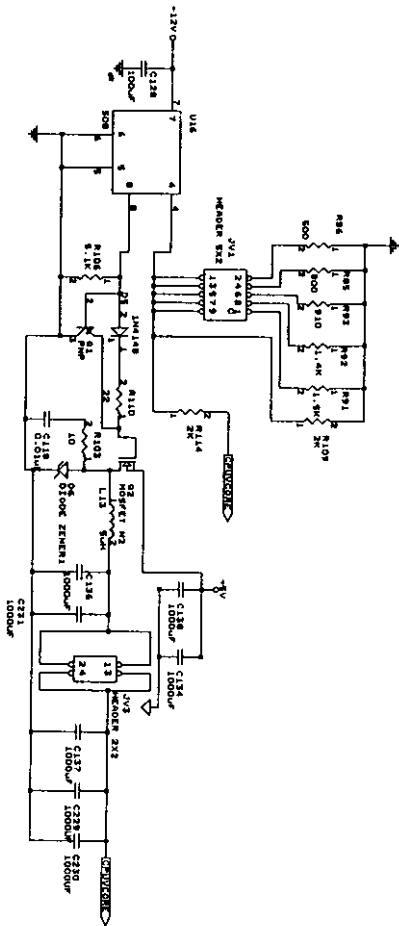
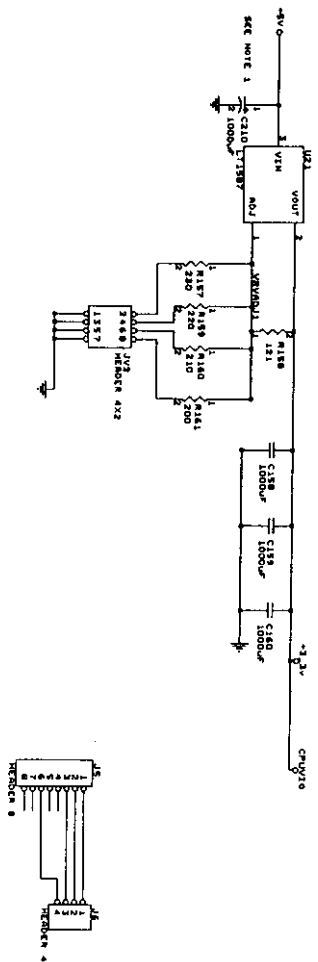
NOTE: THE X DENOTES A 40011 200P.F. CAPACITOR.

MODE	1-2	1-3	1-4
Program Switch	1-2	1-3	1-4 (5-6 FOR AND 2N WITS)
Plug-in-Flam	2-3	3-4	
Plug-in-Flam	X		
Not Used	2-3	3-4	

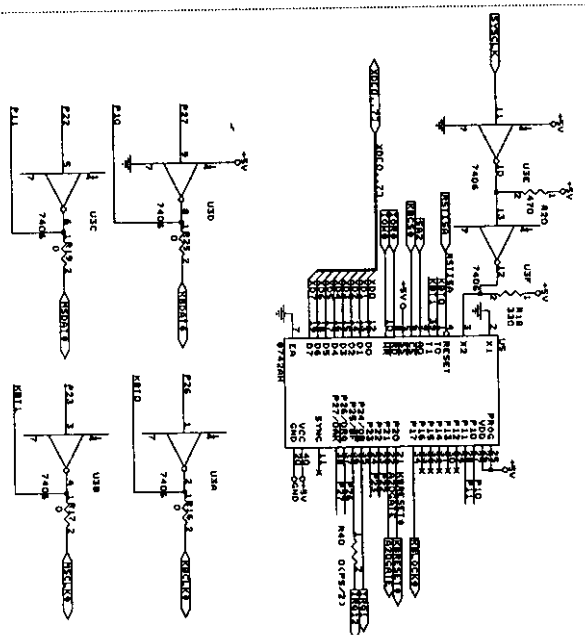


FLASH ROM	INTEL	AND
INSTR	R1	R2
	R2	R3
	R3	R4
	R4	R5
	R5	R6
	R6	R7
	R7	R8
	R8	R9
	R9	R10
	R10	R11
	R11	R12
	R12	R13
	R13	R14
	R14	R15
	R15	R16
	R16	R17
	R17	R18
	R18	R19
	R19	R20
	R20	R21
	R21	R22
	R22	R23
	R23	R24
	R24	R25
	R25	R26
	R26	R27
	R27	R28
	R28	R29
	R29	R30
	R30	R31
	R31	R32
	R32	R33
	R33	R34
	R34	R35
	R35	R36
	R36	R37
	R37	R38
	R38	R39
	R39	R40
	R40	R41
	R41	R42
	R42	R43
	R43	R44
	R44	R45
	R45	R46
	R46	R47
	R47	R48
	R48	R49
	R49	R50
	R50	R51
	R51	R52
	R52	R53
	R53	R54
	R54	R55
	R55	R56
	R56	R57
	R57	R58
	R58	R59
	R59	R60
	R60	R61
	R61	R62
	R62	R63
	R63	R64
	R64	R65
	R65	R66
	R66	R67
	R67	R68
	R68	R69
	R69	R70
	R70	R71
	R71	R72
	R72	R73
	R73	R74
	R74	R75
	R75	R76
	R76	R77
	R77	R78
	R78	R79
	R79	R80
	R80	R81
	R81	R82
	R82	R83
	R83	R84
	R84	R85
	R85	R86
	R86	R87
	R87	R88
	R88	R89
	R89	R90
	R90	R91
	R91	R92
	R92	R93
	R93	R94
	R94	R95
	R95	R96
	R96	R97
	R97	R98
	R98	R99
	R99	R100



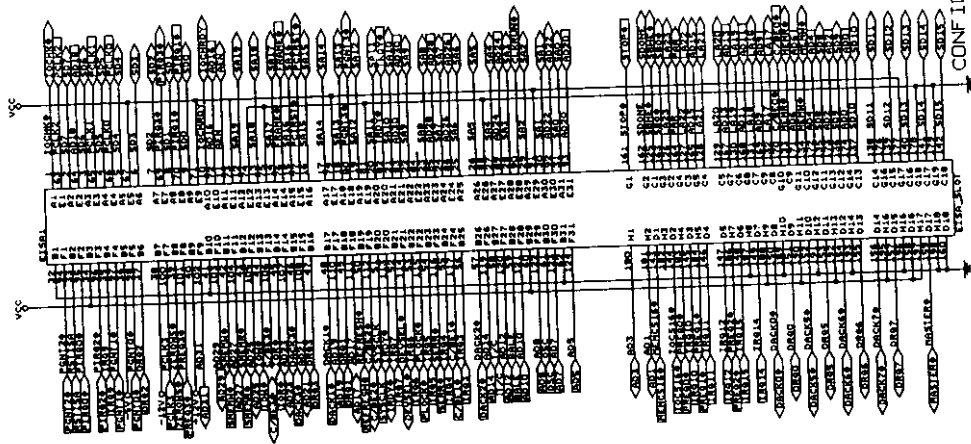


Resistors are implemented as copper traces on layer 1 (top).
 For J88, connect:
 Note 1: trace width is 50 mils, 1.24" long.
 Note 2: trace width is 50 mils, 0.82" long.



DO NOT REPRODUCE

ISA SLOTS 3&4



INTEL SECRET
CONFIDENTIAL INFORMATION