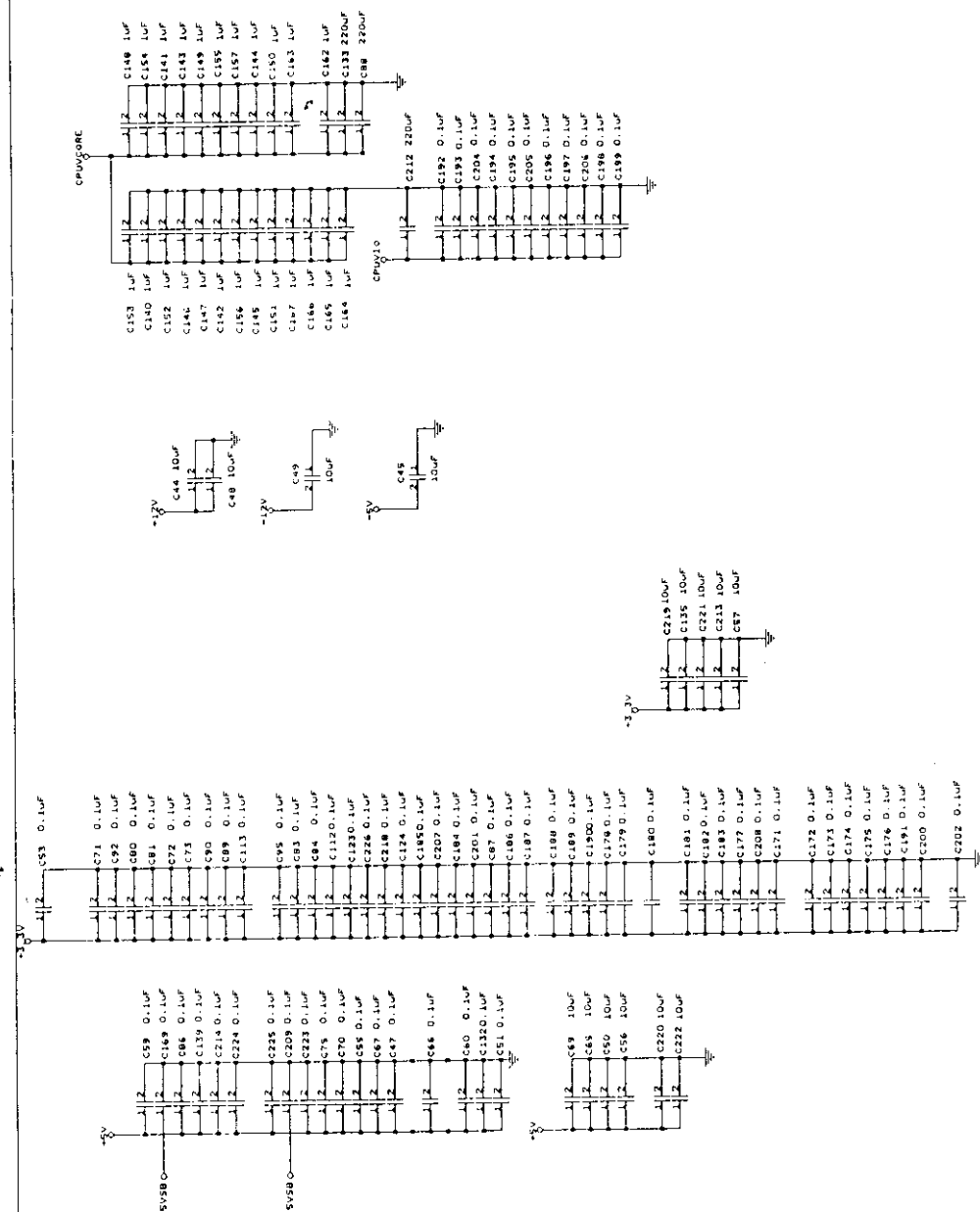
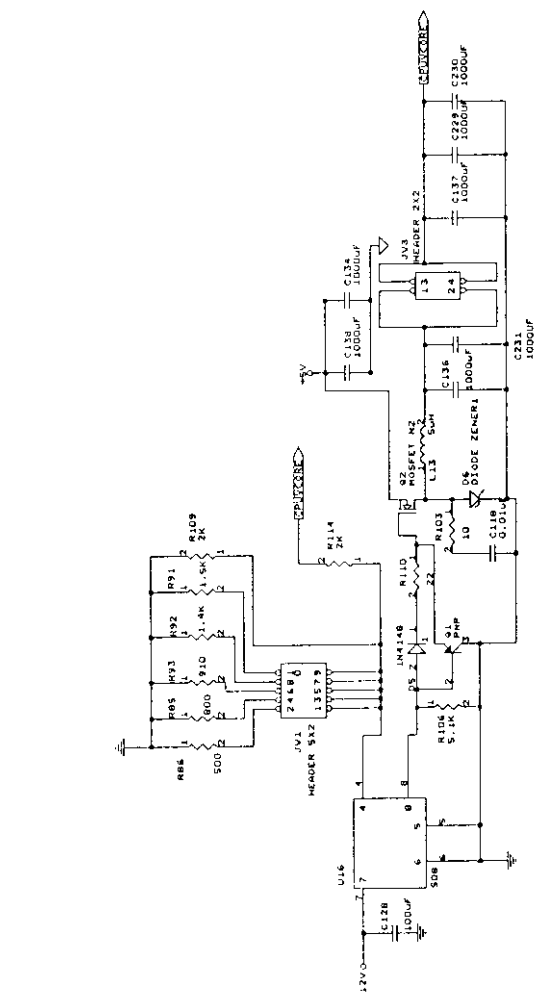
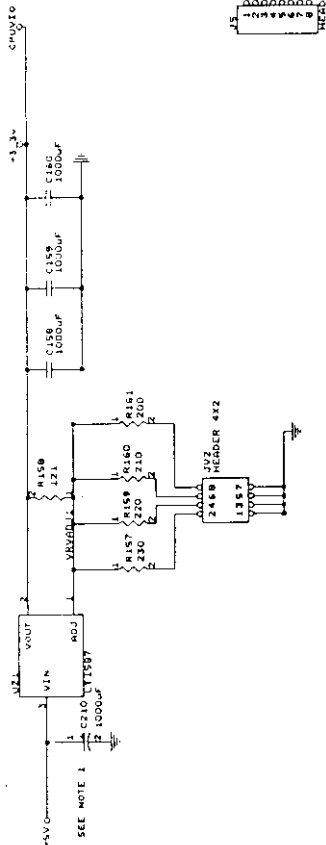
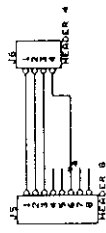
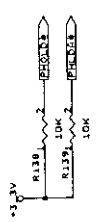
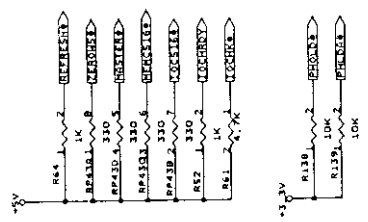
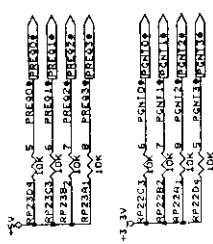
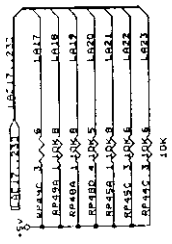
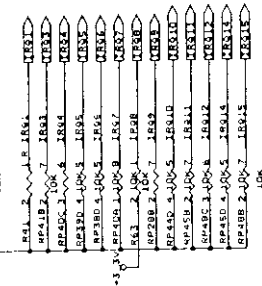
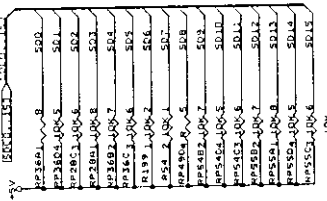
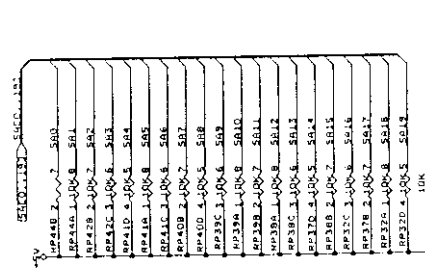
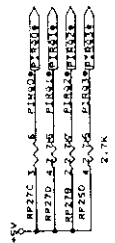






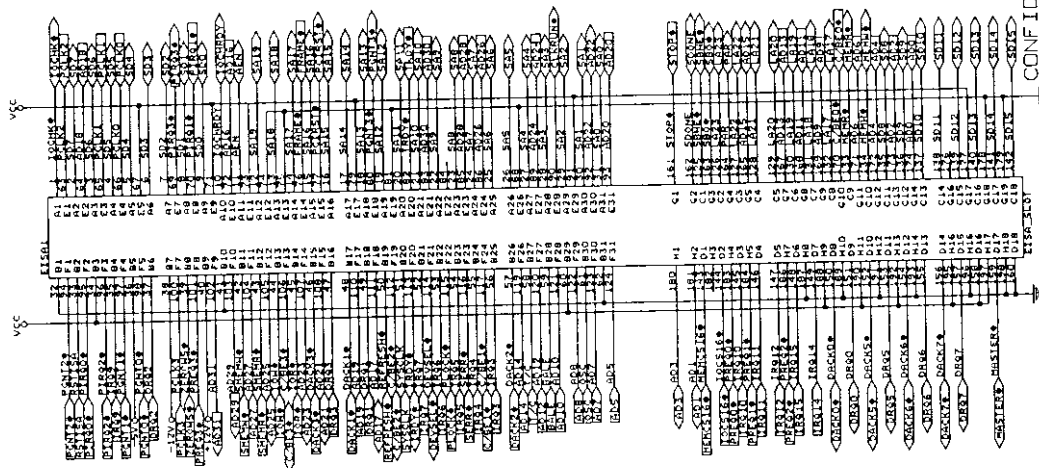
Heatstons are implemented as corner traces on inter layer:
 For 10% corner:
 Note 1: trace widths are 50 mils, 1.24" long.
 Note 2: trace widths are 50 mils, 0.82" long.





DO NOT REPRODUCE

ISA SLOTS 3&4



INTEL SECRET
CONFIDENTIAL INFORMATION

130

Platform : ATX

P54C,P55C,6X86,K5

SIMM : SOCKET 2 X 72 Pins

SIMM : 5V - EDO,FPM DRAM

CACHE : ON BOARD 256K/512K PBSRAM/M-CACHE

BIOS : 5V,12V FLASH ROM,2 Mbits FLASH ROM

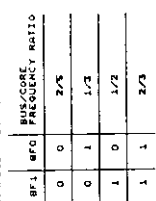
USB : 2

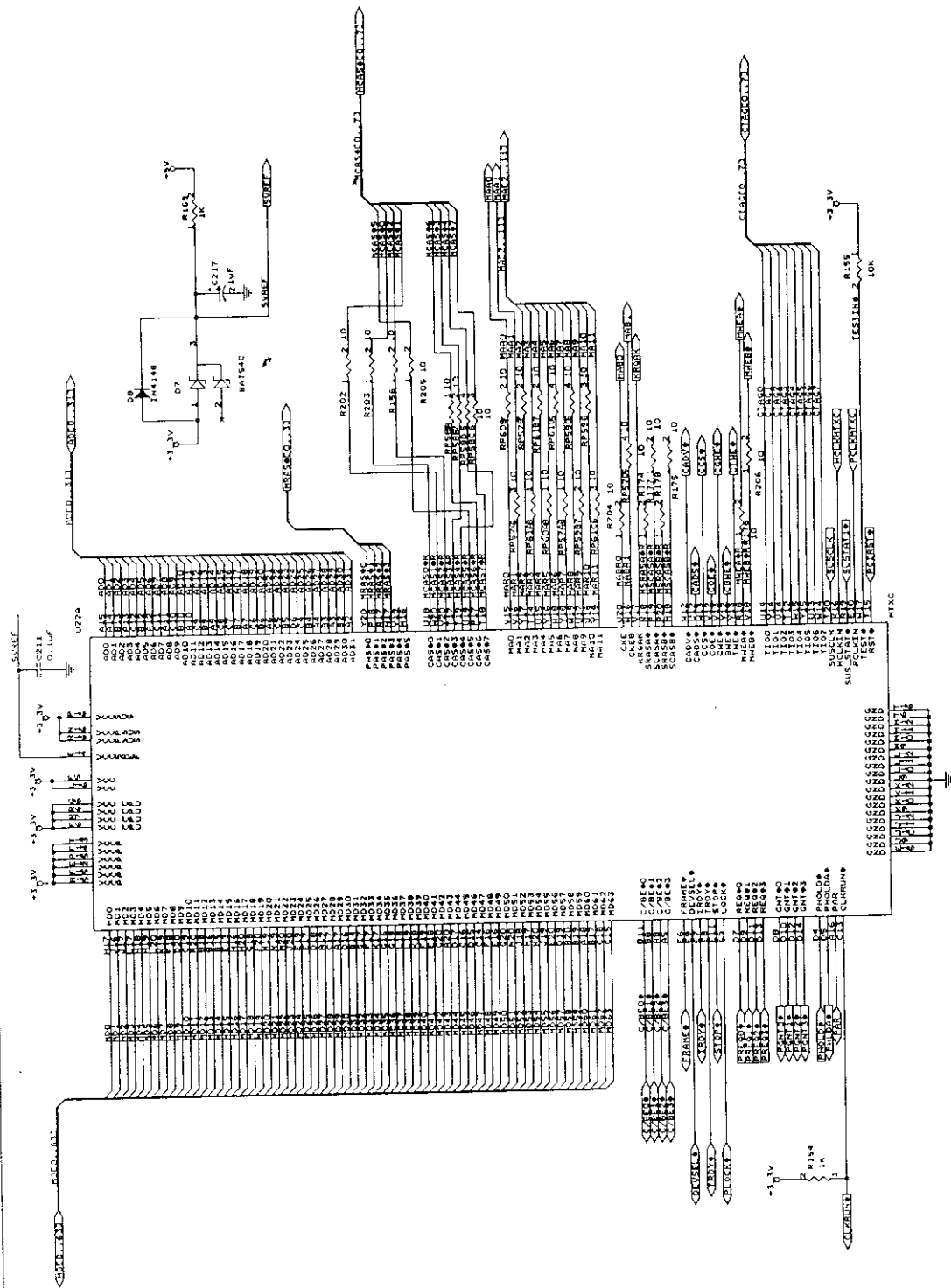
IO : SMC 37C672

ULTRA DMA 33 IDE PORT X 2

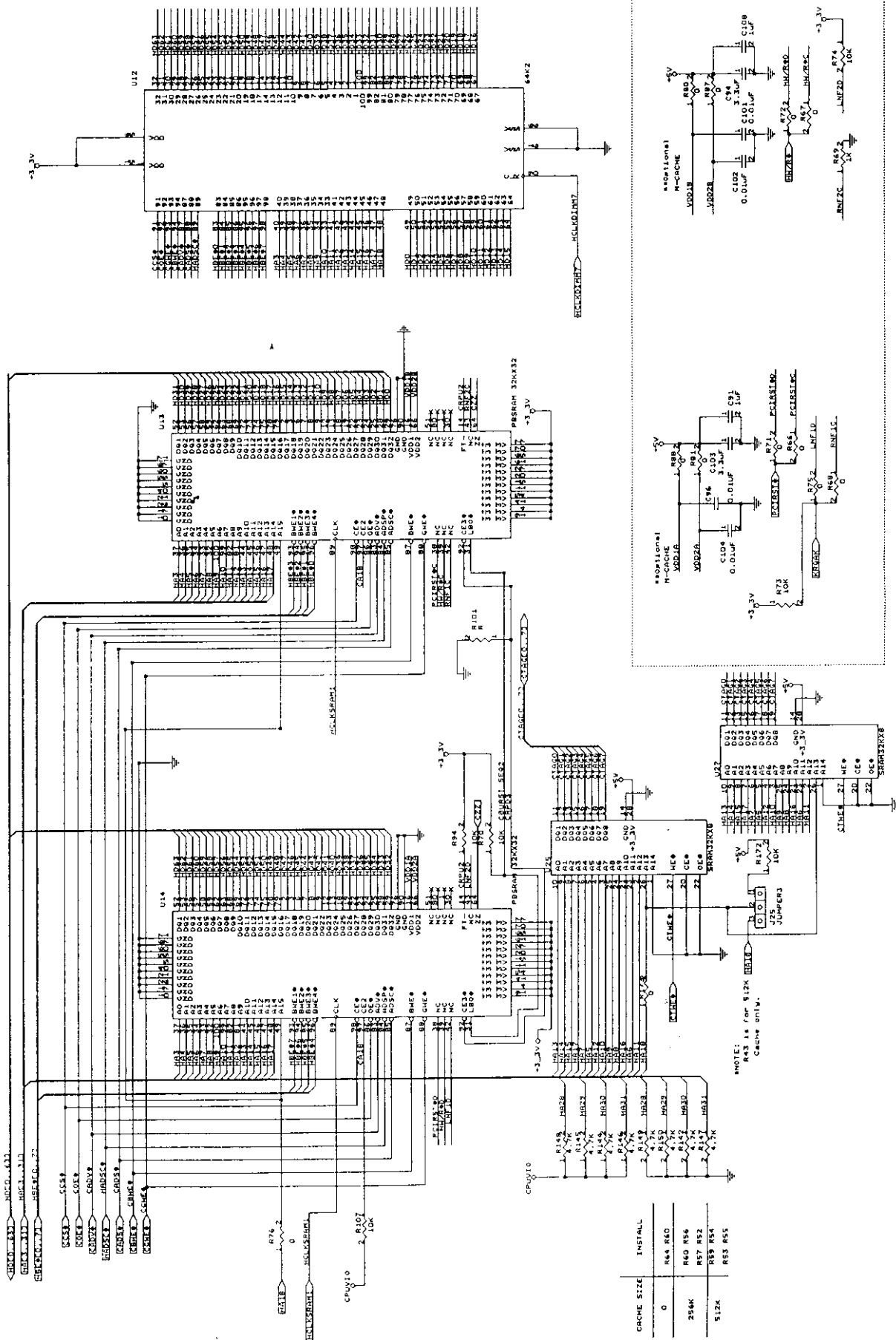
ILINK
IP54C.SCH
ISYCLK.SCH
IMTXC_A.SCH
IMTXC_B.SCH
ICACHE.SCH
ISIMM.SCH
PIIX4_A.SCH
PIIX4_B.SCH
IDE&USB.SCH
ISUPER_IO.SCH
ICOM_PORT.SCH
IBIOS.SCH
IPOWER.SCH
IVOLT_REG.SCH
IDECOUP.SCH
IPULLUP.SCH
IRTC_KBC.SCH
IT2_ISA16.SCH

FILE		INDEX	
Star Document Number		REV	
C	1	0	0
Date		Date	
June 25, 1997		June 25, 1997	



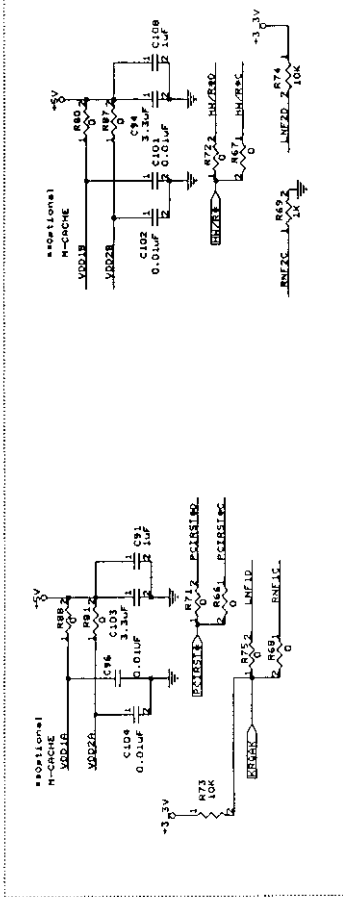


NOTE: PIN 16, 39, 42, 43 & 46 ARE NOCONNECTS FOR PM SRAM

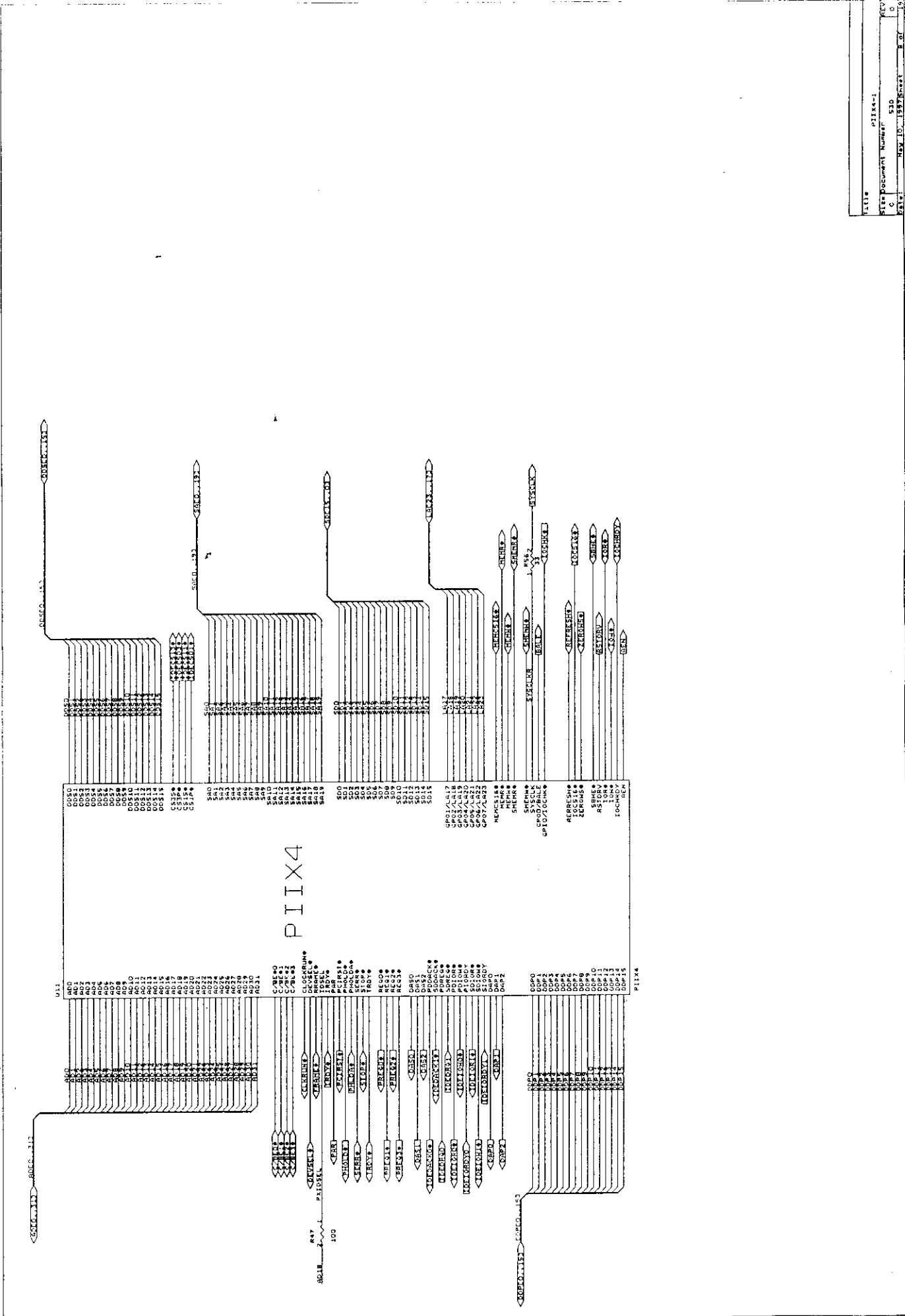


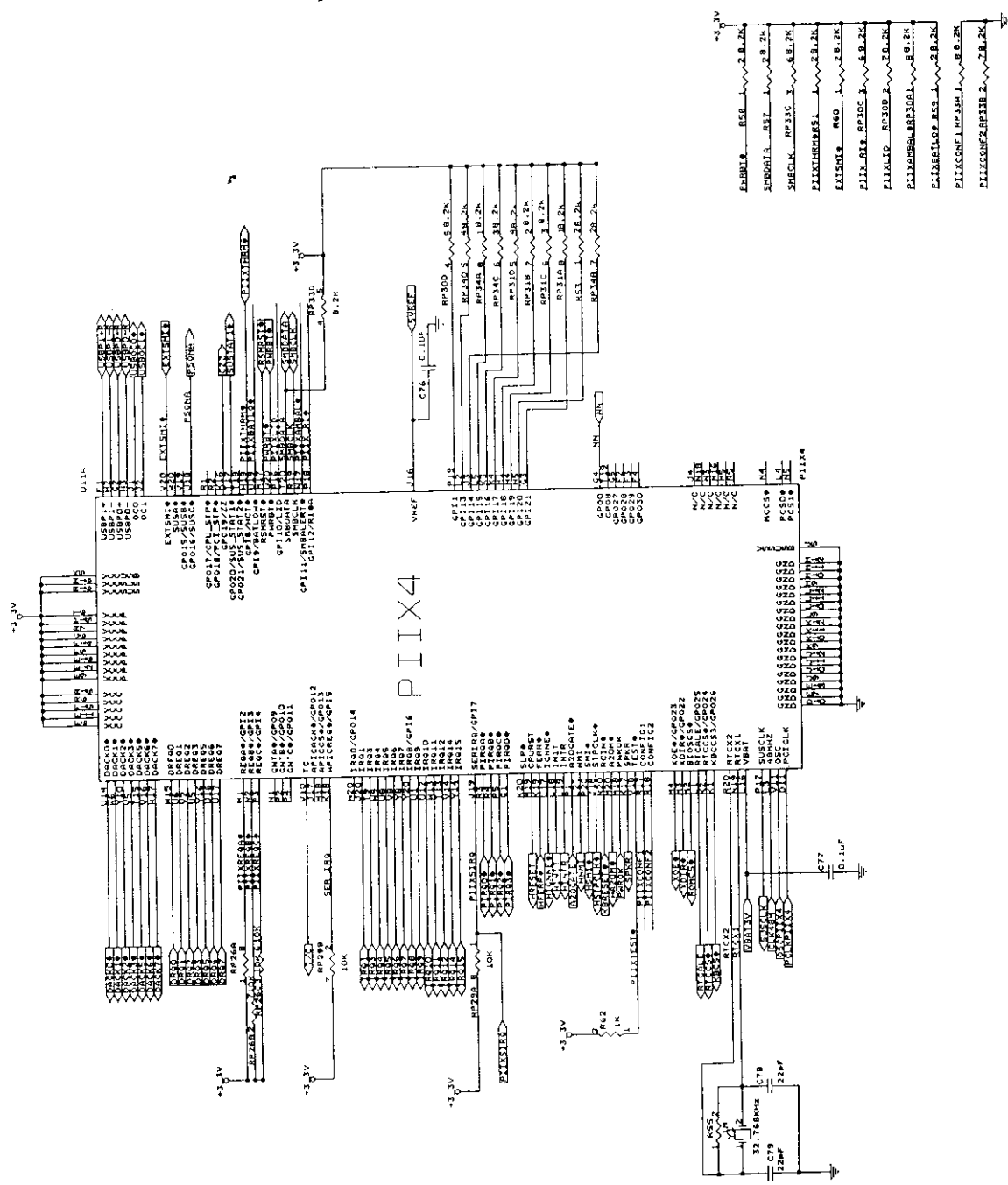
NOTE: R43 is for 512K cache only.

CACHE SIZE	INSTALL
0	R44 R40
256K	R40 R56
512K	R57 R52
	R59 R54
	R55 R55

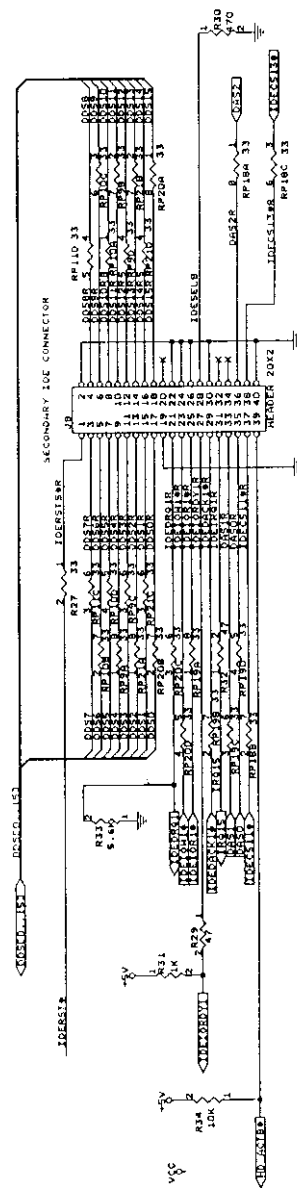
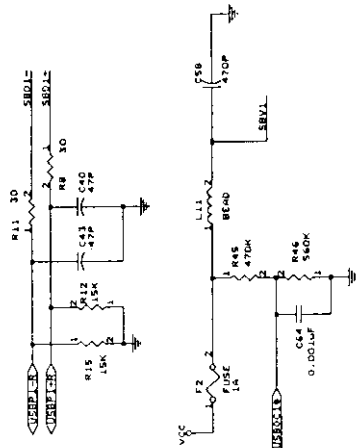


[illegible]

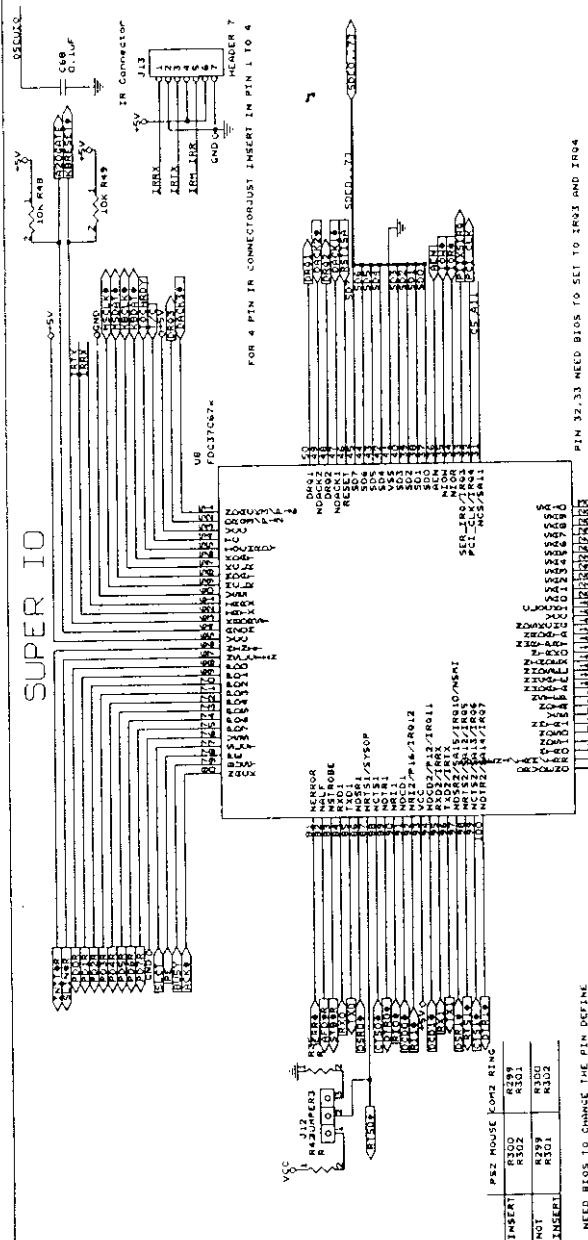




PIXA1	50	28.2K
PIXA2	50	28.2K
PIXA3	50	28.2K
PIXA4	50	28.2K
PIXA5	50	28.2K
PIXA6	50	28.2K
PIXA7	50	28.2K
PIXA8	50	28.2K
PIXA9	50	28.2K
PIX10	50	28.2K
PIX11	50	28.2K
PIX12	50	28.2K
PIX13	50	28.2K
PIX14	50	28.2K
PIX15	50	28.2K
PIX16	50	28.2K
PIX17	50	28.2K
PIX18	50	28.2K
PIX19	50	28.2K
PIX20	50	28.2K
PIX21	50	28.2K
PIX22	50	28.2K
PIX23	50	28.2K
PIX24	50	28.2K
PIX25	50	28.2K
PIX26	50	28.2K
PIX27	50	28.2K
PIX28	50	28.2K
PIX29	50	28.2K
PIX30	50	28.2K
PIX31	50	28.2K
PIX32	50	28.2K
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PIX35	50	28.2K
PIX36	50	28.2K
PIX37	50	28.2K
PIX38	50	28.2K
PIX39	50	28.2K
PIX40	50	28.2K
PIX41	50	28.2K
PIX42	50	28.2K
PIX43	50	28.2K
PIX44	50	28.2K
PIX45	50	28.2K
PIX46	50	28.2K
PIX47	50	28.2K
PIX48	50	28.2K
PIX49	50	28.2K
PIX50	50	28.2K



SUPER IO



FLOPPY CONNECTOR

