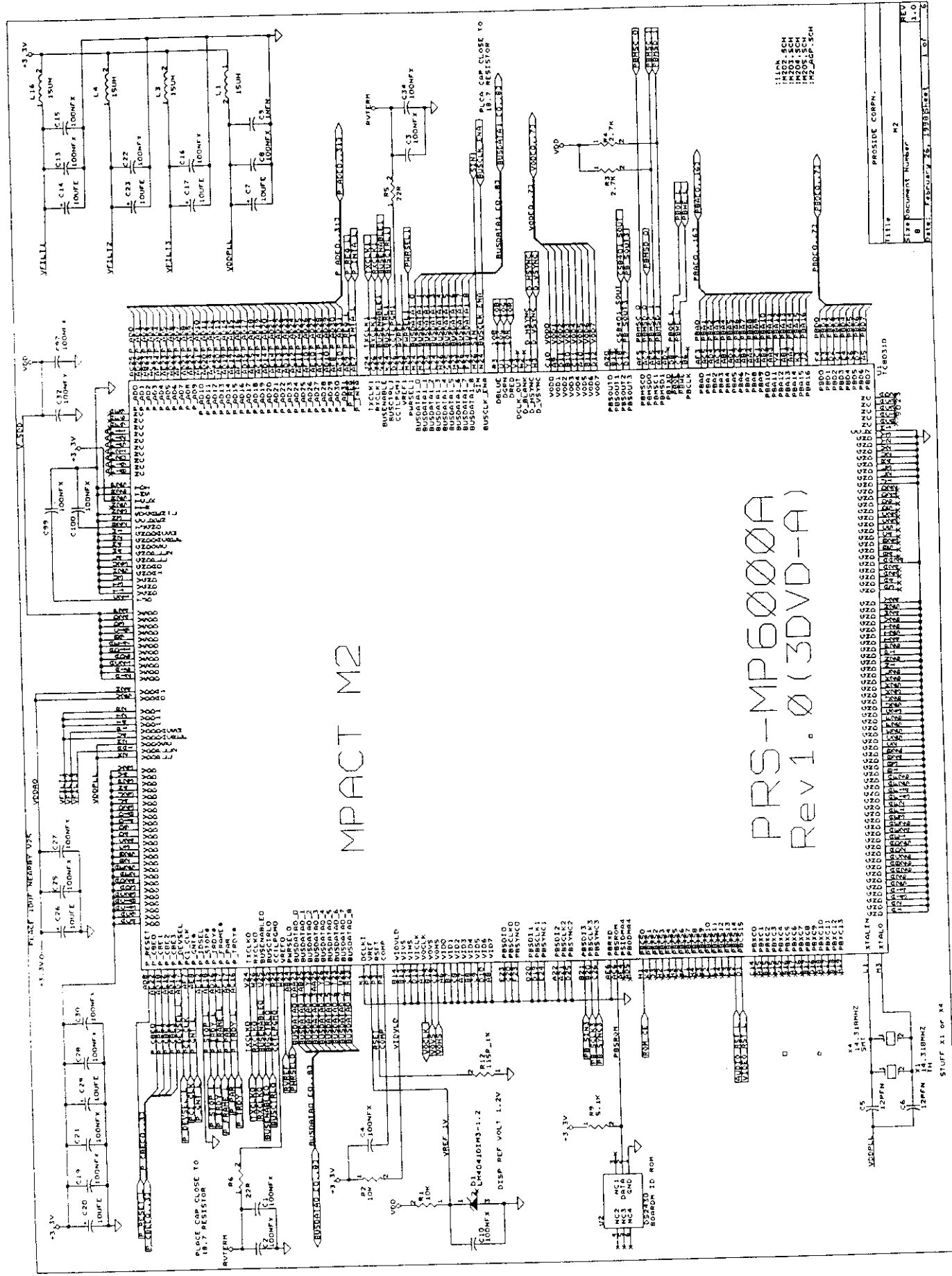


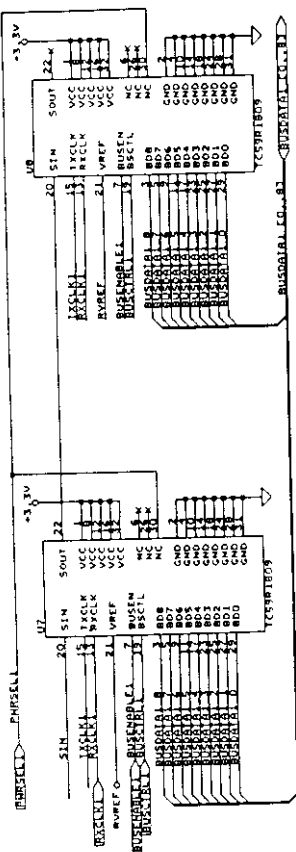
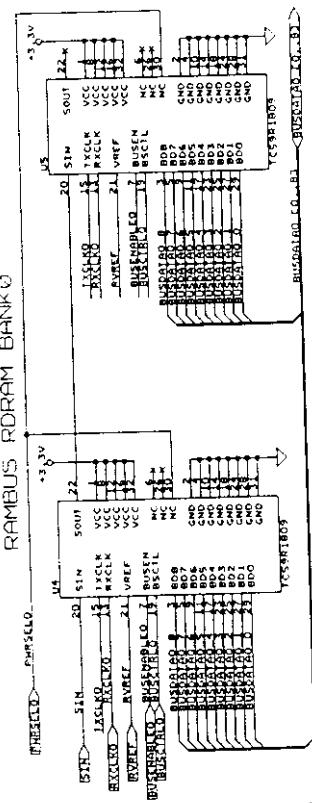


MPACT M2

PRS-MP6000A
Rev 1.0 (3DVD-A)

REV	DATE	DESCRIPTION
1.0	1998-01-15	Initial Release

RAMBUS RORAM BANK



The diagram illustrates a 100MHz bus system with four bus lines (BUS1 to BUS4) and their associated termination and bypass components.

Termination: Each bus line is terminated at both ends with a 50Ω resistor (RP1, RP2, RP3, RP4).

Bypass Components:

- Bus 1:** Bypass capacitor C35 (100nF) to ground. Bypass network C39 (100nF) and C40 (100nF) to +3.3V.
- Bus 2:** Bypass capacitor C41 (100nF) to ground. Bypass network C42 (100nF), C43 (100nF), and C44 (100nF) to +3.3V.
- Bus 3:** Bypass capacitor C45 (100nF) to ground. Bypass network C46 (100nF), C47 (100nF), and C48 (100nF) to +3.3V.
- Bus 4:** Bypass capacitor C49 (100nF) to ground. Bypass network C50 (100nF), C51 (100nF), and C52 (100nF) to +3.3V.

Component Values:

- RP1, RP2, RP3, RP4: 50Ω
- C35, C39, C40, C41, C42, C43, C44, C45, C46, C47, C48, C49, C50, C51, C52: 100nF

Labels:

- BUS1, BUS2, BUS3, BUS4
- RP1, RP2, RP3, RP4
- C35, C39, C40, C41, C42, C43, C44, C45, C46, C47, C48, C49, C50, C51, C52
- +3.3V

Notes:

- PLACE ONE SIDE OF TERMINATION IN VIANT ISLAND
- PLACE OUTSIDE OF DECOUPLING CAPS
- PLACE OUTSIDE OF DECOUPLING CAPS
- PLACE OUTSIDE OF DECOUPLING CAPS
- PLACE OUTSIDE OF DECOUPLING CAPS

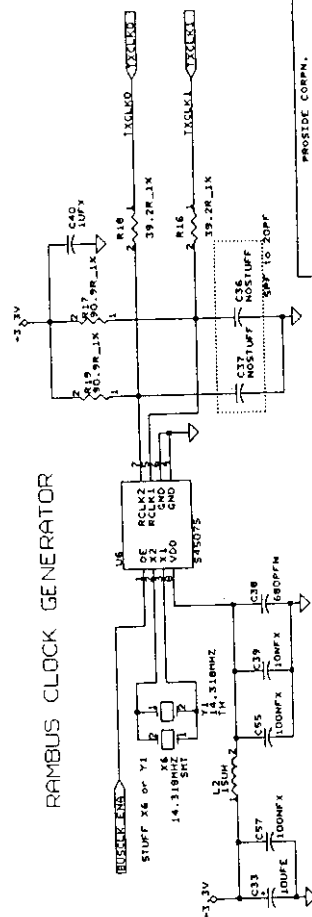
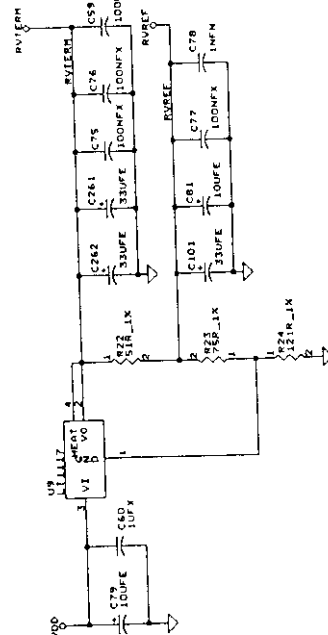
Figure 1 is a schematic diagram of a termination network. It consists of two parallel signal paths. The top path is connected to a 3.3V supply and has four capacitors in series: C65, C66, C67, and C68. The bottom path is also connected to a 3.3V supply and has four capacitors in series: C61, C62, C63, and C64. Each capacitor is labeled with its value: 100nF. The diagram is annotated with 'PLACE CLOSE TO PIN 8 & 16 OF U1'S' and 'PLACE CLOSE TO PIN 21 OF U1'S'.

TERMINATION CHANNEL

RAMBUS CHANNEL TERMINATION

RAMBUS	VTERM/VREF	GENERATION
0	0	0
1	1	1
2	2	2
3	3	3
4	4	4
5	5	5
6	6	6
7	7	7
8	8	8
9	9	9
10	10	10
11	11	11
12	12	12
13	13	13
14	14	14
15	15	15
16	16	16
17	17	17
18	18	18
19	19	19
20	20	20
21	21	21
22	22	22
23	23	23
24	24	24
25	25	25
26	26	26
27	27	27
28	28	28
29	29	29
30	30	30
31	31	31
32	32	32
33	33	33
34	34	34
35	35	35
36	36	36
37	37	37
38	38	38
39	39	39
40	40	40
41	41	41
42	42	42
43	43	43
44	44	44
45	45	45
46	46	46
47	47	47
48	48	48
49	49	49
50	50	50
51	51	51
52	52	52
53	53	53
54	54	54
55	55	55
56	56	56
57	57	57
58	58	58
59	59	59
60	60	60
61	61	61
62	62	62
63	63	63
64	64	64
65	65	65
66	66	66
67	67	67
68	68	68
69	69	69
70	70	70
71	71	71
72	72	72
73	73	73
74	74	74
75	75	75
76	76	76
77	77	77
78	78	78
79	79	79
80	80	80
81	81	81
82	82	82
83	83	83
84	84	84
85	85	85
86	86	86
87	87	87
88	88	88
89	89	89
90	90	90
91	91	91
92	92	92
93	93	93
94	94	94
95	95	95
96	96	96
97	97	97
98	98	98
99	99	99

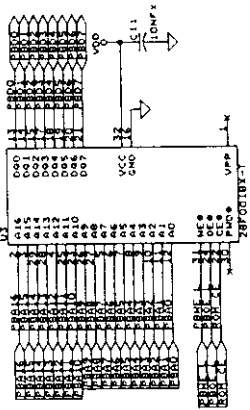
RAMB15 CLOCK GENERATOR



RAMBUS CLOCK MUST BE MATCHED
FOR TIME AND LENGTH, FRONT SIDE
TRACE ONLY. CONTROLLED IMPEDANCE

PROSIDE CORPN.	
Title	
HZ MPACT	
Drawing Number	
8	REV
1.0	2 of 6

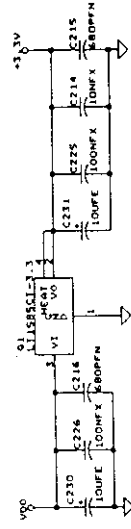
NO STUFF FOR MASS PRODUCTION
3.3V IS SUPPLIED BY AGP SLOT



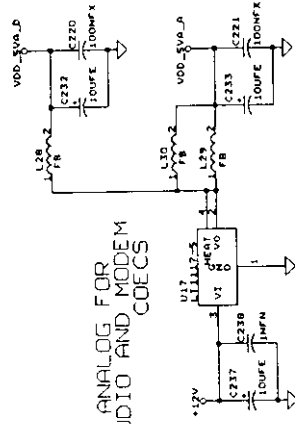
Use heatsink MIZUDEN 'SP414K' on Q1

L1585C1
 L1585C2
 L1585C3
 L1585C4
 L1585C5
 L1585C6
 L1585C7
 L1585C8
 L1585C9
 L1585C10
 L1585C11
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 L1585C88
 L1585C89
 L1585C90
 L1585C91
 L1585C92
 L1585C93
 L1585C94
 L1585C95
 L1585C96
 L1585C97
 L1585C98
 L1585C99
 L1585C100

+3.3V GENERATION



5V ANALOG FOR AUDIO AND MODEM CODECS



ALTERNATES: R76 = 200K 1X
R77 = 100K 1X
C7 = 100MFX

