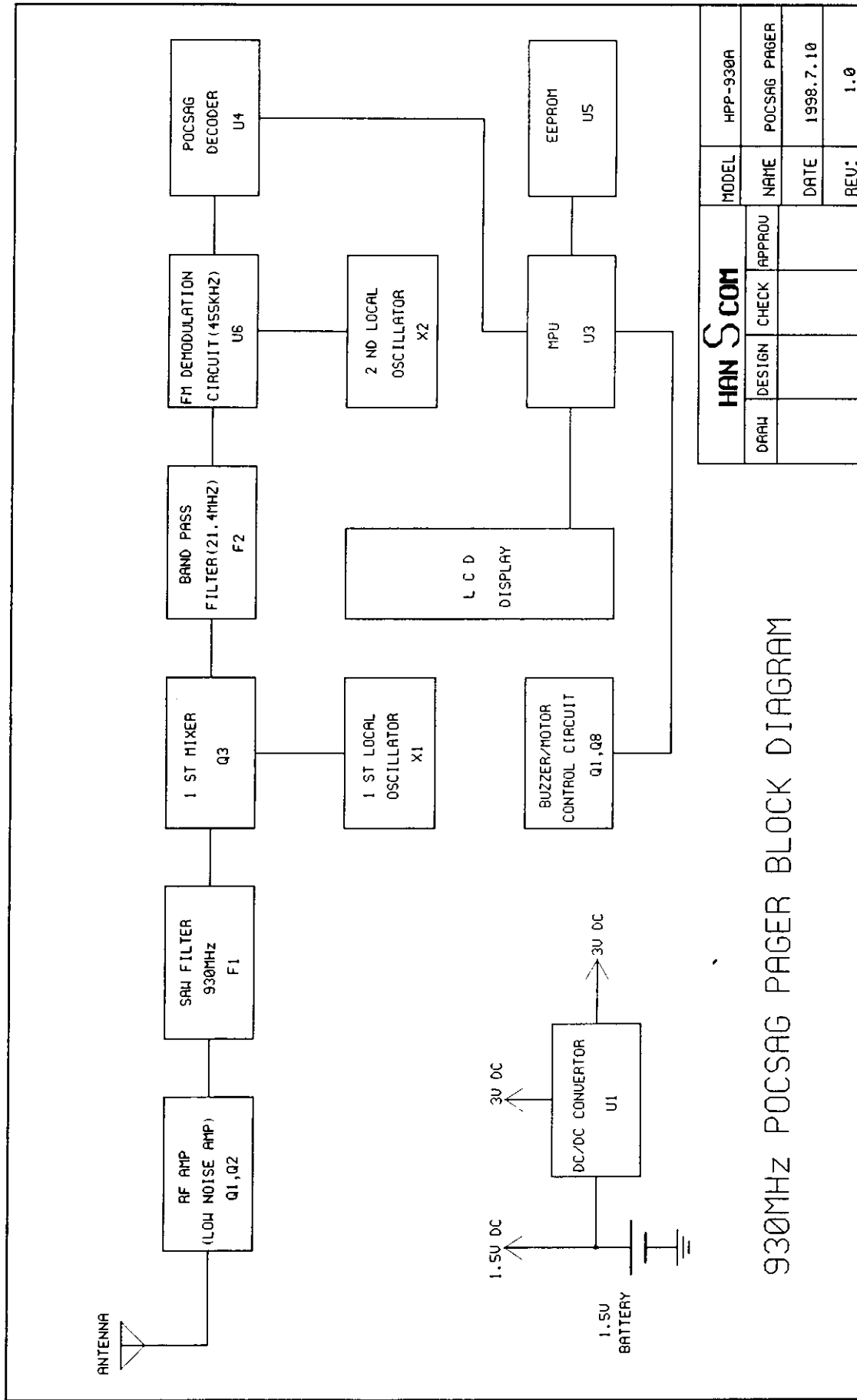

Appendix A –EUT BLOCK DIAGRAM



HAN Scom				MODEL	HPP-930A
DRAW	DESIGN	CHECK	APPROV	NAME	POCSAG PAGER
				DATE	1998.7.10
				REV:	1.0

930MHZ POCSAG PAGER BLOCK DIAGRAM