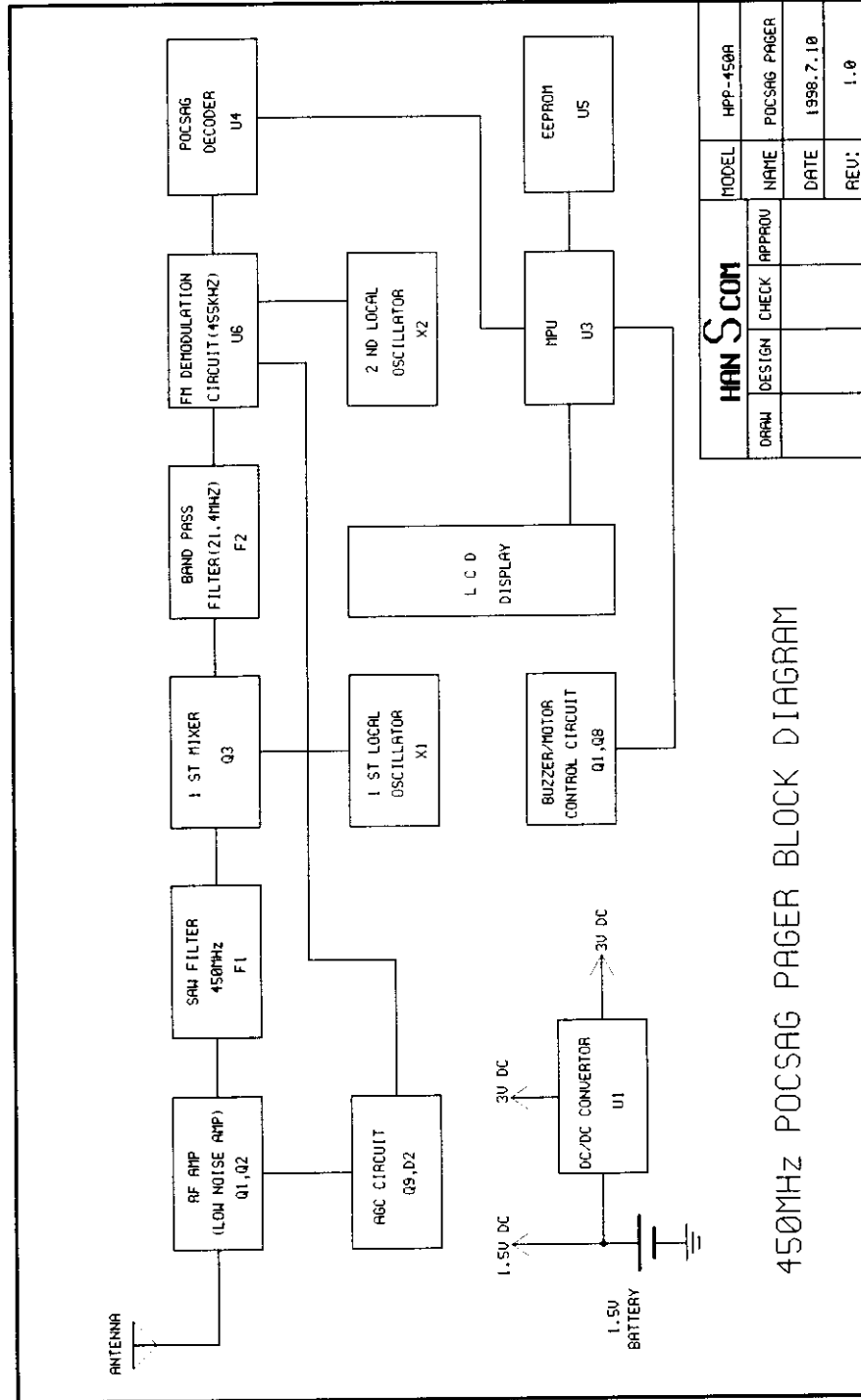

Appendix A –EUT BLOCK DIAGRAM



450MHz POCsag PAGER BLOCK DIAGRAM