

DATA SHEET

3216 Ceramic Chip Antenna for Bluetooth/WLAN Application

R&D	Print date 05/09/06									
	3216 Ceramic Chip Antenna for Bluetooth/WLAN Application					CAN4311712002453K			2005.4.4	
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Preliminary Product Specification

Quick Reference Data

Centre Frequency	2.45 GHz* ¹
Bandwidth	at least 100 MHz* ²
VSWR	2.5 (Max.)* ²
Polarization	Linear
Azimuth Beamwidth	Omni-directional
Peak Gain	3.1 dBi* ²
Impedance	50Ω
Operating Temperature	-25~85 °C
Termination	Ni / Sn (Environmentally-Friendly Leadless)
Resistance to soldering heats	260°C , 10sec.
Maximum Power	1W

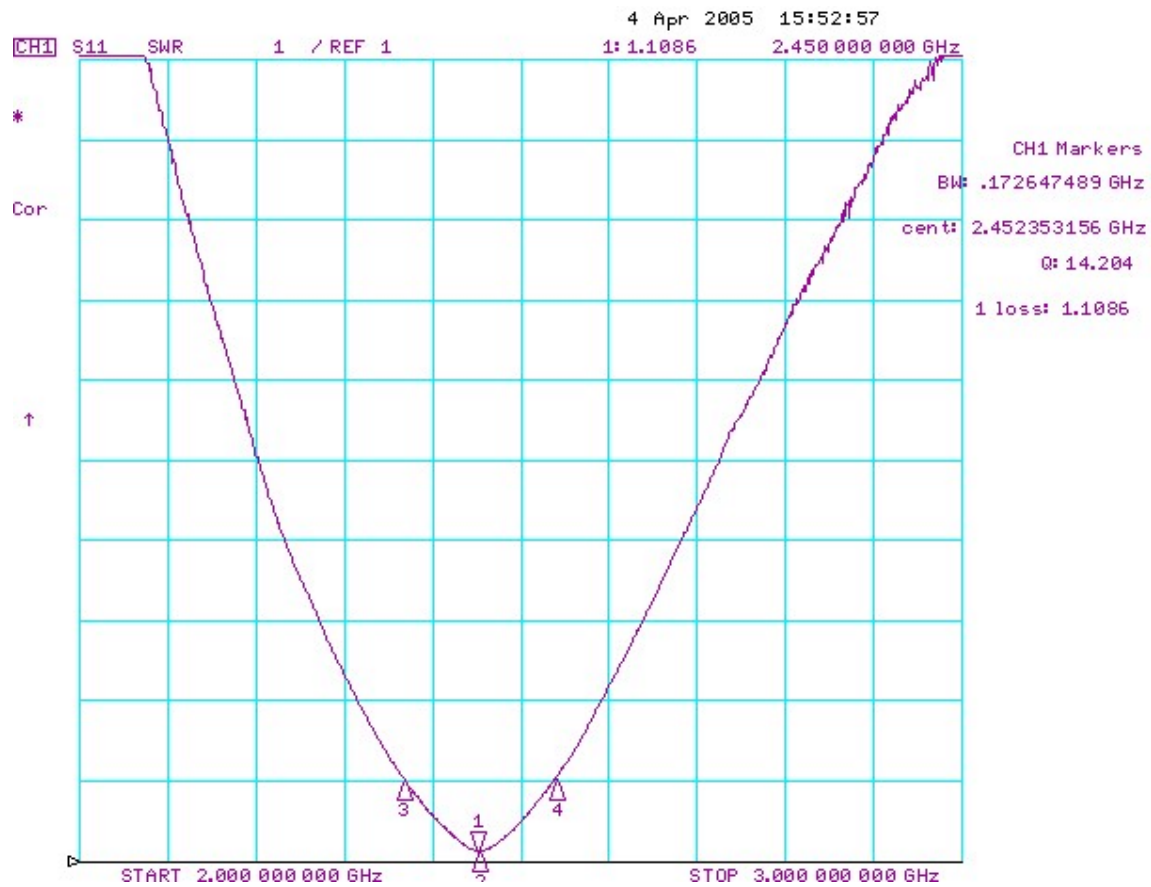
¹ All the technical data and information contained herein are subject to change without prior notice

² Testing under evaluation board of page2

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3. Soldering Pads Dimension

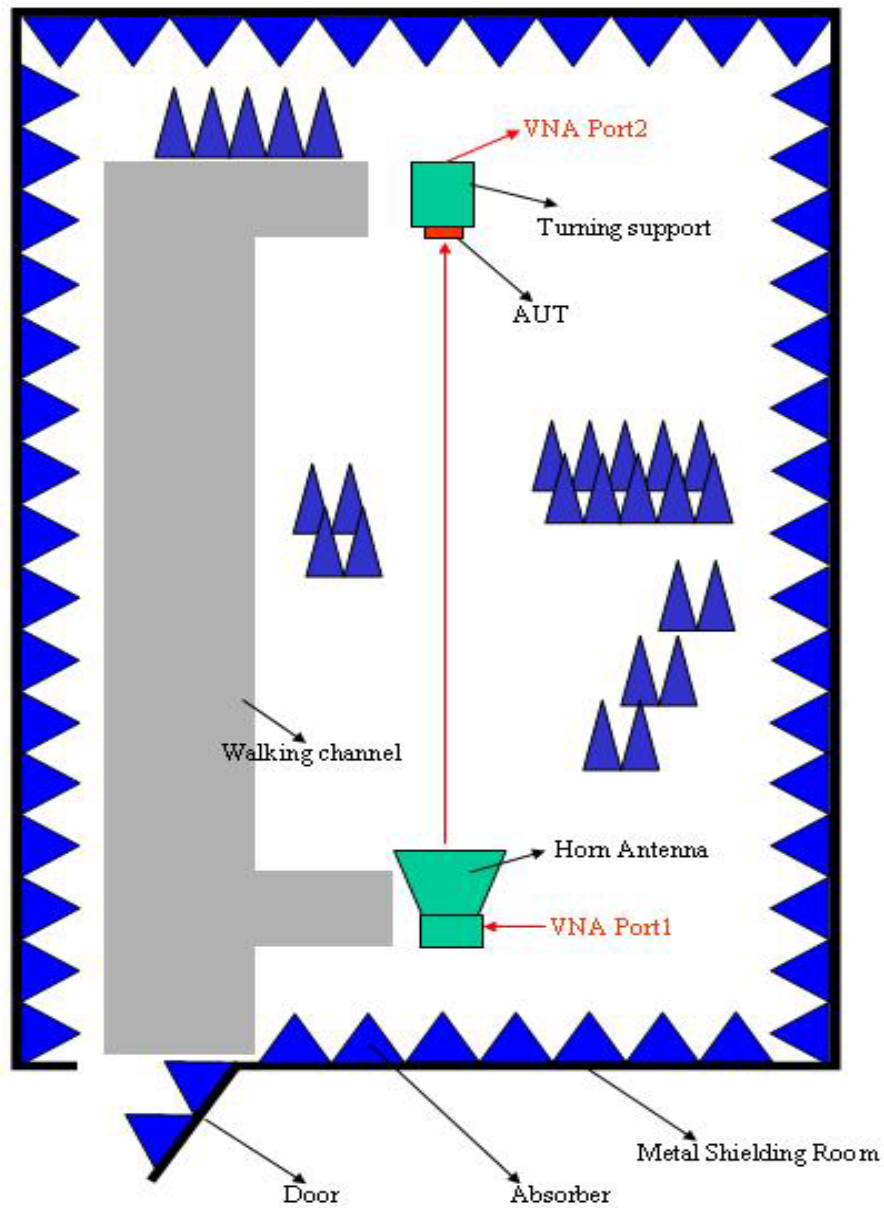
5. VSWR



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7. The Environment of Antenna Radiation Pattern

Anechoic Chamber Dimension=8(m) × 4(m) × 4(m)



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IEC 384-10/ CECC 32 100 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS
4.4		Mounting	The antenna can be mounted on printed-circuit boards or ceramic substrates by applying wave soldering, reflow soldering (including vapour phase soldering) or conductive adhesive	No visible damage
4.5		Visual inspection and dimension check	Any applicable method using $\times 10$ magnification	In accordance with specification (chip off 4mm)
4.6.1		Antenna	Central Frequency at 20 °C	Standard test board in page 4
4.8		Adhesion	A force of 3 N applied for 10 s to the line joining the terminations and in a plane parallel to the substrate	No visible damage
4.9		Bond strength of plating on end face	Mounted in accordance with CECC 32 100, paragraph 4.4	No visible damage
			Conditions: bending 0.5 mm at a rate of 1mm/s, radius jig. 340 mm, 2mm warp on FR4 board of 90 mm length	No visible damage
4.10	20(Tb)	Resistance to soldering heat	260 ± 5 °C for 10 ± 0.5 s in a static solder bath	The terminations shall be well tinned after recovery and Central Freq. Change $\pm 6\%$
		Resistance to leaching	260 ± 5 °C for 30 ± 1 s in a static solder bath	Using visual enlargement of $\times 10$, dissolution of the termination shall not exceed 10%

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IEC 384-10/ CECC 32 100 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS
4.11	20(Ta)	Solderability	Zero hour test, and test after storage (20 to 24 months) in original atmosphere; un-mounted chips completely immersed for 2 ± 0.5 s in $235 \pm 5^\circ\text{C}$.	The termination must be well tinned, at least 75% is well tinned at termination
4.12	4(Na)	Rapid change of temperature	-25°C (30 minutes) to $+85^\circ\text{C}$ (30 minutes); 100 cycles	No visible damage Central Freq. Change $\pm 6\%$
4.14	3(Ca)	Damp heat	500 ± 12 hours at 60°C ; 90 to 95 % RH	No visible damage 2 hours recovery Central Freq. Change $\pm 6\%$
4.15		Endurance	500 ± 12 hours at 85°C ;	No visible damage 2 hours recovery Central Freq. Change $\pm 6\%$

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Taping Blister Tape

